

# **USER MANUAL**

## **SERIES R I/O**

### **WITH PROTOCOL**

### **MODBUS TCP-IP and MODBUS RTU**



**SENECA S.r.l.**  
**Via Austria 26 – 35127 – Z.I. - PADOVA (PD) - ITALY**  
**Tel. +39.049.8705355 – 8705355 Fax +39 049.8706287**  
**[www.seneca.it](http://www.seneca.it)**



**ORIGINAL INSTRUCTIONS**

### **CAUTION**

SENECA does not guarantee that all specifications and/or aspects of the product and firmware, included in them, will meet the requirements of the actual final application even if the product referred to in this documentation is in compliance with the technological state of the art.

The user assumes full responsibility and/or risk with regard to the configuration of the product to achieve the intended results in relation to the specific installation and/or end application.

SENECA may, with prior agreement, provide consultancy services for the successful completion of the final application, but under no circumstances can it be held responsible for its proper functioning.

The SENECA product is an advanced product, the operation of which is specified in the technical documentation supplied with the product itself and/or can be downloaded, if desired prior to purchase, from the [www.seneca.it](http://www.seneca.it) website.

SENECA has a policy of continuous development and accordingly reserves the right to make and/or introduce - without prior notice - changes and/or improvements to any product described in this documentation.

The product described in this documentation may solely and exclusively be used by personnel qualified for the specific activity and in accordance with the relevant technical documentation, with particular attention being paid to the safety instructions.

Qualified personnel means personnel who, on the basis of their training, competence and experience, are able to identify risks and avoid potential hazards that could occur during the use of this product.

SENECA products may only be used for the applications and in the manner described in the technical documentation relating to the products themselves.

To ensure proper operation and prevent the occurrence of malfunctions, the transport, storage, installation, assembly, maintenance of SENECA products must comply with the safety instructions and environmental conditions specified in this documentation.

SENECA's liability in relation to its products is governed by the general conditions of sale, which can be downloaded from [www.seneca.it](http://www.seneca.it).

Neither SENECA nor its employees, within the limits of applicable law, will in any case be liable for any lost profits and/or sales, loss of data and/or information, higher costs incurred for goods and/or replacement services, damage to property and/or persons, interruption of activities and/or provision of services, any direct, indirect, incidental, pecuniary and non-pecuniary, consequential damages in any way caused and/or caused, due to negligence, carelessness, incompetence and/or other liabilities arising from the installation, use and/or inability to use the product.

### **CONTACT US**

|                     |                                                                  |
|---------------------|------------------------------------------------------------------|
| Technical support   | <a href="mailto:supporto@seneca.it">supporto@seneca.it</a>       |
| Product information | <a href="mailto:commerciale@seneca.it">commerciale@seneca.it</a> |

This document is the property of SENECA srl.  
Copies and reproduction are prohibited unless authorised.

## **Document revisions**

| DATE       | REVISION | NOTES                                                                                                  | AUTHOR |
|------------|----------|--------------------------------------------------------------------------------------------------------|--------|
| 10/02/2023 | 0        | First revision<br>R-32DIDO-1, R-32DIDO-2, R-16DI-8DO, R-8AI-8DIDO                                      | MM     |
| 02/03/2023 | 1        | Added Chapter "Protection of digital outputs"                                                          | MM     |
| 15/03/2023 | 2        | Fix Seneca Discovery Device, Easy Setup 2, Seneca Studio Seneca Studio<br>Fix cross references         | MM     |
| 15/03/2023 | 3        | Tables translated in English language                                                                  | MM     |
| 08/05/2023 | 5        | Added info about RW register<br>Fix registers info in English language                                 | MM     |
| 29/05/2023 | 6        | Added R-SG3 device, modified chapter "Factory configuration reset"                                     | MM     |
| 31/05/2023 | 7        | Added DIP SWITCH chapter                                                                               | MM     |
| 19/07/2023 | 8        | Fixed ModBUS registers 40044, 40079 and 40080 of R-SG3                                                 | AZ     |
| 13/11/2023 | 9        | Changed old R-8AI-8DIDO with new R-8AI-8DIDO version<br>Deleted -1 R-series HW code<br>Minor fix       | MM     |
| 27/11/2023 | 10       | Fix R-8AI-8DIDO Modbus table                                                                           | MM     |
| 04/12/2023 | 11       | Updated registers 40201 to 40269 of R-16DI-8DO                                                         | AZ     |
| 14/02/2024 | 12       | Added support for R-8AI-8DIDO-2 release fw >= 1012                                                     | MM     |
| 09/04/2024 | 13       | Added R-32DIDO configuration registers                                                                 | MM     |
| 27/05/2024 | 14       | Fix Counters Filter R-16DI-8DO                                                                         | MM     |
| 08/07/2024 | 15       | Added R-4AO-8DIDO device                                                                               | MM     |
| 10/12/2024 | 16       | Added R-4AO-8DIDO fault value info                                                                     | MM     |
| 16/01/2025 | 17       | Fixed R-4AO-8DIDO Modbus registers                                                                     | MM     |
| 22/01/2025 | 18       | Fixed R-4AO-8DIDO MSW/LSW Modbus registers<br>Added R-4AO-8DIDO info about dip switch and IN/OUT speed | MM     |
| 03/03/2025 | 19       | Added R-4RTD-8DIDO device<br>Various fix                                                               | MM     |
| 10/10/2025 | 20       | Aggiunto FLEX                                                                                          | AC     |
| 17/10/2025 | 21       | Added R-2AI-6DIDO device                                                                               | MM     |

## TABLE OF CONTENTS

|                                                                       |           |
|-----------------------------------------------------------------------|-----------|
| <b>1. INTRODUCTION.....</b>                                           | <b>9</b>  |
| <b>2. FLEX TECHNOLOGY FOR PROTOCOL CHANGE.....</b>                    | <b>9</b>  |
| <b>3. R SERIES DEVICES.....</b>                                       | <b>10</b> |
| 3.1. R-32DIDO .....                                                   | 10        |
| 3.1.1. PROTECTION OF DIGITAL OUTPUTS .....                            | 10        |
| 3.1.2. DIGITAL COUNTERS ACQUISITION TIME .....                        | 10        |
| 3.2. R-16DI-8DO .....                                                 | 10        |
| 3.3. R-8AI-8DIDO.....                                                 | 11        |
| 3.3.1. ANALOG INPUT UPDATE TIME.....                                  | 11        |
| 3.3.2. UPDATE TIME OF DIGITAL INPUTS/OUTPUTS .....                    | 11        |
| 3.3.3. PROTECTION OF DIGITAL OUTPUTS.....                             | 11        |
| 3.4. R-SG3 .....                                                      | 12        |
| 3.4.1. LOAD CELL CONNECTION.....                                      | 12        |
| 3.4.2. 4- OR 6-WIRE LOAD CELL CONNECTION .....                        | 12        |
| 3.4.3. CHECKING THE LOAD CELL OPERATION .....                         | 12        |
| 3.4.3.1. CHECKING CABLES WITH A DIGITAL MULTIMETER .....              | 12        |
| 3.4.4. CONNECTION OF MORE LOAD CELLS IN PARALLEL.....                 | 13        |
| 3.4.5. TRIMMING 4-WIRE LOAD CELLS.....                                | 15        |
| 3.5. R-4AO-8DIDO.....                                                 | 15        |
| 3.5.1. ANALOG OUTPUTS RESPONSE TIME.....                              | 15        |
| 3.5.2. UPDATE TIME OF DIGITAL INPUTS/OUTPUTS .....                    | 16        |
| 3.5.3. DIGITAL COUNTERS ACQUISITION TIME .....                        | 16        |
| 3.5.4. PROTECTION OF DIGITAL OUTPUTS.....                             | 16        |
| 3.6. R-4RTD-8DIDO.....                                                | 16        |
| 3.6.1. ANALOG INPUT UPDATE TIME.....                                  | 16        |
| 3.6.2. UPDATE TIME OF DIGITAL INPUTS/OUTPUTS .....                    | 17        |
| 3.6.3. DIGITAL COUNTERS ACQUISITION TIME .....                        | 17        |
| 3.6.4. PROTECTION OF DIGITAL OUTPUTS.....                             | 17        |
| 3.7. R-2AI-6DIDO.....                                                 | 17        |
| 3.7.1. ANALOG INPUT UPDATE TIME.....                                  | 17        |
| 3.7.2. UPDATE TIME OF DIGITAL INPUTS/OUTPUTS .....                    | 17        |
| 3.7.3. DIGITAL COUNTERS ACQUISITION TIME .....                        | 18        |
| 3.7.4. PROTECTION OF DIGITAL OUTPUTS.....                             | 18        |
| <b>4. DIP SWITCH .....</b>                                            | <b>19</b> |
| 4.1. MEANING OF THE DIP SWITCHES SW1 FOR THE R-8AI-8DIDO MODEL.....   | 19        |
| 4.2. MEANING OF SW1 DIP-SWITCHES FOR THE R-32DIDO MODEL.....          | 20        |
| 4.2.1. DIP SWITCH SW1 FOR FIRMWARE REVISION <= 1014 .....             | 20        |
| 4.2.2. DIP SWITCH SW1 FOR FIRMWARE REVISION >= 1015 .....             | 20        |
| 4.3. MEANING OF THE SW1 DIP SWITCHES FOR THE R-SG3 MODEL.....         | 21        |
| 4.4. MEANING OF THE DIP SWITCHES SW1 FOR THE R-8AI-8DIDO MODEL.....   | 21        |
| 4.5. MEANING OF THE DIP SWITCHES SW1 FOR THE R-4AO-8DIDO MODEL .....  | 22        |
| 4.6. MEANING OF THE DIP SWITCHES SW1 FOR THE R-4RTD-8DIDO MODEL ..... | 22        |

|           |                                                                           |    |
|-----------|---------------------------------------------------------------------------|----|
| 4.7.      | MEANING OF THE DIP SWITCHES SW1 FOR THE R-2AI-6DIDO MODEL .....           | 22 |
| 5.        | I/O COPY USING THE PEER TO PEER FUNCTION WITHOUT A MASTER CONTROLLER..... | 23 |
| 6.        | MODBUS PASSTHROUGH .....                                                  | 24 |
| 7.        | RESETTING THE DEVICE TO FACTORY CONFIGURATION .....                       | 25 |
| 7.1.      | PROCEDURE FOR RESTORING DEVICES TO THE FACTORY CONFIGURATION.....         | 25 |
| 8.        | CONNECTION OF THE DEVICE TO A NETWORK.....                                | 25 |
| 9.        | WEB SERVER .....                                                          | 26 |
| 9.1.      | ACCESS TO THE WEB SERVER.....                                             | 26 |
| 10.       | CONFIGURATION OF THE R-32DIDO DEVICE VIA WEB SERVER .....                 | 27 |
| 10.1.     | SETUP SECTION .....                                                       | 27 |
| 10.2.     | DIGITAL I/O SETUP SECTION .....                                           | 29 |
| 10.3.     | SETUP COUNTERS SECTION .....                                              | 30 |
| 10.4.     | P2P CONFIGURATION .....                                                   | 30 |
| 11.       | CONFIGURATION OF THE R-16DI-8DO DEVICE VIA WEB SERVER.....                | 31 |
| 11.1.     | SETUP SECTION .....                                                       | 31 |
| 11.2.     | SETUP 2 SECTION .....                                                     | 34 |
| 12.       | CONFIGURATION OF THE R-8AI-8DIDO DEVICE VIA WEB SERVER .....              | 36 |
| 12.1.     | SETUP SECTION .....                                                       | 36 |
| 12.2.     | SETUP AIN 1. 8 SECTION .....                                              | 38 |
| 12.3.     | DIGITAL I/O SETUP SECTION .....                                           | 40 |
| 12.4.     | EVENT SETUP SECTION .....                                                 | 41 |
| 13.       | CONFIGURATION OF THE R- SG3 DEVICE VIA WEB SERVER.....                    | 42 |
| 13.1.     | SETUP SECTION .....                                                       | 42 |
| 13.2.     | LOAD CELL SETUP SECTION.....                                              | 43 |
| 13.3.     | I/O SETUP SECTION .....                                                   | 46 |
| 13.4.     | TEST AND LOAD CELL CALIBRATION SECTION.....                               | 49 |
| 13.5.     | P2P CONFIGURATION .....                                                   | 49 |
| 13.6.     | LOAD CELL CALIBRATION THROUGH THE WEB SERVER .....                        | 49 |
| 13.6.1.   | CELL CALIBRATION WITH FACTORY PARAMETERS .....                            | 50 |
| 13.6.1.1. | MANUAL ENTRY OF THE TARE VIA WEB SERVER.....                              | 50 |
| 13.6.1.2. | ACQUISITION OF THE TARE FROM THE FIELD VIA WEB SERVER.....                | 50 |
| 13.6.2.   | CELL CALIBRATION WITH A STANDARD WEIGHT .....                             | 51 |
| 14.       | CONFIGURATION OF THE R-4AO-8DIDO DEVICE VIA WEB SERVER .....              | 52 |
| 14.1.     | SETUP SECTION .....                                                       | 52 |

|            |                                                                                    |           |
|------------|------------------------------------------------------------------------------------|-----------|
| 14.2.      | ANALOG OUTPUT SETUP SECTION .....                                                  | 54        |
| 14.3.      | DIGITAL I/O SETUP SECTION .....                                                    | 55        |
| 14.4.      | SETUP COUNTERS SECTION .....                                                       | 56        |
| 14.5.      | P2P CONFIGURATION .....                                                            | 56        |
| 14.6.      | ANALOG TEST SECTION .....                                                          | 56        |
| 14.7.      | DIGITAL TEST SECTION.....                                                          | 56        |
| 14.8.      | COUNTERS TEST SECTION .....                                                        | 56        |
| <b>15.</b> | <b>CONFIGURATION OF THE R-4RTD-8DIDO DEVICE VIA WEB SERVER .....</b>               | <b>56</b> |
| 15.1.      | SETUP SECTION .....                                                                | 56        |
| 15.2.      | ANALOG INPUT SETUP SECTION .....                                                   | 58        |
| 15.3.      | ANALOG TEST SECTION .....                                                          | 60        |
| 15.4.      | DIGITAL I/O SETUP SECTION .....                                                    | 60        |
| 15.5.      | SETUP COUNTERS SECTION .....                                                       | 61        |
| 15.6.      | DIGITAL IO TEST SECTION .....                                                      | 61        |
| 15.7.      | COUNTERS TEST SECTION .....                                                        | 61        |
| 15.8.      | P2P CONFIGURATION .....                                                            | 61        |
| <b>16.</b> | <b>CONFIGURATION OF THE R-2AI-6DIDO DEVICE VIA WEB SERVER .....</b>                | <b>62</b> |
| 16.1.      | SETUP SECTION .....                                                                | 62        |
| 16.2.      | SETUP AIN 1. 8 SECTION .....                                                       | 64        |
| 16.3.      | DIGITAL I/O SETUP SECTION .....                                                    | 65        |
| 16.4.      | SETUP COUNTERS SECTION .....                                                       | 65        |
| 16.5.      | DIGITAL IO TEST SECTION .....                                                      | 66        |
| 16.6.      | COUNTERS TEST SECTION .....                                                        | 66        |
| 16.7.      | P2P CONFIGURATION .....                                                            | 66        |
| <b>17.</b> | <b>P2P CLIENT .....</b>                                                            | <b>67</b> |
| <b>18.</b> | <b>P2P SERVER.....</b>                                                             | <b>69</b> |
| 18.1.      | P2P CONFIGURATION EXAMPLE.....                                                     | 70        |
| 18.2.      | P2P EXECUTION TIME .....                                                           | 71        |
| <b>19.</b> | <b>UPDATING THE FIRMWARE AND SAVING/OPENING A CONFIGURATION .....</b>              | <b>71</b> |
| <b>20.</b> | <b>MODBUS RTU/ MODBUS TCP-IP REGISTERS.....</b>                                    | <b>72</b> |
| 20.1.      | NUMBERING OF "0-BASED" OR "1-BASED" MODBUS ADDRESSES .....                         | 73        |
| 20.2.      | NUMBERING OF MODBUS ADDRESSES WITH "0-BASED" CONVENTION .....                      | 73        |
| 20.3.      | NUMBERING OF MODBUS ADDRESSES WITH "1 BASED" CONVENTION (STANDARD) .....           | 73        |
| 20.4.      | BIT CONVENTION WITHIN A MODBUS HOLDING REGISTER .....                              | 74        |
| 20.5.      | MSB and LSB BYTE CONVENTION WITHIN A MODBUS HOLDING REGISTER.....                  | 74        |
| 20.6.      | REPRESENTATION OF A 32-BIT VALUE IN TWO CONSECUTIVE MODBUS HOLDING REGISTERS ..... | 75        |
| 20.7.      | TYPE OF 32-BIT FLOATING POINT DATA (IEEE 754) .....                                | 76        |
| 20.8.      | SUPPORTED MODBUS COMMUNICATION PROTOCOLS.....                                      | 77        |
| 20.9.      | SUPPORTED MODBUS FUNCTION CODES.....                                               | 77        |
| <b>21.</b> | <b>MODBUS REGISTER TABLE FOR THE R-32DIDO PRODUCT .....</b>                        | <b>78</b> |

|            |                                                                                                 |            |
|------------|-------------------------------------------------------------------------------------------------|------------|
| 21.1.      | R-32DIDO: MODBUS 4X HOLDING REGISTERS TABLE (FUNCTION CODE 3) .....                             | 78         |
| 21.2.      | R-32DIDO: MODBUS CONFIGURATION REGISTERS TABLE 4x HOLDING REGISTERS (FUNCTION CODE 3) .....     | 85         |
| 21.3.      | R-32DIDO: TABLE OF MODBUS REGISTERS 0x COIL STATUS (FUNCTION CODE 1) .....                      | 105        |
| 21.4.      | R-32DIDO: TABLE OF MODBUS REGISTERS 1x INPUT STATUS (FUNCTION CODE 2) .....                     | 106        |
| <b>22.</b> | <b>MODBUS REGISTER TABLE FOR THE R-16DI-8DO PRODUCT .....</b>                                   | <b>107</b> |
| 22.1.      | R-16DI-8DO: MODBUS 4X HOLDING REGISTERS TABLE (FUNCTION CODE 3) .....                           | 107        |
| 22.2.      | R-16DI-8DO: CONSECUTIVE REGISTERS MODBUS 4x COPY (WITH INTEGER MEASURE REGISTERS) .....         | 131        |
| 22.3.      | R-16DI-8DO: TABLE OF MODBUS REGISTERS 0x COIL STATUS (FUNCTION CODE 1) .....                    | 141        |
| 22.4.      | R-16DI-8DO: TABLE OF REGISTERS 1x INPUT STATUS (FUNCTION CODE 2) .....                          | 143        |
| <b>23.</b> | <b>MODBUS REGISTER TABLE FOR THE R-8AI-8DIDO DEVICE .....</b>                                   | <b>144</b> |
| 23.1.      | R-8AI-8DIDO: MODBUS 4X HOLDING REGISTERS TABLE (FUNCTION CODE 3) .....                          | 144        |
| 23.2.      | R-8AI-8DIDO: TABLE OF MODBUS REGISTERS 0x COIL STATUS (FUNCTION CODE 1) .....                   | 152        |
| 23.3.      | R-8AI-8DIDO: TABLE OF MODBUS REGISTERS 1x INPUT STATUS (FUNCTION CODE 2) .....                  | 153        |
| <b>24.</b> | <b>MODBUS REGISTER TABLE FOR THE R-SG3 DEVICE .....</b>                                         | <b>154</b> |
| 24.1.      | R-SG3: MODBUS 4X HOLDING REGISTERS TABLE (FUNCTION CODE 3) .....                                | 154        |
| <b>25.</b> | <b>MODBUS REGISTER TABLE FOR THE R-4AO-8DIDO PRODUCT .....</b>                                  | <b>171</b> |
| 25.1.      | R-4AO-8DIDO: MODBUS 4X HOLDING REGISTERS TABLE (FUNCTION CODE 3) .....                          | 171        |
| 25.2.      | R-4AO-8DIDO: TABLE OF MODBUS REGISTERS 0x COIL STATUS (FUNCTION CODE 1) .....                   | 175        |
| 25.3.      | R-4AO-8DIDO: TABLE OF MODBUS REGISTERS 1x INPUT STATUS (FUNCTION CODE 2) .....                  | 175        |
| <b>26.</b> | <b>MODBUS REGISTER TABLE FOR THE R-4RTD-8DIDO PRODUCT .....</b>                                 | <b>176</b> |
| 26.1.      | R-4RTD-8DIDO: MODBUS 4X HOLDING REGISTERS TABLE (FUNCTION CODE 3) .....                         | 176        |
| 26.2.      | R-4RTD-8DIDO: TABLE OF MODBUS REGISTERS 0x COIL STATUS (FUNCTION CODE 1) .....                  | 179        |
| 26.3.      | R-4RTD-8DIDO: TABLE OF MODBUS REGISTERS 1x INPUT STATUS (FUNCTION CODE 2) .....                 | 180        |
| <b>27.</b> | <b>R-SG3: LOAD CELL CALIBRATION THROUGH MODBUS REGISTERS .....</b>                              | <b>180</b> |
| 27.1.      | CELL CALIBRATION PROCEDURE WITH FACTORY PARAMETERS AND WITH TARE ACQUIRED FROM THE FIELD<br>180 |            |
| 27.2.      | CELL CALIBRATION PROCEDURE WITH FACTORY PARAMETERS AND MANUALLY ENTERED TARE .....              | 181        |
| 27.3.      | PROCEDURE FOR CALIBRATION OF THE CELL WITH A STANDARD WEIGHT .....                              | 181        |
| <b>28.</b> | <b>MODBUS REGISTER TABLE FOR THE R-2AI-6DIDO PRODUCT .....</b>                                  | <b>181</b> |
| 28.1.      | R-2AI-6DIDO: MODBUS 4X HOLDING REGISTERS TABLE (FUNCTION CODE 3) .....                          | 181        |
| 28.2.      | R-2AI-6DIDO: TABLE OF MODBUS REGISTERS 0x COIL STATUS (FUNCTION CODE 1) .....                   | 183        |
| 28.3.      | R-2AI-6DIDO: TABLE OF MODBUS REGISTERS 1x INPUT STATUS (FUNCTION CODE 2) .....                  | 184        |
| <b>29.</b> | <b>CABLE HARNESS FOR MODELS WITH DOUBLE ETHERNET PORT .....</b>                                 | <b>184</b> |
| 29.1.      | CHAIN ETHERNET CONNECTION (DAISY CHAIN) .....                                                   | 184        |
| 29.2.      | LAN FAULT-BYPASS FUNCTION .....                                                                 | 186        |

---

|                                                                                                     |            |
|-----------------------------------------------------------------------------------------------------|------------|
| <b>30. SEARCH AND MODIFICATION OF THE DEVICE IP WITH THE SENECA<br/>DISCOVERY DEVICE TOOL .....</b> | <b>187</b> |
|-----------------------------------------------------------------------------------------------------|------------|



## 1. INTRODUCTION



### ATTENTION!

This user manual extends the information from the installation manual to the configuration of the device. Use the installation manual for more information.



### ATTENTION!

In any case, SENECA s.r.l. or its suppliers will not be responsible for the loss of data/revenue or consequential or incidental damages due to negligence or bad/improper management of the device, even if SENECA is well aware of these possible damages.

SENECA, its subsidiaries, affiliates, group companies, suppliers and distributors do not guarantee that the functions fully meet the customer's expectations or that the device, firmware and software should have no errors or operate continuously.

## 2. FLEX TECHNOLOGY FOR PROTOCOL CHANGE



Flex allows you to change the combination of industrial communication protocols supported by the devices at will from a list of available ones, the development is continuously updated, for a complete list refer to the page: <https://www.seneca.it/flex/>

Some examples of supported protocols are:



The device then becomes “universal” and compatible with Siemens or Rockwell or Schneider systems etc... without the need to purchase different hardware.

### 3. R SERIES DEVICES

The R Series I/O modules are devices designed for flexible cabling needs, reduced installation spaces, high I/O density applications with ModBUS communication (serial and Ethernet). Configuration can be done via dedicated software and/or DIP switches.

The devices equipped with two Ethernet ports can be connected in daisy chain mode (without the use of an external switch) and support fault-bypass mode to ensure the Ethernet connection even in the event of failure of a module in the chain.

For more information on these protocols, see the website:

<http://www.modbus.org/specs.php>.

#### 3.1. R-32DIDO

The devices allow the use of 32 digital channels that can be individually configured for input or output. When a digital channel is configured as an input, a 32-bit counter is also associated with a value saved in non-volatile memory.

| <b>CODE</b> | <b>ETHERNET PORT</b>                 |
|-------------|--------------------------------------|
| R-32DIDO-2  | 2 PORTS 10/100 Mbit<br>(Switch mode) |

##### 3.1.1. PROTECTION OF DIGITAL OUTPUTS

The outputs are protected against overload and against overtemperature, they open cyclically until the fault is repaired or the output opens.

The limit current is between 0.6 and 1.2 A.

##### 3.1.2. DIGITAL COUNTERS ACQUISITION TIME

The faster digital counters acquisition time is 1ms

#### 3.2. R-16DI-8DO

The devices allow the use of 16 digital input channels and 8 digital relay output channels.

| <b>CODE</b> | <b>ETHERNET PORT</b> |
|-------------|----------------------|
|-------------|----------------------|

|           |                                      |
|-----------|--------------------------------------|
| R-16DI8DO | 2 PORTS 10/100 Mbit<br>(Switch mode) |
|-----------|--------------------------------------|

### 3.3. **R-8AI-8DIDO**

The devices allow the use of 8 analog input channels and 8 digital channels that can be individually configured for input or output.

| <b>CODE</b>   | <b>ETHERNET PORT</b>                 |
|---------------|--------------------------------------|
| R-8AI-8DIDO-2 | 2 PORTS 10/100 Mbit<br>(Switch mode) |

#### 3.3.1. **ANALOG INPUT UPDATE TIME**

Sampling time can be configured from 25ms to 400ms per each channel, in particular:

| <b>CHANNEL SAMPLING TIME</b> |
|------------------------------|
| 25ms                         |
| 50ms                         |
| 100ms                        |
| 200ms                        |
| 400ms                        |

To calculate the update time of a channel, consider the following example:

By activating 8 channels and setting a sampling time of 25 ms, you get an input update every:  $25 \times 8 = 200$  ms.

**Note (only if thermocouple channels are enabled):**

In the case of a thermocouple input, the Burnout check is carried out every 10 seconds.

The duration of this check takes 25ms on each enabled thermocouple channel.

For example, with 3 active thermocouples, every 10 seconds the following are used:

$25\text{ms} \times 3 \text{ channels} = 75 \text{ ms}$  for Burnout evaluation.

#### 3.3.2. **UPDATE TIME OF DIGITAL INPUTS/OUTPUTS**

The update time of the 8 digital inputs/outputs is 25ms.

#### 3.3.3. **PROTECTION OF DIGITAL OUTPUTS**

The outputs are protected against overload and against overtemperature, they open cyclically until the fault is repaired or the output opens.

The limit current is between 0.6 and 1.2 A.

### 3.4. **R-SG3**

R-SG3 is a load cell converter (strain gauge). The measurement, carried out with the 4 or 6-wire technique, is available via the server TCP-IP Modbus or via RTU slave Modbus protocols. The device is equipped with a new noise filter specifically developed to obtain a rapid response time. The device is also fully configurable via the webserver.

| <b>CODE</b> | <b>ETHERNET PORT</b> |
|-------------|----------------------|
| R-SG3       | 1 PORT 10/100 Mbit   |

#### 3.4.1. **LOAD CELL CONNECTION**

It is possible to connect the converter to the load cell in 4- or 6-wire mode. 6-wire measurement is preferable for measurement accuracy.

The load cell power supply is provided directly by the device.

#### 3.4.2. **4- OR 6-WIRE LOAD CELL CONNECTION**

A load cell can have a four-wire or six-wire cable. In addition to having the +/- excitation and +/- signal lines a six-wire cable also has the +/- sense lines. It is a common misconception to think that the only difference between 4- or 6-wire load cells is the possibility of the latter to measure the actual voltage at the load cell. A load cell is compensated to work within specifications in a certain temperature range (usually -10 - +40°C). Since the cable resistance depends on the temperature, the response of the cable to temperature changes must be eliminated. The 4-wire cable is part of the load cell temperature compensation system. The 4-wire load cell is calibrated and compensated with a certain amount of cable connected. For this reason, never cut the cable of a 4-wire load cell. The cable of a 6-wire cell, on the other hand, is not part of the load cell temperature compensation system. The sense lines are connected to the R-SG3 sense terminals, to measure and adjust the actual voltage of the load cell. The advantage of using this "active" system is the possibility of cutting (or extending) the 6-wire load cell cable to any length. It must be considered that a 6-wire load cell will not reach the performance declared in the specifications if the sense lines are not used.

#### 3.4.3. **CHECKING THE LOAD CELL OPERATION**

Before starting the configuration of the device it is necessary to verify the correctness of the wiring and the integrity of the load cell.

##### **3.4.3.1. CHECKING CABLES WITH A DIGITAL MULTIMETER**

First you need to check with the load cell manual that there are about 5V DC between the +Excitation and – Excitation cables. If the cell has 6 wires check that the same voltage is also measured between +Sense and – Sense.

Now leave the cell at rest (without the tare) and check that the voltage between the +Signal and –Signal cables is around 0 V.

Now unbalance the cell by applying a compression force, checking that the voltage between the +Signal and – Signal cables increases until it reaches the full scale (if possible) where the measurement will be approximately:

$5 * (\text{cell sensitivity}) \text{ mV}$ .

For example, if the declared cell sensitivity is 2 mV/V,  $5 * 2 = 10 \text{ mV}$  must be obtained.

In the case of bipolar measurement only (compression/traction) it is necessary to completely unbalance the cell even in traction, in this case the same value must be measured between the +Signal and –Signal cables but with the negative sign:

$-5 * (\text{cell sensitivity}) \text{ mV}$ .

#### 3.4.4. CONNECTION OF MORE LOAD CELLS IN PARALLEL

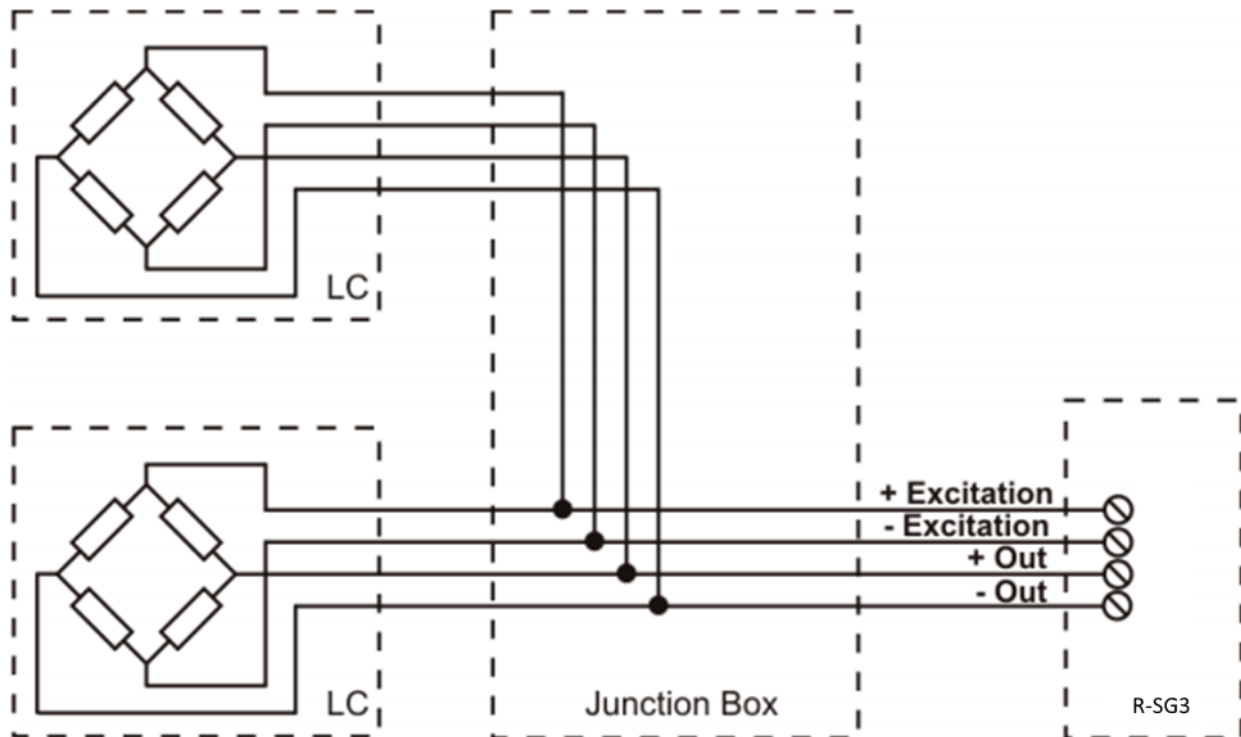
It is possible to connect up to a maximum of 8 load cells (and in any case without ever falling below the minimum 87 Ohms).

It is therefore possible to connect:

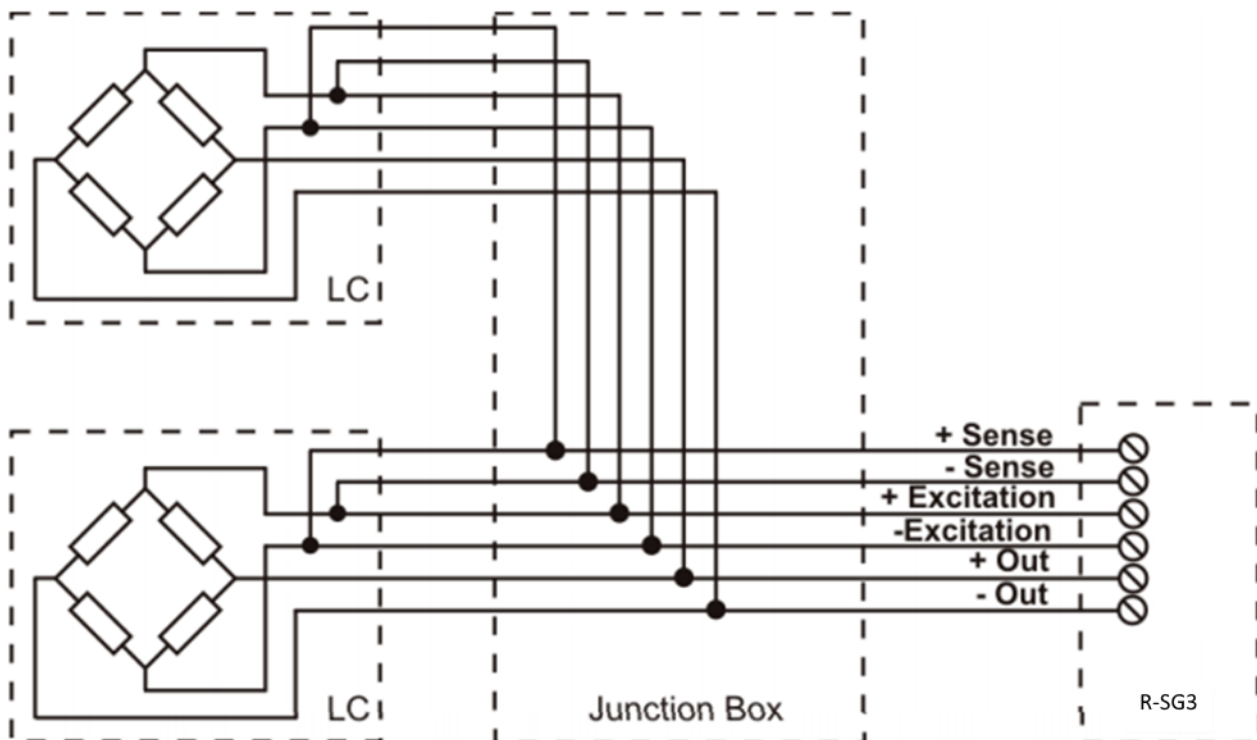
| <b>NUMBER OF LOAD CELLS IN PARALLEL</b>                |                                                        |
|--------------------------------------------------------|--------------------------------------------------------|
| <b>IMPEDANCE OF THE<br/>STATED LOAD CELL<br/>[Ohm]</b> | <b>MAXIMUM NUMBER OF CONNECTABLE CELLS IN PARALLEL</b> |
| 350                                                    | 4                                                      |
| 1000                                                   | 8                                                      |

For the connection of 4 load cells Seneca recommends using the SG-EQ4 product.

To connect 2 or more 4-wire cells in parallel with the SG-EQ4 junction box, use the following diagram:



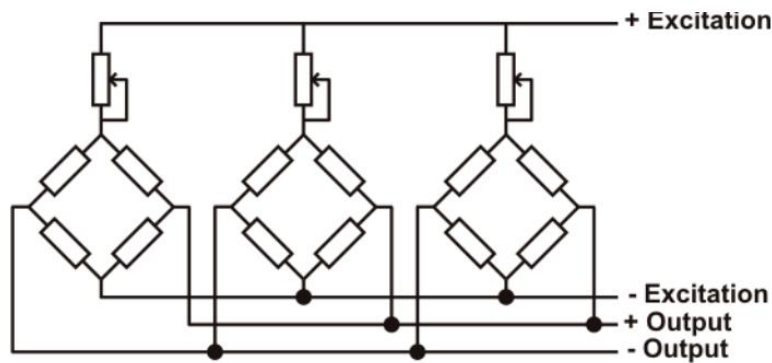
To connect 2 or more 6-wire cells in parallel with the SG-EQ4 junction box use the following diagram:



For more details, refer to the SG-EQ4 Junction Box accessory manual.

### 3.4.5. TRIMMING 4-WIRE LOAD CELLS

The figure below shows a diagram of three trimmed load cells.



A variable resistor, independent of the temperature, or a typically 20  $\Omega$  potentiometer is inserted in the +Excitation cable of each load cell. There are two ways to trim the load cells. The first method is to adjust the potentiometers by trial, shifting the calibration weights from one corner to another. All the potentiometers must be adjusted so as to set the maximum sensitivity for each cell, turning them all completely clockwise. Then, once the angle with the lowest output is located, act on the trimmers of the other cells until obtaining the same minimum output value. This method can be very long, especially for large scales where the use of test weights on the corners is not very practical. In these cases the second, more suitable method is to "pre-trim" the potentiometers using a precision voltmeter (at least 4 1/2 digits). You can use the following procedure:

- 1) Determine the exact mV/V ratio of each load cell, shown in the calibration certificate of the cell itself.
- 2) Determine the exact excitation voltage provided by the indicator/meter (for example Z-SG), measuring this voltage with the voltmeter (for example 10.05 V).
- 3) Multiply the lowest mV/V value found (point 1) by the excitation voltage (point 2).
- 4) Divide the trimming factor calculated in point 3 by the mV/V value of the other load cells.
- 5) Measure and adjust the excitation voltage of the other three load cells using the respective potentiometer. Check the results and make a final adjustment by moving a test load from corner to corner.

### 3.5. R-4AO-8DIDO

The devices allow the use of 4 analog output analog channels (that can be individually configured in Voltage or Current) and 8 digital channels that can be individually configured for input or output.

| <b>CODE</b> | <b>ETHERNET PORT</b>                 |
|-------------|--------------------------------------|
| R-4AO-8DIDO | 2 PORTS 10/100 Mbit<br>(Switch mode) |

#### 3.5.1. ANALOG OUTPUTS RESPONSE TIME

The analog output response time to go from 10% to 90% is 5ms.

### 3.5.2. UPDATE TIME OF DIGITAL INPUTS/OUTPUTS

The update time of the 8 digital inputs/outputs is 1ms.

### 3.5.3. DIGITAL COUNTERS ACQUISITION TIME

The faster digital counters acquisition time is 1ms

### 3.5.4. PROTECTION OF DIGITAL OUTPUTS

The outputs are protected against overload and against overtemperature, they open cyclically until the fault is repaired or the output opens.

The limit current is between 0.6 and 1.2 A.

## 3.6. R-4RTD-8DIDO

The devices allow the use of 4 thermoresistance measurement channels and 8 digital channels that can be individually configured for input or output.

| <b>CODE</b>  | <b>ETHERNET PORT</b>                 |
|--------------|--------------------------------------|
| R-4RTD-8DIDO | 2 PORTS 10/100 Mbit<br>(Switch mode) |

### 3.6.1. ANALOG INPUT UPDATE TIME

Sampling time is configurable from 25ms to 400ms, in particular:

| <b>CHANNEL UPDATE TIME</b> |
|----------------------------|
| 25ms                       |
| 50ms                       |
| 100ms                      |
| 200ms                      |
| 400ms                      |

Since each channel has an independent analog/digital converter, this set time coincides with the refresh time of each channel.

Therefore, by activating all 4 channels and setting a sampling time of 25 ms, you get an input update every 25 ms.



### 3.6.2. UPDATE TIME OF DIGITAL INPUTS/OUTPUTS

The update time of the 8 digital inputs/outputs is 25ms.

### 3.6.3. DIGITAL COUNTERS ACQUISITION TIME

The faster digital counters acquisition time is 25ms

### 3.6.4. PROTECTION OF DIGITAL OUTPUTS

The outputs are protected against overload and against overtemperature, they open cyclically until the fault is repaired or the output opens.

The limit current is between 0.6 and 1.2 A.

## 3.7. R-2AI-6DIDO

The devices allow the use of 2 analog input channels and 6 digital channels that can be individually configured for input or output.

| <b>CODE</b> | <b>ETHERNET PORT</b> |
|-------------|----------------------|
| R-2AI-6DIDO | 1 PORTS 10/100 Mbit  |

### 3.7.1. ANALOG INPUT UPDATE TIME

Sampling time can be configured from 25ms to 400ms per each channel, in particular:

| <b>CHANNEL SAMPLING TIME</b> |
|------------------------------|
| 25ms                         |
| 50ms                         |
| 100ms                        |
| 200ms                        |
| 400ms                        |

To calculate the update time of a channel, consider the following example:

By activating 2 channels and setting a sampling time of 25 ms, you get an input update every:  $25 \times 2 = 50$  ms.

### 3.7.2. UPDATE TIME OF DIGITAL INPUTS/OUTPUTS

The update time of the 6 digital inputs/outputs is 10ms.

### 3.7.3. **DIGITAL COUNTERS ACQUISITION TIME**

The faster digital counters acquisition time is 10ms

### 3.7.4. **PROTECTION OF DIGITAL OUTPUTS**

The outputs are protected against overload and against overtemperature, they open cyclically until the fault is repaired or the output opens.

The limit current is between 0.6 and 1.2 A.

## 4. DIP SWITCH

### **ATTENTION!**

THE DIP SWITCH SETTINGS ARE READ ONLY AT THE START. AT EACH CHANGE, IT IS NECESSARY TO RESTART.

### **ATTENTION!**

DEPENDING ON THE MODEL IT MAY BE NECESSARY TO REMOVE THE REAR COVER OF THE DEVICE TO ACCESS THE DIP SWITCHES

#### 4.1. MEANING OF THE DIP SWITCHES SW1 FOR THE R-8AI-8DIDO MODEL

Below is the meaning of the SW1 dip switches:

| <i>DIP1</i> | <i>DIP2</i> | <i>MEANING</i>                                                       |
|-------------|-------------|----------------------------------------------------------------------|
| OFF         | OFF         | Normal operation: The device loads the configuration from the flash. |
| ON          | ON          | Resets the device to its factory configuration                       |
| OFF         | ON          | Disables access to the Web server                                    |
| ON          | OFF         | Reserved                                                             |

## **ATTENTION!**

**ONCE COMMISSIONING HAS BEEN COMPLETED, IN ORDER TO INCREASE THE SECURITY OF THE DEVICE, DISABLE THE WEBSERVER THROUGH THE DIP SWITCHES**

### 4.2. MEANING OF SW1 DIP-SWITCHES FOR THE R-32DIDO MODEL

Below is the meaning of the SW1 dip switches for the various firmware revisions:

#### 4.2.1. DIP SWITCH SW1 FOR FIRMWARE REVISION <= 1014

| <i>DIP1</i> | <i>DIP2</i> | <i>MEANING</i>                                                                                         |
|-------------|-------------|--------------------------------------------------------------------------------------------------------|
| OFF         | OFF         | Normal operation: The device loads the configuration from the flash.                                   |
| ON          | ON          | Resets the device to its factory configuration                                                         |
| OFF         | ON          | Only forces the device IP address to the standard value of SENECA Ethernet products:<br>192.168.90.101 |
| ON          | OFF         | Reserved                                                                                               |

#### 4.2.2. DIP SWITCH SW1 FOR FIRMWARE REVISION >= 1015

| <i>DIP1</i> | <i>DIP2</i> | <i>MEANING</i>                                                       |
|-------------|-------------|----------------------------------------------------------------------|
| OFF         | OFF         | Normal operation: The device loads the configuration from the flash. |
| ON          | ON          | Resets the device to its factory configuration                       |
| OFF         | ON          | Disables access to the Web server                                    |
| ON          | OFF         | Reserved                                                             |

## **ATTENTION!**

**ONCE COMMISSIONING HAS BEEN COMPLETED, IN ORDER TO INCREASE THE SECURITY OF THE DEVICE, DISABLE THE WEBSERVER THROUGH THE DIP SWITCHES**

#### 4.3. MEANING OF THE SW1 DIP SWITCHES FOR THE R-SG3 MODEL

Below is the meaning of the SW1 dip switches:

| <i>DIP1</i> | <i>DIP2</i> | <i>MEANING</i>                                                       |
|-------------|-------------|----------------------------------------------------------------------|
| OFF         | OFF         | Normal operation: The device loads the configuration from the flash. |
| ON          | ON          | Resets the device to its factory configuration                       |
| OFF         | ON          | Disables access to the Web server                                    |
| ON          | OFF         | Reserved                                                             |



**ONCE COMMISSIONING HAS BEEN COMPLETED, IN ORDER TO INCREASE THE SECURITY OF THE DEVICE, DISABLE THE WEBSERVER THROUGH THE DIP SWITCHES**

#### 4.4. MEANING OF THE DIP SWITCHES SW1 FOR THE R-8AI-8DIDO MODEL

Below is the meaning of the SW1 dip switches:

| <i>DIP1</i> | <i>DIP2</i> | <i>MEANING</i>                                                       |
|-------------|-------------|----------------------------------------------------------------------|
| OFF         | OFF         | Normal operation: The device loads the configuration from the flash. |
| ON          | ON          | Resets the device to its factory configuration                       |
| OFF         | ON          | Disables access to the Web server                                    |
| ON          | OFF         | Reserved                                                             |



**ONCE COMMISSIONING HAS BEEN COMPLETED, IN ORDER TO INCREASE THE SECURITY OF THE DEVICE, DISABLE THE WEBSERVER THROUGH THE DIP SWITCHES**

#### 4.5. MEANING OF THE DIP SWITCHES SW1 FOR THE R-4AO-8DIDO MODEL

Below is the meaning of the SW1 dip switches:

| <i>DIP1</i> | <i>DIP2</i> | <i>MEANING</i>                                                       |
|-------------|-------------|----------------------------------------------------------------------|
| OFF         | OFF         | Normal operation: The device loads the configuration from the flash. |
| ON          | ON          | Resets the device to its factory configuration                       |
| OFF         | ON          | Disables access to the Web server                                    |
| ON          | OFF         | Reserved                                                             |



**ONCE COMMISSIONING HAS BEEN COMPLETED, IN ORDER TO INCREASE THE SECURITY OF THE DEVICE, DISABLE THE WEBSERVER THROUGH THE DIP SWITCHES**

#### 4.6. MEANING OF THE DIP SWITCHES SW1 FOR THE R-4RTD-8DIDO MODEL

Below is the meaning of the SW1 dip switches:

| <i>DIP1</i> | <i>DIP2</i> | <i>MEANING</i>                                                       |
|-------------|-------------|----------------------------------------------------------------------|
| OFF         | OFF         | Normal operation: The device loads the configuration from the flash. |
| ON          | ON          | Resets the device to its factory configuration                       |
| OFF         | ON          | Disables access to the Web server                                    |
| ON          | OFF         | Reserved                                                             |



**ONCE COMMISSIONING HAS BEEN COMPLETED, IN ORDER TO INCREASE THE SECURITY OF THE DEVICE, DISABLE THE WEBSERVER THROUGH THE DIP SWITCHES**

#### 4.7. MEANING OF THE DIP SWITCHES SW1 FOR THE R-2AI-6DIDO MODEL

Below is the meaning of the SW1 dip switches:

| <i>DIP1</i> | <i>DIP2</i> | <i>MEANING</i>                                                       |
|-------------|-------------|----------------------------------------------------------------------|
| OFF         | OFF         | Normal operation: The device loads the configuration from the flash. |

|     |     |                                                |
|-----|-----|------------------------------------------------|
| ON  | ON  | Resets the device to its factory configuration |
| OFF | ON  | Disables access to the Web server              |
| ON  | OFF | Reserved                                       |

## **ATTENTION!**

**ONCE COMMISSIONING HAS BEEN COMPLETED, IN ORDER TO INCREASE THE SECURITY OF THE DEVICE, DISABLE THE WEBSERVER THROUGH THE DIP SWITCHES**

### 5. I/O COPY USING THE PEER TO PEER FUNCTION WITHOUT A MASTER CONTROLLER

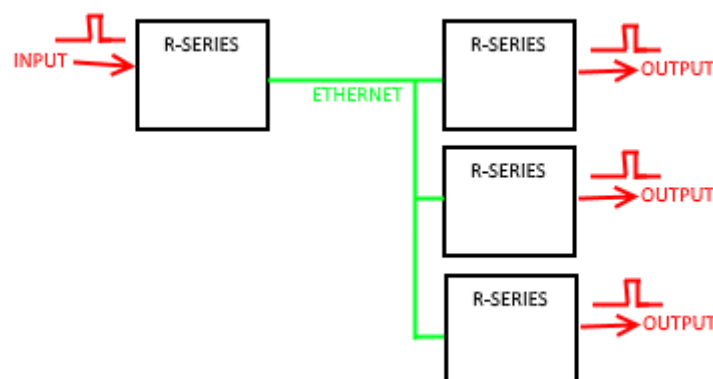
The "R" series devices can be used to copy and update in real time an input channel on a remote output channel without the aid of a master controller.

For example, a digital input can be copied to a remote digital output device:



Note that no controller is required because the communication is managed directly by the R series devices. It is possible to make a more sophisticated connection, for example it is possible to copy the inputs to different R-series remote devices (from Device 1 Input 1 to Device 2 Output1, Device 1 Input 2 to Device 3 Output 1 etc ...)

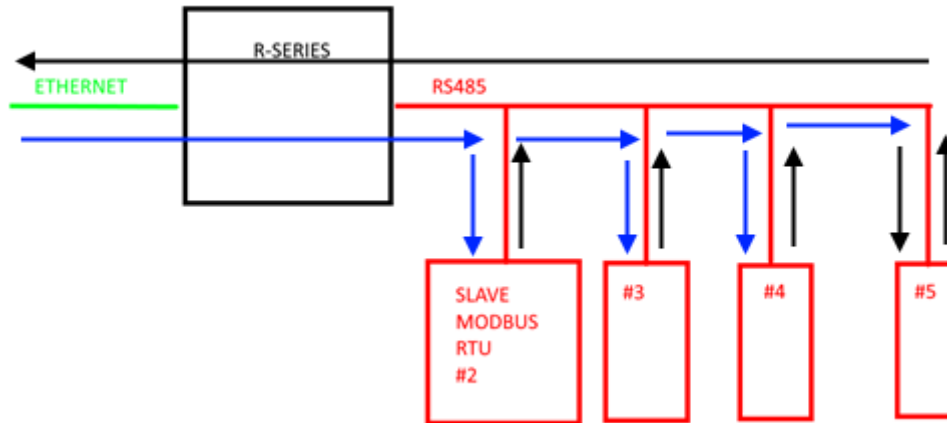
It is also possible to copy an input to an output of multiple remote devices:



Each R-series device can send and receive a maximum of 32 inputs.

## 6. MODBUS PASSTHROUGH

Thanks to the Modbus Passthrough function it is possible to extend the amount of I/O available in the device via the RS485 port and the Modbus RTU slave protocol, for example by using the Seneca Z-PC series products. In this mode the RS485 port stops working as Modbus RTU slave and the device becomes a gateway from Modbus TCP-IP (ethernet) to Modbus RTU (serial):



Each Modbus TCP-IP request with station address other than that of the R series device is converted into a serial packet on the RS485 and, in the case of a reply, it is turned over to TCP-IP. Therefore, it is no longer necessary to purchase gateways to extend the I/O number or to connect already available Modbus RTU I/O.



## 7. RESETTING THE DEVICE TO FACTORY CONFIGURATION

### 7.1. PROCEDURE FOR RESTORING DEVICES TO THE FACTORY CONFIGURATION

It is possible to reset the device to the factory configuration using the dip-switches (see chapter 4).

## 8. CONNECTION OF THE DEVICE TO A NETWORK

The factory configuration of the IP address is:

**Static address: 192.168.90.101**

Therefore, multiple devices must not be inserted on the same network with the same static IP.

If you want to connect multiple devices on the same network, you need to change the IP address configuration using Seneca Discovery Device software.



### **ATTENTION!**

***DO NOT CONNECT 2 OR MORE FACTORY-CONFIGURED DEVICES ON THE SAME NETWORK, OR THE  
ETHERNET INTERFACE WILL NOT WORK  
(CONFLICT OF IP ADDRESSES 192.168.90.101)***

If the addressing mode with DHCP is activated and an IP address is not received within 1 minute, the device will set an IP address with a fixed error:

169.254.x.y

Where x.y are the last two values of the MAC ADDRESS.

This way it is possible to install more I/O of the R series and then configure the IP with the Seneca Discovery Device software even on networks without a DHCP server.

## 9. WEB SERVER

### 9.1. ACCESS TO THE WEB SERVER

Access to the web server takes place using a web browser and entering the IP address of the device.  
To know the IP address of the device you can use the Seneca Discovery Device software.

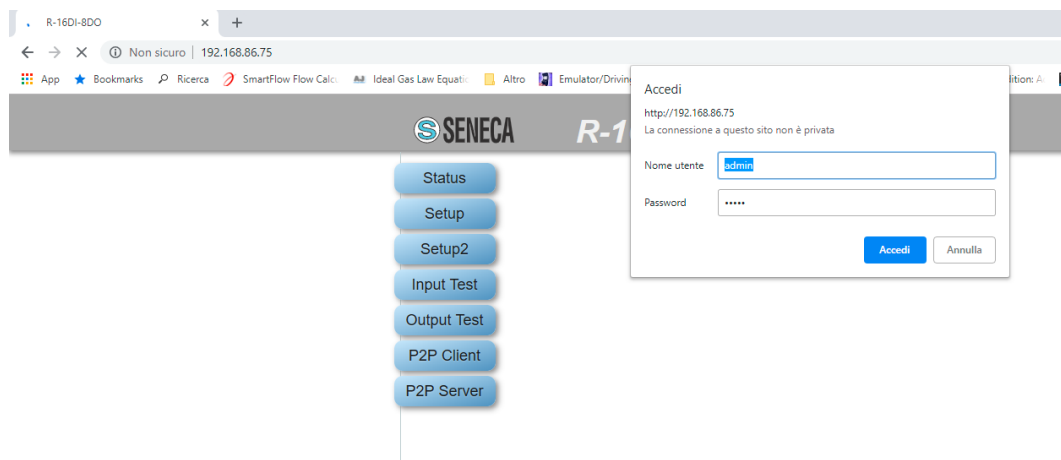
On first access the user name and password will be requested.  
The default values are:

User Name: admin  
Password: admin



# ATTENTION!

**AFTER THE FIRST ACCESS CHANGE USER NAME AND PASSWORD IN ORDER TO PREVENT ACCESS TO THE DEVICE TO UNAUTHORIZED PEOPLE.**



# ATTENTION!

**IF THE PARAMETERS TO ACCESS THE WEB SERVER HAVE BEEN LOST, IT IS NECESSARY TO RESET THE FACTORY-SET CONFIGURATION**



# ATTENTION!

**BEFORE ACCESSING THE WEBSERVER, CHECK THE STATE OF THE DIP-SWITCHES (SEE CHAPTER4)**

## 10. CONFIGURATION OF THE R-32DIDO DEVICE VIA WEB SERVER

### 10.1. SETUP SECTION

***DHCP (ETH) (default: Disabled)***

Sets the DHCP client to get an IP address automatically.

***IP ADDRESS STATIC (ETH) (default: 192.168.90.101)***

Sets the device static address. Careful not to enter devices with the same IP address into the same network.

***IP MASK STATIC (ETH) (default: 255.255.255.0)***

Sets the mask for the IP network.

***GATEWAY ADDRESS STATIC (ETH) (default: 192.168.90.1)***

Sets the gateway address.

***PROTECT CONFIGURATION (default: Disabled)***

Allows you to enable or disable password protection for reading and writing the configuration (including the IP address) using the Seneca Discovery Device software. The password is the same one that allows accessing the web server.



## ATTENTION!

IF THE CONFIGURATION PROTECTION IS ENABLED IT WILL BE IMPOSSIBLE TO READ/WRITE THE CONFIGURATION OF THE DEVICE WITHOUT KNOWING THE PASSWORD.  
IF THE PASSWORD IS LOST, IT WILL BE POSSIBLE TO RETURN THE DEVICE TO THE FACTORY-SET CONFIGURATION USING THE DIP SWITCHES

***MODBUS SERVER PORT (ETH) (default: 502)***

Sets the communication port for the Modbus TCP-IP server.

***MODBUS SERVER STATION ADDRESS (ETH) (default: 1)***

Active only if Modbus Passthrough is also active, it sets the station address of the modbus TCP-IP server.



## ATTENTION!

THE MODBUS SERVER WILL ANSWER ANY STATION ADDRESS ONLY IF THE MODBUS PASSTHROUGH MODE IS DISABLED.

***MODBUS PASSTHROUGH (ETH) (default: disabled)***

Sets the conversion mode from Modbus TCP-IP to Modbus RTU serial (see chapter 6).

**MODBUS TCP-IP CONNECTION TIMEOUT [sec] (ETH) (default: 60)**

Sets the TCP-IP connection timeout for the Modbus TCP-IP server and Passthrough modes.

**P2P SERVER PORT (default: 50026)**

Sets the communication port for the P2P server.

**WEB SERVER USERNAME (default: admin)**

Sets the username to access the webserver.

**CONFIGURATION/WEB SERVER PASSWORD (default: admin)**

Sets the password to access the webserver and to read/write the configuration (if enabled).

**WEB SERVER PORT (default: 80)**

Sets the communication port for the web server.

**BAUDRATE MODBUS RTU (SER) (default: 38400 baud)**

Sets the baud rate for the RS485 communication port.

**DATA MODBUS RTU (SER) (default: 8 bit)**

Sets the number of bits for the RS485 communication port.

**PARITY MODBUS RTU (SER) (default: None)**

Sets the parity for the RS485 communication port.

**STOP BIT MODBUS RTU (SER) (default: 1 bit)**

Sets the number of stop bits for the RS485 communication port.

**MODBUS PASSTHROUGH SERIAL TIMEOUT (default: 100ms)**

Active only if passthrough mode is activated, sets the maximum waiting time before sending a new packet from TCP-IP to the serial port. It must be set according to the longest response time of all the devices present on the RS485 serial port.

## 10.2. **DIGITAL I/O SETUP SECTION**

This section allows the configuration of the digital I/Os present in the device.

### ***DIGITAL I/O MODE (default Input)***

Selects whether the selected input will work as an input or output.

### ***DIGITAL INPUT NORMALLY HIGH/LOW (default Normally Low)***

If selected as digital input, it configures whether the input is normally high or low.

### ***DIGITAL OUTPUT NORMALLY STATE (default Normally Open)***

If selected as digital output, it configures whether the output is normally open or closed.

### ***DIGITAL OUTPUT WATCHDOG (default Disabled)***

If selected as digital output, it sets the output watchdog mode.

If "Disabled", it disables the watchdog function for the selected output.

If "Enabled on Modbus Communication" the output goes into "Watchdog state" if there has been no generic Modbus communication within the set time.

If "Enabled on Modbus Digital Output Writing" the output goes into "Watchdog state" if there has been no writing of the output within the set time.

### ***DIGITAL OUTPUT WATCHDOG STATE (default Open)***

Sets the value that the digital output must adopt if the watchdog has been triggered.

### ***DIGITAL OUTPUT WATCHDOG TIMEOUT [s] (default 100s)***

Represents the watchdog time of the digital output in seconds.

### 10.3. **SETUP COUNTERS SECTION**

#### **COUNTERS FILTER [ms] (default 0)**

Sets the value in [ms] for filtering all the counters connected to the inputs.

### 10.4. **P2P CONFIGURATION**

In the P2P Client section it is possible to define which local events to send to one or more remote devices. This way it is possible to send the status of the inputs to the remote outputs and obtain the input-output replication without wiring. It is also possible to send the same input to several outputs simultaneously.

In the P2P Server section it is instead possible to define which inputs must be copied to the outputs.

The "**Disable all rules**" button places all the rules in a disabled status (default).

The "**APPLY**" button allows you to confirm and then save the set rules in the non-volatile memory.

## 11. CONFIGURATION OF THE R-16DI-8DO DEVICE VIA WEB SERVER

### 11.1. SETUP SECTION


**SENECA**
*R-16DI-8DO (web server)*

Status  
Setup  
Setup2  
Input Test  
Output Test  
P2P Client  
P2P Server

Setup page(1/2):

|                                             | CURRENT        | UPDATED        |
|---------------------------------------------|----------------|----------------|
| DHCP (ETH)                                  | Enabled        | Disabled ▼     |
| IP ADDRESS STATIC (ETH)                     | 192.168.90.101 | 192.168.90.101 |
| IP MASK STATIC (ETH)                        | 255.255.255.0  | 255.255.255.0  |
| GATEWAY ADDRESS STATIC (ETH)                | 192.168.90.1   | 192.168.90.1   |
| PROTECT CONFIGURATION                       | Disabled       | Disabled ▼     |
| MODBUS SERVER PORT (ETH)                    | 502            | 502            |
| MODBUS SERVER STATION ADDRESS (ETH)         | 1              | 1              |
| MODBUS PASSTHROUGH (ETH)                    | Enabled        | Enabled ▼      |
| MODBUS TCP-IP CONNECTION TIMEOUT(sec) (ETH) | 60             | 60             |
| P2P SERVER PORT (ETH)                       | 50026          | 50026          |
| WEBSERVER USER NAME                         | admin          | admin          |
| CONFIGURATION/WEBSERVER PASSWORD            | admin          | admin          |
| WEBSERVER PORT                              | 80             | 80             |
| BAUDRATE MODBUS RTU (SER)                   | 38400          | 38400 ▼        |
| DATA MODBUS RTU (SER)                       | 8              | 8 ▼            |
| PARITY MODBUS RTU (SER)                     | None           | None ▼         |
| STOP BIT MODBUS RTU (SER)                   | 1              | 1 ▼            |
| MODBUS PASSTHROUGH SERIAL TIMEOUT [ms]      | 100            | 100            |

REBOOT
FACTORY DEFAULT
APPLY

#### **DHCP (ETH) (default: Disabled)**

Sets the DHCP client to get an IP address automatically.

#### **IP ADDRESS STATIC (ETH) (default: 192.168.90.101)**

Sets the device static address. Careful not to enter devices with the same IP address into the same network.

#### **IP MASK STATIC (ETH) (default: 255.255.255.0)**

Sets the mask for the IP network.

#### **GATEWAY ADDRESS STATIC (ETH) (default: 192.168.90.1)**

Sets the gateway address.

#### **PROTECT CONFIGURATION (default: Disabled)**

Allows you to enable or disable password protection for reading and writing the configuration (including the IP address) using the Seneca Discovery Device software.

**ATTENTION!**

IF THE CONFIGURATION PROTECTION IS ENABLED IT WILL BE IMPOSSIBLE TO READ/WRITE THE CONFIGURATION OF THE DEVICE WITHOUT KNOWING THE PASSWORD.  
IF THE PASSWORD HAS BEEN LOST, THE DEVICE CAN BE RETURNED TO ITS DEFAULT SETTINGS BY CONNECTING IT VIA USB TO THE EASY SETUP 2 SOFTWARE

**MODBUS SERVER PORT (ETH) (default: 502)**

Sets the communication port for the Modbus TCP-IP server.

**MODBUS SERVER STATION ADDRESS (ETH) (default: 1)**

Active only if Modbus Passthrough is also active, it sets the station address of the modbus TCP-IP server.

**ATTENTION!**

THE MODBUS SERVER WILL ANSWER ANY STATION ADDRESS ONLY IF THE MODBUS PASSTHROUGH MODE IS DISABLED.

**MODBUS PASSTHROUGH (ETH) (default: disabled)**

Sets the conversion mode from Modbus TCP-IP to Modbus RTU serial (see chapter 6).

**MODBUS TCP-IP CONNECTION TIMEOUT [sec] (ETH) (default: 60)**

Sets the TCP-IP connection timeout for the Modbus TCP-IP server and Passthrough modes.

**P2P SERVER PORT (default: 50026)**

Sets the communication port for the P2P server.

**WEB SERVER USER NAME (default: admin)**

Sets the user name to access the web server.

**CONFIGURATION/WEB SERVER PASSWORD (default: admin)**

Sets the password to access the webserver and to read/write the configuration (if enabled).

**WEB SERVER PORT (default: 80)**

Sets the communication port for the web server.

**BAUDRATE MODBUS RTU (SER) (default: 38400 baud)**

Sets the baud rate for the RS485 communication port.

**DATA MODBUS RTU (SER) (default: 8 bit)**

Sets the number of bits for the RS485 communication port.



**PARITY MODBUS RTU (SER) (default: None)**

Sets the parity for the RS485 communication port.

**STOP BIT MODBUS RTU (SER) (default: 1 bit)**

Sets the number of stop bits for the RS485 communication port.

**MODBUS PASSTHROUGH SERIAL TIMEOUT (default: 100ms)**

Active only if passthrough mode is activated, sets the maximum waiting time before sending a new packet from TCP-IP to the serial port. It must be set according to the longest response time of all the devices present on the RS485 serial port.

**ATTENTION!**

THE USB PORT CONFIGURATION PARAMETERS CANNOT BE MODIFIED AND ARE BAUDRATE:

115200

DATA: 8 BIT

PARITY: NONE

STOP BIT: 1

MODBUS RTU PROTOCOL

## 11.2. SETUP 2 SECTION

Status
Setup
Setup2
Input Test
Output Test
P2P Client
P2P Server

**Setup page(2/2):** (WARNING: before update the firmware, it's safe to save the current device configuration.)

|                                    | CURRENT | UPDATED                               |
|------------------------------------|---------|---------------------------------------|
| COUNTERS FILTER [ms]               | 100     | <input type="text" value="0"/>        |
| INPUTS TYPE                        | Pnp     | <input type="text" value="Pnp"/>      |
| COUNTER DIRECTION                  | Up      | <input type="text" value="Up"/>       |
| DIGITAL OUTPUTS WATCHDOG           | Enabled | <input type="text" value="Disabled"/> |
| DIGITAL OUTPUTS WATCHDOG T.OUT [s] | 5       | <input type="text" value="5"/>        |

| state     | NORMALLY STATE           | FAULT                    |
|-----------|--------------------------|--------------------------|
| Output 01 | <input type="checkbox"/> | <input type="checkbox"/> |
| Output 02 | <input type="checkbox"/> | <input type="checkbox"/> |
| Output 03 | <input type="checkbox"/> | <input type="checkbox"/> |
| Output 04 | <input type="checkbox"/> | <input type="checkbox"/> |
| Output 05 | <input type="checkbox"/> | <input type="checkbox"/> |
| Output 06 | <input type="checkbox"/> | <input type="checkbox"/> |
| Output 07 | <input type="checkbox"/> | <input type="checkbox"/> |
| Output 08 | <input type="checkbox"/> | <input type="checkbox"/> |

REBOOT
FACTORY DEFAULT
APPLY

Configure
Scegli file
Nessun file selezionato
Load config
Save config

Firmware
Scegli file
Nessun file selezionato
Update firmware

### COUNTERS FILTER (default: 100ms)

Sets the filtering of the counters, the value is expressed in [ms].

The filter cut-off frequency corresponds to:

$$f_{cut}[Hz] = \frac{1000}{2 * Counters Filter [ms]}$$

For example, if the filter counter is 100ms the cutting frequency will be:

$$f_{cut}[Hz] = \frac{1000}{2 * Counters Filter [ms]} = 5 Hz$$

So all input frequencies greater than 5 Hz will be cut.

### INPUTS TYPE (default: Pnp "Source")

Sets the input/counter operating mode to between npn "Sink" and pnp "Source".

### COUNTER DIRECTION (default: Up)

Sets the counting mode of the counters "forward", up or back "down".

In the "Up" mode when the counter reaches the value:

$$Max\ Value = 2^{32} - 1 = 4294967295$$

A subsequent increase will return the value to 0.

In the "Down" mode, if the counter value is 0, a subsequent input pulse will return the value to 4294967295.

#### ***DIGITAL OUTPUT WATCHDOG (default: Disabled)***

Set whether the digital output watchdog is to activated. When enabled, if within the timeout time there has been no communication from the master to the device (Modbus serial communication, TCP-IP or USB or P2P communication) the outputs go into a Fail state. This mode makes it possible to obtain a secure system in the event of a master malfunction and its use is recommended in the case of radio type connections.

#### ***DIGITAL OUTPUTS WATCHDOG T.OUT [s] (default: 5 s)***

Sets the watchdog time of the digital outputs (valid only if the DIGITAL OUTPUT WATCHDOG parameter is enabled)

#### ***NORMALLY STATE/FAULT (default: normally Normally open (N.O.) and Normally closed (N.C.) state in case of fail***

They set the states of each of the outputs in normal conditions and in the event of a failure.

In the case of normally open (not energized)  writing in the Modbus "Outputs" register with 0 will cause the relay not to energize, otherwise, in the case of normally closed (energized)  writing in the Modbus "Outputs" register with 1 will determine the relay not to be energized.

In the case of "fail" the output will go into the selected configuration between not energized  or energized .

The "**Configure**" section allows you to save or open a complete configuration of the device.

The "**Firmware**" section allows you to update the device firmware in order to obtain new functions.

## 12. CONFIGURATION OF THE R-8AI-8DIDO DEVICE VIA WEB SERVER

### 12.1. SETUP SECTION

***DHCP (ETH) (default: Disabled)***

Sets the DHCP client to get an IP address automatically.

***IP ADDRESS STATIC (ETH) (default: 192.168.90.101)***

Sets the device static address. Careful not to enter devices with the same IP address into the same network.

***IP MASK STATIC (ETH) (default: 255.255.255.0)***

Sets the mask for the IP network.

***GATEWAY ADDRESS STATIC (ETH) (default: 192.168.90.1)***

Sets the gateway address.

***PROTECT CONFIGURATION (default: Disabled)***

Allows you to enable or disable password protection for reading and writing the configuration (including the IP address) using the Seneca Discovery Device software. The password is the same one that allows accessing the web server.



## ATTENTION!

IF THE CONFIGURATION PROTECTION IS ENABLED IT WILL BE IMPOSSIBLE TO READ/WRITE THE CONFIGURATION OF THE DEVICE WITHOUT KNOWING THE PASSWORD.  
IN THE EVENT OF LOSING THE PASSWORD IT WILL BE POSSIBLE TO RETURN THE DEVICE TO THE FACTORY CONFIGURATION (SEE CHAPTER7)

***MODBUS SERVER PORT (ETH) (default: 502)***

Sets the communication port for the Modbus TCP-IP server.

***MODBUS SERVER STATION ADDRESS (ETH) (default: 1)***

Active only if Modbus Passthrough is also active, it sets the station address of the modbus TCP-IP server.



## ATTENTION!

THE MODBUS SERVER WILL ANSWER ANY STATION ADDRESS ONLY IF THE MODBUS PASSTHROUGH MODE IS DISABLED.

***MODBUS PASSTHROUGH (ETH) (default: disabled)***

Sets the conversion mode from Modbus TCP-IP to Modbus RTU serial (see chapter 6).

**MODBUS TCP-IP CONNECTION TIMEOUT [sec] (ETH) (default: 60)**

Sets the TCP-IP connection timeout for the Modbus TCP-IP server and Passthrough modes.

**P2P SERVER PORT (default: 50026)**

Sets the communication port for the P2P server.

**WEB SERVER USERNAME (default: admin)**

Sets the username to access the webserver.

**CONFIGURATION/WEB SERVER PASSWORD (default: admin)**

Sets the password to access the webserver and to read/write the configuration (if enabled).

**WEB SERVER PORT (default: 80)**

Sets the communication port for the web server.

**BAUDRATE MODBUS RTU (SER) (default: 38400 baud)**

Sets the baud rate for the RS485 communication port.

**DATA MODBUS RTU (SER) (default: 8 bit)**

Sets the number of bits for the RS485 communication port.

**PARITY MODBUS RTU (SER) (default: None)**

Sets the parity for the RS485 communication port.

**STOP BIT MODBUS RTU (SER) (default: 1 bit)**

Sets the number of stop bits for the RS485 communication port.

**MODBUS PASSTHROUGH SERIAL TIMEOUT (default: 100ms)**

Active only if Passthrough mode is activated, sets the maximum waiting time before sending a new packet from TCP-IP to the serial port. It must be set according to the longest response time of all the devices present on the RS485 serial port.

**CHANNEL SAMPLE TIME [ms] (default: 100ms)**

Sets the sampling time of each analog input.

## 12.2. SETUP AIN 1.8 SECTION

This section allows the configuration of the analog inputs present in the device.



### ATTENTION!

THE DEVICE CAN DETECT THE COLD JOINT TEMPERATURE FROM THE INTERNAL SENSORS OR FROM ANALOG INPUT 1 (THROUGH EXTERNAL PT100-TYPE SENSOR).  
IN THIS CASE ALL THE DETECTIONS OF THE INTERNAL SENSORS WILL BE REPLACED BY THE READING OF ANALOG INPUT 1.

#### ***ANALOG INPUT MODE (default +-30V)***

Set the type of measurement for the selected input.

It is possible to choose between the following types of input:

+-30V

+-100mV

+-24 mA

Thermocouple

PT100 3 wires only for input 1)

If the "IN2..8 CJ PT100" type of measurement is selected for input 1, this will automatically be used as a measurement of the cold junction for all inputs configured by thermocouple between IN2 and IN8 included.

#### ***ANALOG INPUT 1 PT100 WIRE RESISTANCE [Ohm] (default 0 Ohm)***

(Only for analog input 1) allows to compensate the cable resistance in case of 2-wire connection to the PT100.

#### ***ANALOG INPUT TC TYPE (default J)***

In the case of thermocouple measurement, it allows to select the type of thermocouple between:

J, K, R, S, T, B, E, N, L

#### ***ANALOG INPUT TEMPERATURE OFFSET (default 0°C)***

Sets a temperature offset in °C for thermocouple measurements

#### ***ANALOG INPUT ONBOARD COLD JUNCTION (default ENABLED)***

In the case of thermocouple measurement, it enables or disables the automatic cold junction offset of the device. If channel 1 has been configured as PT100 cold junction measurement, this sensor will be used for the offset and not the one inside the instrument.

#### ***ANALOG INPUT COLD JUNCTION VALUE [°C] (default 0°C)***

In the case of thermocouple measurement, if the automatic measurement of the cold junction has been deactivated, it is possible to manually enter the cold junction temperature.

***ANALOG INPUT BURNOUT MODE (default FAIL VALUE)***

In the case of thermocouple measurement, it selects the behaviour in case of sensor failure:

In the case of "Last Value" the value is stopped at the last valid value, in the case of "Fail Value" the "Burnout" value is loaded in the registers.

***ANALOG INPUT BURNOUT VALUE (default 10000°C)***

In the case of thermocouple measurement, if the ANALOG INPUT BURNOUT MODE = "FAIL VALUE" mode is activated and the sensor is in the "burn" state, it allows you to set a value in °C to be taken by the measurement register.

***ANALOG INPUT UNIT MEASURE (default °C)***

In the case of thermocouple measurement, it allows you to set the measurement unit of the measurement register between °C, K, °F and mV.

***ANALOG INPUT FILTER [samples] (default 0)***

Allows you to set the moving average filter with the selected number of samples. If the value is "0" the filter is disabled.

***ANALOG INPUT START SCALE***

Represents the start of the electrical scale of the analog measurement used for the register of the engineering measurement.

***ANALOG INPUT STOP SCALE***

Represents the electrical full scale of the analog measurement used for the engineering measurement register.

***ANALOG INPUT ENG START SCALE***

It represents the value of the engineering measurement register when the input reaches the value shown in the ANALOG INPUT START SCALE parameter.

For example if:

ANALOG INPUT START SCALE = 4mA

ANALOG INPUT STOP SCALE = 20mA

ANALOG INPUT ENG STOP SCALE = -200 metres

ANALOG INPUT ENG START SCALE = 200 metres

With a 12 mA input the engineering value will be 0 metres.

***ANALOG INPUT ENG STOP SCALE***

It represents the value of the engineering measurement register when the input reaches the value shown in the ANALOG INPUT STOP SCALE parameter.

For example if:

ANALOG INPUT START SCALE = 4mA

ANALOG INPUT STOP SCALE = 20mA

ANALOG INPUT ENG STOP SCALE = -200 metres

ANALOG INPUT ENG START SCALE = 200 metres

With a 12 mA input the engineering value will be 0 metres.

### 12.3. **DIGITAL I/O SETUP SECTION**

This section allows the configuration of the digital I/Os present in the device.

#### ***DIGITAL I/O MODE (default Input)***

Selects whether the chosen terminal will work as an input, output or output commanded by an event related to the selected analog input.

#### ***DIGITAL INPUT NORMALLY HIGH/LOW (default Normally Low)***

If selected as digital input, it configures whether the input is normally high or low.

#### ***DIGITAL OUTPUT NORMALLY STATE (default Normally Open)***

If selected as digital output, it configures whether the output is normally open or closed.

#### ***DIGITAL OUTPUT WATCHDOG (default Disabled)***

If selected as digital output, it sets the output watchdog mode.

If "Disabled", it disables the watchdog function for the selected output.

If "Enabled on Modbus Communication" the output goes into "Watchdog state" if there has been no generic Modbus communication within the set time.

If "Enabled on Modbus Digital Output Writing" the output goes into "Watchdog state" if there has been no writing of the output within the set time.

#### ***DIGITAL OUTPUT WATCHDOG STATE (default Open)***

Sets the value that the digital output must adopt if the watchdog has been triggered.

#### ***DIGITAL OUTPUT WATCHDOG TIMEOUT [s] (default 100s)***

Represents the watchdog time of the digital output in seconds.



## 12.4. **EVENT SETUP SECTION**

This section allows the configuration of events to send analog values with the P2P protocol.

### **EVENT AIN MODE (Default: DISABLED)**

Represents the event condition for sending packets linked to the analog inputs in the P2P protocol.

It may be:

"Disabled" the sending event of the analog packet is disabled

"Event when  $AIN > HIGH\ THRESHOLD$ " the packet sending event occurs when the analog input exceeds the

"High" threshold set.

"Event when  $AIN < LOW\ THRESHOLD$ " the packet sending event occurs when the analog input is lower than the "Low" threshold set.

### **EVENT AIN HIGH THRESHOLD (Default: 0)**

Threshold value linked to the "High" event.

### **EVENT AIN LOW THRESHOLD (Default: 0)**

Threshold value linked to the "Low" event.

### **EVENT AIN HYSTERESYS**

Hysteresis value for the reset of the "event" condition.

For example, if the event is configured in "Event when  $AIN > HIGH\ THRESHOLD$ " mode, when the analog input exceeds the threshold value, the packet will be sent, to send the next packet it will be necessary for the analog value to fall below the value ( $EVENT\ AIN\ HIGH\ THRESHOLD + EVENT\ AIN\ HYSTERESIS$ ) and then to rise above the HIGH value again.

## 13. CONFIGURATION OF THE R- SG3 DEVICE VIA WEB SERVER

### 13.1. SETUP SECTION

***DHCP (ETH) (default: Disabled)***

Sets the DHCP client to get an IP address automatically.

***IP ADDRESS STATIC (ETH) (default: 192.168.90.101)***

Sets the device static address. Careful not to enter devices with the same IP address into the same network.

***IP MASK STATIC (ETH) (default: 255.255.255.0)***

Sets the mask for the IP network.

***GATEWAY ADDRESS STATIC (ETH) (default: 192.168.90.1)***

Sets the gateway address.

***MODBUS SERVER PORT (ETH) (default: 502)***

Sets the communication port for the Modbus TCP-IP server.

***MODBUS SERVER STATION ADDRESS (ETH) (default: 1)***

Active only if Modbus Passthrough is also active, it sets the station address of the modbus TCP-IP server.



## ATTENTION!

THE MODBUS SERVER WILL ANSWER ANY STATION ADDRESS ONLY IF THE MODBUS PASSTHROUGH MODE IS DISABLED.

***MODBUS PASSTHROUGH (ETH) (default: disabled)***

Sets the conversion mode from Modbus TCP-IP to Modbus RTU serial (see chapter 6).

***MODBUS TCP-IP CONNECTION TIMEOUT [sec] (ETH) (default: 60)***

Sets the TCP-IP connection timeout for the Modbus TCP-IP server and Passthrough modes.

***P2P SERVER PORT (default: 50026)***

Sets the communication port for the P2P server.

***WEB SERVER USERNAME (default: admin)***

Sets the username to access the webserver.

***CONFIGURATION/WEB SERVER PASSWORD (default: admin)***

Sets the password to access the webserver and to read/write the configuration (if enabled).

***WEB SERVER PORT (default: 80)***

Sets the communication port for the web server.

***BAUDRATE MODBUS RTU (SER) (default: 38400 baud)***

Sets the baud rate for the RS485 communication port.

***DATA MODBUS RTU (SER) (default: 8 bit)***

Sets the number of bits for the RS485 communication port.

***PARITY MODBUS RTU (SER) (default: None)***

Sets the parity for the RS485 communication port.

***STOP BIT MODBUS RTU (SER) (default: 1 bit)***

Sets the number of stop bits for the RS485 communication port.

***MODBUS PASSTHROUGH SERIAL TIMEOUT (default: 100ms)***

Active only if Passthrough mode is activated, sets the maximum waiting time before sending a new packet from TCP-IP to the serial port. It must be set according to the longest response time of all the devices present on the RS485 serial port.

## 13.2. **LOAD CELL SETUP SECTION**

### **FUNCTION MODE**

It allows to configure the basic operation of the device, can be set to factory calibration or to Calibration with standard weight.

#### **FACTORY CALIBRATION**

It is used when a load cell with declared sensitivity is available.

In this mode, calibration only consists in acquiring the tare directly in the field with a direct measurement.

If it is not possible to acquire the tare with a direct measurement (for example in the case of an already filled silo) it is possible to manually enter the tare value in the desired unit of measurement (kg, t, etc.).

#### **CALIBRATION WITH STANDARD WEIGHT**

It is used when a standard weight is available (as far as possible towards the load cell full scale).

In this mode the calibration consists in acquiring both the tare and the standard weight directly on the field.

### **MEASURE TYPE**

It allows to configure the operation of the device between:

#### **BALANCE (UNIPOLAR)**

It is used when a scale is being created in which the load cell is only compressed, in this case the maximum resolution of the compression measurement is obtained.

**COMPRESSION AND TRACTION (BIPOLAR)**

It is used when a measurement system (typically of force) is being created that can both compress and extend the load cell. In this case the direction of the force can also be decided, if compression the measurement will have the + sign, if traction it will have the - sign. A typical case of use is to link the direction of the force to the analog output so that, for example, 4mA correspond to the maximum traction force and 20mA correspond to the maximum compression force (in this case the cell at rest will provide 12Ma).

**MEASURE UNIT**

Sets the unit of measurement for the weighing in g, Kg, t etc.

**CELL SENSITIVITY**

It is the declared cell sensitivity value expressed in mV/V (in most cells it is 2mV/V).

**CELL FULL SCALE**

It is the full scale value of the cell expressed in the selected unit of measurement.

**STANDARD WEIGHT VALUE**

It represents the value of the standard weight that will be used in the calibration if the operating mode with standard weight has been chosen.

**NOISE FILTER**

Enables or disables measurement filtering.

**FILTER LEVEL**

Allows you to set the measurement filter level according to the following table:

| FILTER LEVEL | RESPONSE TIME [ms] |
|--------------|--------------------|
| 0            | 2                  |
| 1            | 6.7                |
| 2            | 13                 |
| 3            | 30                 |
| 4            | 50                 |
| 5            | 250                |
| 6            | 850                |
| ADVANCED     | Configurable       |

The higher the filter level the more stable (but slow) the weight measurement will be.

If you select the advanced filtering level (Advanced), the configuration will allow you to select the following parameters:

**ADC SPEED** Selects the ADC acquisition speed from 4.7 Hz to 960 Hz

**NOISE VARIATION** It is the variation in ADC points due to noise alone (represents the measurement uncertainty due to noise) or how much we expect the measurement to vary (the unit of measurement is in raw ADC points).

**FILTER RESPONSE SPEED**

Represents a parameter related to the filter response speed, it can vary from 0.001 (slowest response) to 1 (fastest response). Represents the variance of the process.

**NET WEIGHT RESOLUTION**

It is the resolution with which the value of the net weighing is represented, it can be worth:

**MAXIMUM RESOLUTION**

It will represent the net weight with the highest possible resolution

**MANUAL**

It will represent the net weight with the manual resolution set (in engineering units).

For example, by setting 0.1 Kg you will get that the net weight can only vary by multiples of 100g.

**AUTOMATIC RESOLUTION**

It will represent the net weight with a calculated resolution of about 20000 points. Unlike Maximum or Manual resolution, this setting limits also the ADC value and therefore affects all measurements.

 **CAUTION**

Keep in mind that in the "Calibration with standard weight" mode, using the "Manual Resolution", the correct standard weight value may not be perfectly represented:

For example, you have:

Cell full scale 15000 g

Standard weight 14000 g

Manual Resolution 1.5 g

The value of the standard weight (14000 g) cannot be represented with the resolution in 1.5g steps (14000/1.5g = 9333.333 is not an integer value) so it will be represented as:  $9333 \times 1.5g = 13999.5g$

To avoid this effect, use a resolution that allows the value to be represented (for example 1g or 2g).

**SAMPLE PIECE WEIGHT**

Sets the weight of a single piece in technical units for the mode. By setting the net weight of a single element in this register, the converter will be able to indicate the number of pieces present in the scales special register according to the relation:

$$Nr\ Pezzi = \frac{Peso\ Netto}{Peso\ Pezzo\ Campione}$$

**AUTOMATIC TARE TRACKER**

It allows you to enable or disable the automatic tare reset.

**ADC VALUE**

It allows to set the number of ADC points within which to reset the tare automatically.

If after 5 seconds of stable weighing condition the ADC value of the net weight deviates by less than this value then a new tare is acquired.

**13.3. I/O SETUP SECTION****DIGITAL I/O MODE**

Configures the digital I/O of the device

**DIGITAL INPUT**

If the nth IO is configured as an input, it is possible to choose its function from:

**FUNCTION DIGITAL INPUT**

The input is configured as a digital input whose value can be read from the appropriate register.

**FUNCTION ACQUIRE TARE**

In this mode, if the digital input is activated for a time longer than 3 seconds, a new tare value is acquired (in RAM, then it is lost upon restart). It is equivalent to sending the command 49594 (decimal) in the command register.

**DIGITAL OUTPUT**

If the nth IO is configured as an output, it is possible to choose its function from:

**DIGITAL OUTPUT MODE**

The output can be configured as normally open (**Normally Open**) or as normally closed (**Normally Close**).

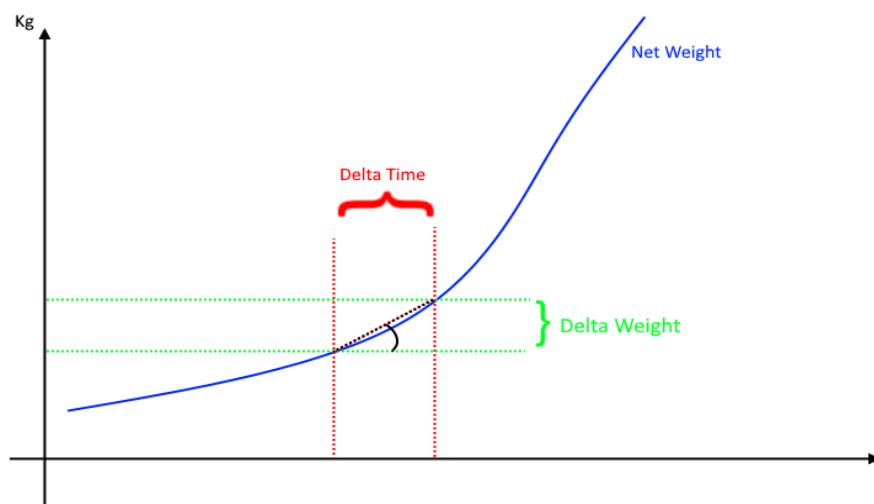
**DIGITAL OUTPUT CONFIGURATION**

Here you can choose the behaviour of the digital output:

**STABLE WEIGHT**

The stable weighing condition is used to indicate that the net weight measurement is stable if:

The net weight remains within the weight  $\Delta peso\_netto$  over time  $\Delta tempo$  or if the slope of the curve drawn by the net weight is less than  $\frac{\Delta peso\_netto}{\Delta tempo}$  :



You will be prompted to enter Delta Net Weight (**Delta Weight**) (in engineering units) and Delta Time (**Delta Time**) (in 0.1 seconds).

### **THRESHOLD AND STABLE WEIGHT**

In this mode, the output activates when the net weight reaches the threshold and the weigh is in a stable weighing condition.

### **STABLE WEIGHT**

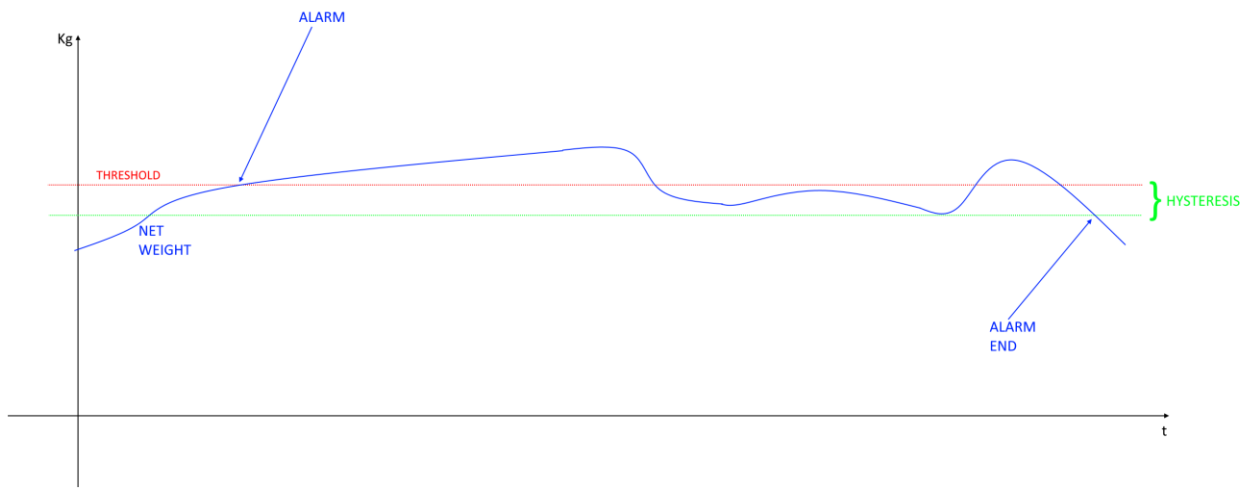
In this mode the output is activated if the weighing is in the stable weighing condition.

## COMMANDABLE FROM MODBUS

In this mode the output can be controlled by the modbus register.

### THRESHOLD WITH HYSTERESIS

In this mode the output is activated when the net weight reaches the threshold, the alarm is cancelled when the net weight falls below the Threshold-Hysteresis value:

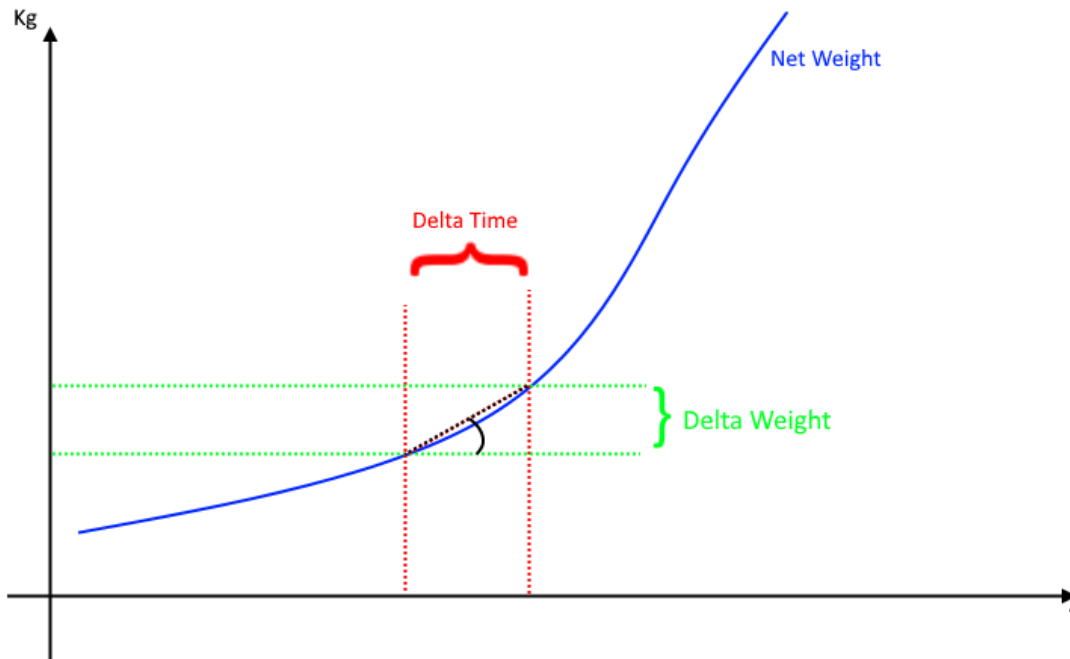


### STABLE WEIGHT CONDITION

The stable weighing condition is used to indicate that the net weight measurement is stable if:

The net weight remains within the weight  $\Delta peso\_netto$  (DELAT WEIGHT) over time  $\Delta tempo$  (DELTA TIME) or if the slope of the curve drawn by the net weight is less than  $\frac{\Delta peso\_netto}{\Delta tempo}$  :





#### 13.4. TEST AND LOAD CELL CALIBRATION SECTION

In this section it is possible to calibrate the cell and carry out the tests. For more information on cell calibration refer to the Cell Calibration chapter of this manual.

#### 13.5. P2P CONFIGURATION

In the P2P Client section it is possible to define which local events to send to one or more remote devices. This way it is possible to send the status of the inputs to the remote outputs and obtain the input-output replication without wiring. It is also possible to send the same input to several outputs simultaneously.

In the P2P Server section it is instead possible to define which inputs must be copied to the outputs.

The "**Disable all rules**" button places all the rules in a disabled status (default).

The "**APPLY**" button allows you to confirm and then save the set rules in the non-volatile memory.

#### 13.6. LOAD CELL CALIBRATION THROUGH THE WEB SERVER

To calibrate the load cell, access the "TEST AND LOAD CELL CALIBRATION" section of the web server. Depending on the two modes chosen between factory calibration or with standard weight, it will be possible to proceed with the calibration.

### 13.6.1. CELL CALIBRATION WITH FACTORY PARAMETERS

In cell calibration with factory parameters it is not necessary to use a standard weight as reference is made to the parameters acquired in the factory.

The necessary data are:

- The cell sensitivity
- The cell full scale

For the cell calibration procedure it is necessary to acquire the tare.

The tare can be entered manually in technical units (if known) or it can be acquired from the field.



**TO OBTAIN A BETTER MEASUREMENT ACCURACY ACQUIRE THE TARE FROM THE FIELD**

#### 13.6.1.1. MANUAL ENTRY OF THE TARE VIA WEB SERVER

It is not always possible to acquire the tare value from the field (for example in the case of already filled silos), in these cases it is possible to introduce the tare weight in technical units.

#### LOAD CELL CALIBRATION

| VALUE                                     |                    |
|-------------------------------------------|--------------------|
| <b>FUNCTION MODE: FACTORY CALIBRATION</b> |                    |
| <b>TARE ACQUISITION</b>                   |                    |
| <b>TARE VALUE [g]</b> 50.00000            | MANUAL INSERTION ▾ |
|                                           | 750                |
| SET MANUAL TARE (FLASH)                   |                    |

To acquire the tare value, press the "SET MANUAL TARE (FLASH)" button

#### 13.6.1.2. ACQUISITION OF THE TARE FROM THE FIELD VIA WEB SERVER

- 1) Enter the "Test and load cell calibration" web server page
- 2) Replace the tare on the cell
- 3) Wait for the measurement to stabilize
- 4) Press the "TARE ACQUISITION (FLASH)" button

## LOAD CELL CALIBRATION

|                                                                                                           | VALUE        |
|-----------------------------------------------------------------------------------------------------------|--------------|
| <b>FUNCTION MODE: FACTORY CALIBRATION</b>                                                                 |              |
| <b>TARE ACQUISITION</b>                                                                                   | FROM FIELD ▼ |
| TARE ACQUISITION(FLASH)  |              |

### 13.6.2. CELL CALIBRATION WITH A STANDARD WEIGHT

In cell calibration with a standard weight it is necessary to know:

- The cell sensitivity
- The cell full scale
- A standard weight (so that Standard weight + Tare are as close as possible to the cell full scale)

- 1) Enter the "Test and load cell calibration" web server page
- 2) Replace the tare on the cell
- 3) Wait for the measurement to stabilize
- 4) Press the "TARE ACQUISITION (FLASH)" button

## LOAD CELL CALIBRATION

**FUNCTION MODE: CALIBRATION WITH STANDARD WEIGHT**

|                                                                                                             |                                                                                                                          |
|-------------------------------------------------------------------------------------------------------------|--------------------------------------------------------------------------------------------------------------------------|
| TARE ACQUISITION(FLASH)  | STANDARD WEIGHT ACQUISITION(FLASH)  |
|-------------------------------------------------------------------------------------------------------------|--------------------------------------------------------------------------------------------------------------------------|

- 5) Replace the Tare + Standard Weight
- 6) Wait for the measurement to stabilize
- 7) Press the "STANDARD WEIGHT ACQUISITION (FLASH)" button

## 14. CONFIGURATION OF THE R-4AO-8DIDO DEVICE VIA WEB SERVER

### 14.1. SETUP SECTION

***DHCP (ETH) (default: Disabled)***

Sets the DHCP client to get an IP address automatically.

***IP ADDRESS STATIC (ETH) (default: 192.168.90.101)***

Sets the device static address. Careful not to enter devices with the same IP address into the same network.

***IP MASK STATIC (ETH) (default: 255.255.255.0)***

Sets the mask for the IP network.

***GATEWAY ADDRESS STATIC (ETH) (default: 192.168.90.1)***

Sets the gateway address.

***PROTECT CONFIGURATION (default: Disabled)***

Allows you to enable or disable password protection for reading and writing the configuration (including the IP address) using the Seneca Discovery Device software. The password is the same one that allows accessing the web server.



## ATTENTION!

IF THE CONFIGURATION PROTECTION IS ENABLED IT WILL BE IMPOSSIBLE TO READ/WRITE THE CONFIGURATION OF THE DEVICE WITHOUT KNOWING THE PASSWORD.  
IN THE EVENT OF LOSING THE PASSWORD IT WILL BE POSSIBLE TO RETURN THE DEVICE TO THE FACTORY CONFIGURATION (SEE CHAPTER7)

***MODBUS SERVER PORT (ETH) (default: 502)***

Sets the communication port for the Modbus TCP-IP server.

***MODBUS SERVER STATION ADDRESS (ETH) (default: 1)***

Active only if Modbus Passthrough is also active, it sets the station address of the modbus TCP-IP server.



## ATTENTION!

THE MODBUS SERVER WILL ANSWER ANY STATION ADDRESS ONLY IF THE MODBUS PASSTHROUGH MODE IS DISABLED.

***MODBUS PASSTHROUGH (ETH) (default: disabled)***

Sets the conversion mode from Modbus TCP-IP to Modbus RTU serial (see chapter 6).

**MODBUS TCP-IP CONNECTION TIMEOUT [sec] (ETH) (default: 60)**

Sets the TCP-IP connection timeout for the Modbus TCP-IP server and Passthrough modes.

**P2P SERVER PORT (default: 50026)**

Sets the communication port for the P2P server.

**WEB SERVER USERNAME (default: admin)**

Sets the username to access the webserver.

**CONFIGURATION/WEB SERVER PASSWORD (default: admin)**

Sets the password to access the webserver and to read/write the configuration (if enabled).

**WEB SERVER PORT (default: 80)**

Sets the communication port for the web server.

**BAUDRATE MODBUS RTU (SER) (default: 38400 baud)**

Sets the baud rate for the RS485 communication port.

**DATA MODBUS RTU (SER) (default: 8 bit)**

Sets the number of bits for the RS485 communication port.

**PARITY MODBUS RTU (SER) (default: None)**

Sets the parity for the RS485 communication port.

**STOP BIT MODBUS RTU (SER) (default: 1 bit)**

Sets the number of stop bits for the RS485 communication port.

**MODBUS PASSTHROUGH SERIAL TIMEOUT (default: 100ms)**

Active only if Passthrough mode is activated, sets the maximum waiting time before sending a new packet from TCP-IP to the serial port. It must be set according to the longest response time of all the devices present on the RS485 serial port.

**CHANNEL SAMPLE TIME [ms] (default: 100ms)**

Sets the sampling time of each analog input.

## 14.2. ANALOG OUTPUT SETUP SECTION

This section allows the configuration of the analog outputs present in the device.

### **MODE (default Voltage)**

Sets whether the analog output must work in Voltage (range +-10V) or Current (range 0-20mA).

### **WATCHDOG MODE (default Disabled)**

Sets whether the analog output must load the safety value or not in the event of a lack of communication with the Modbus client.

### **WATCHDOG TIMEOUT [s] (default 0)**

Sets the time in seconds within which communication with the Modbus master must take place in order not to trigger the watchdog.

### **WATCHDOG FAIL ENG. VALUE (default 0)**

Sets the engineering value (scaled) that the output must take on in the event of a fail due to the watchdog. It is also the value the analogs assume at startup.

### **START SCALE [mV/uA]**

Represents the start of the electrical scale of the analog output used for the register of the engineering measurement.

### **STOP SCALE [mV/uA]**

Represents the electrical full scale of the analog measurement used for the engineering measurement register.

### **ENG START SCALE**

Represents the value of the engineering register when the output reaches the value shown in the START SCALE parameter.

For example if:

START SCALE = 4mA

STOP SCALE = 20mA

ENG STOP SCALE = -200 metres

ENG START SCALE = 200 metres

By writing the engineering value -200 meters on the Modbus register, the analog output will be worth 4 mA.

By writing the engineering value 0 meters on the Modbus register, the analog output will be worth 12 mA.

By writing the engineering value 200 meters on the Modbus register, the analog output will be worth 20 mA.

**ENG STOP SCALE**

Represents the value of the engineering register when the output reaches the value shown in the STOP SCALE parameter.

For example if:

START SCALE = 4mA

STOP SCALE = 20mA

ENG STOP SCALE = -200 metres

ENG START SCALE = 200 metres

By writing the engineering value -200 meters on the Modbus register, the analog output will be worth 4 mA.

By writing the engineering value 0 meters on the Modbus register, the analog output will be worth 12 mA.

By writing the engineering value 200 meters on the Modbus register, the analog output will be worth 20 mA.

**14.3. DIGITAL I/O SETUP SECTION**

This section allows the configuration of the digital I/Os present in the device.

***DIGITAL I/O MODE (default Input)***

Selects whether the chosen terminal will work as an input, output or output commanded by an event related to the selected analog input.

***DIGITAL INPUT NORMALLY HIGH/LOW (default Normally Low)***

If selected as digital input, it configures whether the input is normally high or low.

***DIGITAL OUTPUT NORMALLY STATE (default Normally Open)***

If selected as digital output, it configures whether the output is normally open or closed.

***DIGITAL OUTPUT WATCHDOG (default Disabled)***

If selected as digital output, it sets the output watchdog mode.

If "Disabled", it disables the watchdog function for the selected output.

If "Enabled on Modbus Communication" the output goes into "Watchdog state" if there has been no generic Modbus communication within the set time.

If "Enabled on Modbus Digital Output Writing" the output goes into "Watchdog state" if there has been no writing of the output within the set time.

***DIGITAL OUTPUT WATCHDOG STATE (default Open)***

Sets the value that the digital output must adopt if the watchdog has been triggered.

***DIGITAL OUTPUT WATCHDOG TIMEOUT [s] (default 100s)***

Represents the watchdog time of the digital output in seconds.

#### 14.4. **SETUP COUNTERS SECTION**

##### **COUNTERS FILTER [ms] (default 0)**

Sets the value in [ms] for filtering all the counters connected to the inputs.

#### 14.5. **P2P CONFIGURATION**

In the P2P Client section it is possible to define which local events to send to one or more remote devices. This way it is possible to send the status of the inputs to the remote outputs and obtain the input-output replication without wiring. It is also possible to send the same input to several outputs simultaneously.

In the P2P Server section it is instead possible to define which inputs must be copied to the outputs.

The "**Disable all rules**" button places all the rules in a disabled status (default).

The "**APPLY**" button allows you to confirm and then save the set rules in the non-volatile memory.

#### 14.6. **ANALOG TEST SECTION**

Here you can control analog outputs through a web page.

#### 14.7. **DIGITAL TEST SECTION**

Here you can read or control digital inputs or outputs through a web page.

#### 14.8. **COUNTERS TEST SECTION**

In this section it is possible to view the value of the counters or set a starting one.

### 15. **CONFIGURATION OF THE R-4RTD-8DIDO DEVICE VIA WEB SERVER**

#### 15.1. **SETUP SECTION**

##### **DHCP (ETH) (default: Disabled)**

Sets the DHCP client to get an IP address automatically.

##### **IP ADDRESS STATIC (ETH) (default: 192.168.90.101)**

Sets the device static address. Careful not to enter devices with the same IP address into the same network.

##### **IP MASK STATIC (ETH) (default: 255.255.255.0)**

Sets the mask for the IP network.

##### **GATEWAY ADDRESS STATIC (ETH) (default: 192.168.90.1)**



Sets the gateway address.

***PROTECT CONFIGURATION (default: Disabled)***

Allows you to enable or disable password protection for reading and writing the configuration (including the IP address) using the Seneca Discovery Device software. The password is the same one that allows accessing the web server.

**ATTENTION!**

**IF THE CONFIGURATION PROTECTION IS ENABLED IT WILL BE IMPOSSIBLE TO READ/WRITE THE CONFIGURATION OF THE DEVICE WITHOUT KNOWING THE PASSWORD.  
IN THE EVENT OF LOSING THE PASSWORD IT WILL BE POSSIBLE TO RETURN THE DEVICE TO THE FACTORY CONFIGURATION (SEE CHAPTER7)**

***MODBUS SERVER PORT (ETH) (default: 502)***

Sets the communication port for the Modbus TCP-IP server.

***MODBUS SERVER STATION ADDRESS (ETH) (default: 1)***

Active only if Modbus Passthrough is also active, it sets the station address of the modbus TCP-IP server.

**ATTENTION!**

**THE MODBUS SERVER WILL ANSWER ANY STATION ADDRESS ONLY IF THE MODBUS PASSTHROUGH MODE IS DISABLED.**

***MODBUS PASSTHROUGH (ETH) (default: disabled)***

Sets the conversion mode from Modbus TCP-IP to Modbus RTU serial (see chapter 6).

***MODBUS TCP-IP CONNECTION TIMEOUT [sec] (ETH) (default: 60)***

Sets the TCP-IP connection timeout for the Modbus TCP-IP server and Passthrough modes.

***P2P SERVER PORT (default: 50026)***

Sets the communication port for the P2P server.

***WEB SERVER USERNAME (default: admin)***

Sets the username to access the webserver.

***CONFIGURATION/WEB SERVER PASSWORD (default: admin)***

Sets the password to access the webserver and to read/write the configuration (if enabled).

***WEB SERVER PORT (default: 80)***

Sets the communication port for the web server.

***BAUDRATE MODBUS RTU (SER) (default: 38400 baud)***

Sets the baud rate for the RS485 communication port.

***DATA MODBUS RTU (SER) (default: 8 bit)***

Sets the number of bits for the RS485 communication port.

***PARITY MODBUS RTU (SER) (default: None)***

Sets the parity for the RS485 communication port.

***STOP BIT MODBUS RTU (SER) (default: 1 bit)***

Sets the number of stop bits for the RS485 communication port.

***MODBUS PASSTHROUGH SERIAL TIMEOUT (default: 100ms)***

Active only if Passthrough mode is activated, sets the maximum waiting time before sending a new packet from TCP-IP to the serial port. It must be set according to the longest response time of all the devices present on the RS485 serial port.

## 15.2. **ANALOG INPUT SETUP SECTION**

This section allows the configuration of the analog inputs present in the device.

### **SENSOR**

Sets the type of sensor connected to the input.

You can choose between the following sensors:

PT100  
Ni100  
PT500  
PT1000  
Cu50  
Cu100  
Ni120  
Ni1000

### **SAMPLING TIME**

Sets the sampling time of the channel (each channel is independent)

### **MOVING FILTER**

Allows you to set whether or not to activate the 10-sample moving average filter.

**CONNECTION WIRES**

Allows you to select the connection type with the sensors of 2, 3 or 4 wires.

**WIRES CONNECTION RESISTANCE [Ohm]**

Allows you to select the possible resistance of connection cables.

**UNIT MEASURE**

Allows you to select the channel unit of measurement among °C, K, °F, Ohm.

**TEMPERATURE OFFSET**

Allows you to set a channel temperature offset.

**FAULT MODE**

Allows you to set the behaviour in case of channel fault between "Last valid Value" or "Fail value"

**FAIL VALUE**

Allows you to set the channel fail value.

**BURN SENSOR DETECT**

Allows you to activate or deactivate the speed detect of sensor burn state.

**BURN SENSOR SAMPLING TIME (s)**

Allows you to set how often the measurement must be taken to detect the channel burn state.

**ANALOG INPUT START SCALE**

Represents the start of the electrical scale of the analog measurement used for the register of the engineering measurement.

**ANALOG INPUT STOP SCALE**

Represents the electrical full scale of the analog measurement used for the engineering measurement register.

**ANALOG INPUT ENG START SCALE**

It represents the value of the engineering measurement register when the input reaches the value shown in the ANALOG INPUT START SCALE parameter.

For example if:

ANALOG INPUT START SCALE = 0 °C

ANALOG INPUT STOP SCALE = 100 °C

ANALOG INPUT ENG STOP SCALE = -200

ANALOG INPUT ENG START SCALE = 200

With a 50 °C input the engineering value will be 0.

***ANALOG INPUT ENG STOP SCALE***

It represents the value of the engineering measurement register when the input reaches the value shown in the ANALOG INPUT STOP SCALE parameter.

For example if:

ANALOG INPUT START SCALE = 0 °C

ANALOG INPUT STOP SCALE = 100 °C

ANALOG INPUT ENG STOP SCALE = -200

ANALOG INPUT ENG START SCALE = 200

With a 50 °C input the engineering value will be 0.

**15.3. ANALOG TEST SECTION**

Here you can view analog inputs values through a web page.

**15.4. DIGITAL I/O SETUP SECTION**

This section allows the configuration of the digital I/Os present in the device.

***DIGITAL I/O MODE (default Input)***

Selects whether the chosen terminal will work as an input, output or output commanded by an event related to the selected analog input.

***DIGITAL INPUT NORMALLY HIGH/LOW (default Normally Low)***

If selected as digital input, it configures whether the input is normally high or low.

***DIGITAL OUTPUT NORMALLY STATE (default Normally Open)***

If selected as digital output, it configures whether the output is normally open or closed.

***DIGITAL OUTPUT WATCHDOG (default Disabled)***

If selected as digital output, it sets the output watchdog mode.

If "Disabled", it disables the watchdog function for the selected output.

If "Enabled on Modbus Communication" the output goes into "Watchdog state" if there has been no generic Modbus communication within the set time.

If "Enabled on Modbus Digital Output Writing" the output goes into "Watchdog state" if there has been no writing of the output within the set time.

***DIGITAL OUTPUT WATCHDOG STATE (default Open)***

Sets the value that the digital output must adopt if the watchdog has been triggered.

***DIGITAL OUTPUT WATCHDOG TIMEOUT [s] (default 100s)***

Represents the watchdog time of the digital output in seconds.

**15.5. SETUP COUNTERS SECTION*****COUNTERS FILTER [ms] (default 0)***

Sets the value in [ms] for filtering all the counters connected to the inputs.

**15.6. DIGITAL IO TEST SECTION**

Here you can read or control digital inputs or outputs through a web page.

**15.7. COUNTERS TEST SECTION**

In this section it is possible to view the value of the digital counters or set a starting one.

**15.8. P2P CONFIGURATION**

In the P2P Client section it is possible to define which local events to send to one or more remote devices. This way it is possible to send the status of the inputs to the remote outputs and obtain the input-output replication without wiring. It is also possible to send the same input to several outputs simultaneously.

In the P2P Server section it is instead possible to define which inputs must be copied to the outputs.

The "**Disable all rules**" button places all the rules in a disabled status (default).

The "**APPLY**" button allows you to confirm and then save the set rules in the non-volatile memory.

## 16. CONFIGURATION OF THE R-2AI-6DIDO DEVICE VIA WEB SERVER

### 16.1. SETUP SECTION

***DHCP (ETH) (default: Disabled)***

Sets the DHCP client to get an IP address automatically.

***IP ADDRESS STATIC (ETH) (default: 192.168.90.101)***

Sets the device static address. Careful not to enter devices with the same IP address into the same network.

***IP MASK STATIC (ETH) (default: 255.255.255.0)***

Sets the mask for the IP network.

***GATEWAY ADDRESS STATIC (ETH) (default: 192.168.90.1)***

Sets the gateway address.

***PROTECT CONFIGURATION (default: Disabled)***

Allows you to enable or disable password protection for reading and writing the configuration (including the IP address) using the Seneca Discovery Device software. The password is the same one that allows accessing the web server.



## ATTENTION!

IF THE CONFIGURATION PROTECTION IS ENABLED IT WILL BE IMPOSSIBLE TO READ/WRITE THE CONFIGURATION OF THE DEVICE WITHOUT KNOWING THE PASSWORD.  
IN THE EVENT OF LOSING THE PASSWORD IT WILL BE POSSIBLE TO RETURN THE DEVICE TO THE FACTORY CONFIGURATION (SEE CHAPTER7)

***MODBUS SERVER PORT (ETH) (default: 502)***

Sets the communication port for the Modbus TCP-IP server.

***MODBUS SERVER STATION ADDRESS (ETH) (default: 1)***

Active only if Modbus Passthrough is also active, it sets the station address of the modbus TCP-IP server.



## ATTENTION!

THE MODBUS SERVER WILL ANSWER ANY STATION ADDRESS ONLY IF THE MODBUS PASSTHROUGH MODE IS DISABLED.

***MODBUS PASSTHROUGH (ETH) (default: disabled)***

Sets the conversion mode from Modbus TCP-IP to Modbus RTU serial (see chapter 6).

**MODBUS TCP-IP CONNECTION TIMEOUT [sec] (ETH) (default: 60)**

Sets the TCP-IP connection timeout for the Modbus TCP-IP server and Passthrough modes.

**P2P SERVER PORT (default: 50026)**

Sets the communication port for the P2P server.

**WEB SERVER USERNAME (default: admin)**

Sets the username to access the webserver.

**CONFIGURATION/WEB SERVER PASSWORD (default: admin)**

Sets the password to access the webserver and to read/write the configuration (if enabled).

**WEB SERVER PORT (default: 80)**

Sets the communication port for the web server.

**BAUDRATE MODBUS RTU (SER) (default: 38400 baud)**

Sets the baud rate for the RS485 communication port.

**DATA MODBUS RTU (SER) (default: 8 bit)**

Sets the number of bits for the RS485 communication port.

**PARITY MODBUS RTU (SER) (default: None)**

Sets the parity for the RS485 communication port.

**STOP BIT MODBUS RTU (SER) (default: 1 bit)**

Sets the number of stop bits for the RS485 communication port.

**MODBUS PASSTHROUGH SERIAL TIMEOUT (default: 100ms)**

Active only if Passthrough mode is activated, sets the maximum waiting time before sending a new packet from TCP-IP to the serial port. It must be set according to the longest response time of all the devices present on the RS485 serial port.

**CHANNEL SAMPLE TIME [ms] (default: 100ms)**

Sets the sampling time of each analog input.

## 16.2. SETUP AIN 1. 8 SECTION

This section allows the configuration of the analog inputs present in the device.



### ATTENTION!

THE DEVICE CAN DETECT THE COLD JOINT TEMPERATURE FROM THE INTERNAL SENSORS OR FROM ANALOG INPUT 1 (THROUGH EXTERNAL PT100-TYPE SENSOR).  
IN THIS CASE ALL THE DETECTIONS OF THE INTERNAL SENSORS WILL BE REPLACED BY THE READING OF ANALOG INPUT 1.

#### ***ANALOG INPUT MODE (default +-30V)***

Set the type of measurement for the selected input.

It is possible to choose between the following types of input:

+ -30V

+ -24 mA

#### ***ANALOG INPUT FILTER [samples] (default 0)***

Allows you to set the moving average filter with the selected number of samples. If the value is "0" the filter is disabled.

#### ***ANALOG INPUT START SCALE***

Represents the start of the electrical scale of the analog measurement used for the register of the engineering measurement.

#### ***ANALOG INPUT STOP SCALE***

Represents the electrical full scale of the analog measurement used for the engineering measurement register.

#### ***ANALOG INPUT ENG START SCALE***

It represents the value of the engineering measurement register when the input reaches the value shown in the ANALOG INPUT START SCALE parameter.

For example if:

ANALOG INPUT START SCALE = 4mA

ANALOG INPUT STOP SCALE = 20mA

ANALOG INPUT ENG STOP SCALE = -200 metres

ANALOG INPUT ENG START SCALE = 200 metres

With a 12 mA input the engineering value will be 0 metres.

#### ***ANALOG INPUT ENG STOP SCALE***



It represents the value of the engineering measurement register when the input reaches the value shown in the ANALOG INPUT STOP SCALE parameter.

For example if:

ANALOG INPUT START SCALE = 4mA

ANALOG INPUT STOP SCALE = 20mA

ANALOG INPUT ENG STOP SCALE = -200 metres

ANALOG INPUT ENG START SCALE = 200 metres

With a 12 mA input the engineering value will be 0 metres.

### 16.3. **DIGITAL I/O SETUP SECTION**

This section allows the configuration of the digital I/Os present in the device.

#### ***DIGITAL I/O MODE (default Input)***

Selects whether the chosen terminal will work as an input, output or output commanded by an event related to the selected analog input.

#### ***DIGITAL INPUT NORMALLY HIGH/LOW (default Normally Low)***

If selected as digital input, it configures whether the input is normally high or low.

#### ***DIGITAL OUTPUT NORMALLY STATE (default Normally Open)***

If selected as digital output, it configures whether the output is normally open or closed.

#### ***DIGITAL OUTPUT WATCHDOG (default Disabled)***

If selected as digital output, it sets the output watchdog mode.

If "Disabled", it disables the watchdog function for the selected output.

If "Enabled on Modbus Communication" the output goes into "Watchdog state" if there has been no generic Modbus communication within the set time.

If "Enabled on Modbus Digital Output Writing" the output goes into "Watchdog state" if there has been no writing of the output within the set time.

#### ***DIGITAL OUTPUT WATCHDOG STATE (default Open)***

Sets the value that the digital output must adopt if the watchdog has been triggered.

#### ***DIGITAL OUTPUT WATCHDOG TIMEOUT [s] (default 100s)***

Represents the watchdog time of the digital output in seconds.

### 16.4. **SETUP COUNTERS SECTION**

#### ***COUNTERS FILTER [ms] (default 0)***

Sets the value in [ms] for filtering all the counters connected to the inputs.

#### 16.5. **DIGITAL IO TEST SECTION**

Here you can read or control digital inputs or outputs through a web page.

#### 16.6. **COUNTERS TEST SECTION**

In this section it is possible to view the value of the digital counters or set a starting one.

#### 16.7. **P2P CONFIGURATION**

In the P2P Client section it is possible to define which local events to send to one or more remote devices. This way it is possible to send the status of the inputs to the remote outputs and obtain the input-output replication without wiring. It is also possible to send the same input to several outputs simultaneously.

In the P2P Server section it is instead possible to define which inputs must be copied to the outputs.

The "**Disable all rules**" button places all the rules in a disabled status (default).

The "**APPLY**" button allows you to confirm and then save the set rules in the non-volatile memory.

## 17. P2P CLIENT

Status  
Setup  
Setup2  
Input Test  
Output Test  
P2P Client  
P2P Server

**P2P Client Page Rules:** *send Local event to remote server*

disable all rules
automatic configuration
APPLY

| En.    | Rule Nr. | Loc.Ch. | Remote.Ip<br>Use<br>255.255.255.255<br>for send<br>to all devices | Rem.Port | En.          | Tick<br>(mS) |
|--------|----------|---------|-------------------------------------------------------------------|----------|--------------|--------------|
| Dis. ▼ | 1        | Di_1 ▼  | 255.255.255.255                                                   | 50026    | Only Timed ▼ | 1000         |
| Dis. ▼ | 2        | Di_1 ▼  | 255.255.255.255                                                   | 50026    | Only Timed ▼ | 1000         |
| Dis. ▼ | 3        | Di_1 ▼  | 255.255.255.255                                                   | 50026    | Only Timed ▼ | 1000         |
| Dis. ▼ | 4        | Di_1 ▼  | 255.255.255.255                                                   | 50026    | Only Timed ▼ | 1000         |
| Dis. ▼ | 5        | Di_1 ▼  | 255.255.255.255                                                   | 50026    | Only Timed ▼ | 1000         |
| Dis. ▼ | 6        | Di_1 ▼  | 255.255.255.255                                                   | 50026    | Only Timed ▼ | 1000         |
| Dis. ▼ | 7        | Di_1 ▼  | 255.255.255.255                                                   | 50026    | Only Timed ▼ | 1000         |
| Dis. ▼ | 8        | Di_1 ▼  | 255.255.255.255                                                   | 50026    | Only Timed ▼ | 1000         |
| Dis. ▼ | 9        | Di_1 ▼  | 255.255.255.255                                                   | 50026    | Only Timed ▼ | 1000         |
| Dis. ▼ | 10       | Di_1 ▼  | 255.255.255.255                                                   | 50026    | Only Timed ▼ | 1000         |
| Dis. ▼ | 11       | Di_1 ▼  | 255.255.255.255                                                   | 50026    | Only Timed ▼ | 1000         |
| Dis. ▼ | 12       | Di_1 ▼  | 255.255.255.255                                                   | 50026    | Only Timed ▼ | 1000         |
| Dis. ▼ | 13       | Di_1 ▼  | 255.255.255.255                                                   | 50026    | Only Timed ▼ | 1000         |
| Dis. ▼ | 14       | Di_1 ▼  | 255.255.255.255                                                   | 50026    | Only Timed ▼ | 1000         |
| Dis. ▼ | 15       | Di_1 ▼  | 255.255.255.255                                                   | 50026    | Only Timed ▼ | 1000         |
| Dis. ▼ | 16       | Di_1 ▼  | 255.255.255.255                                                   | 50026    | Only Timed ▼ | 1000         |
| Dis. ▼ | 17       | Di_1 ▼  | 255.255.255.255                                                   | 50026    | Only Timed ▼ | 1000         |
| Dis. ▼ | 18       | Di_1 ▼  | 255.255.255.255                                                   | 50026    | Only Timed ▼ | 1000         |
| Dis. ▼ | 19       | Di_1 ▼  | 255.255.255.255                                                   | 50026    | Only Timed ▼ | 1000         |
| Dis. ▼ | 20       | Di_1 ▼  | 255.255.255.255                                                   | 50026    | Only Timed ▼ | 1000         |
| Dis. ▼ | 21       | Di_1 ▼  | 255.255.255.255                                                   | 50026    | Only Timed ▼ | 1000         |
| Dis. ▼ | 22       | Di_1 ▼  | 255.255.255.255                                                   | 50026    | Only Timed ▼ | 1000         |
| Dis. ▼ | 23       | Di_1 ▼  | 255.255.255.255                                                   | 50026    | Only Timed ▼ | 1000         |
| Dis. ▼ | 24       | Di_1 ▼  | 255.255.255.255                                                   | 50026    | Only Timed ▼ | 1000         |
| Dis. ▼ | 25       | Di_1 ▼  | 255.255.255.255                                                   | 50026    | Only Timed ▼ | 1000         |
| Dis. ▼ | 26       | Di_1 ▼  | 255.255.255.255                                                   | 50026    | Only Timed ▼ | 1000         |
| Dis. ▼ | 27       | Di_1 ▼  | 255.255.255.255                                                   | 50026    | Only Timed ▼ | 1000         |
| Dis. ▼ | 28       | Di_1 ▼  | 255.255.255.255                                                   | 50026    | Only Timed ▼ | 1000         |
| Dis. ▼ | 29       | Di_1 ▼  | 255.255.255.255                                                   | 50026    | Only Timed ▼ | 1000         |
| Dis. ▼ | 30       | Di_1 ▼  | 255.255.255.255                                                   | 50026    | Only Timed ▼ | 1000         |
| Dis. ▼ | 31       | Di_1 ▼  | 255.255.255.255                                                   | 50026    | Only Timed ▼ | 1000         |
| Dis. ▼ | 32       | Di_1 ▼  | 255.255.255.255                                                   | 50026    | Only Timed ▼ | 1000         |

The "**Automatic configuration**" button allows you to prepare the rules for sending all the inputs available in the device in use.

### En.

Selects whether the copy rule is active or not.

### Loc. Ch.

Selects the status of which channel should be sent to the remote device(s).

### Remote IP

Selects the IP address of the remote device to which the status of that input channel is to be sent.

If the channel has to be sent simultaneously to all the devices (broadcast), enter the broadcast address (255.255.255.255) as the IP address.

### Remote Port

Selects the communication port for sending the status of the inputs. It must coincide with the **P2P SERVER PORT** parameter of the remote device.

***En***

Selects operation in "Only Timed" or "Timed+Event" mode.

In "Only Timed" mode, the status of the inputs is sent on each "tick [ms]" and then refreshed continuously (cyclic sending).

In the "Timed+Event" mode, the status of the inputs is sent to a digital event (change of status).

***Tick [ms]***

Sets the cyclical sending time of the input status.

 **ATTENTION!**

**IN CASE OF ENABLED WATCHDOG OF DIGITAL OUTPUTS THE RULE'S TICK TIME MUST BE LOWER THAN THE WATCHDOG TIMEOUT SET**

 **ATTENTION!**

***IT IS ALSO POSSIBLE TO COPY SOME I/O OF THE SAME DEVICE (FOR EXAMPLE, COPY THE I01 INPUT TO D01) BY ENTERING THE IP OF THE DEVICE AS REMOTE IP***

## 18. P2P SERVER

Status

Setup

Setup2

Input Test

Output Test

P2P Client

P2P Server

P2P Server Page Rules: receive Remote event from client

disable all rules

automatic configuration

APPLY

| En.    | Rule Nr. | Rem.Ch. | Remote Ip Use<br>255.255.255.255<br>for receive<br>from all devices | Loc.Ch. |
|--------|----------|---------|---------------------------------------------------------------------|---------|
| Ena. ▼ | 1        | Di_1 ▼  | 255.255.255.255                                                     | Do_1 ▼  |
| Ena. ▼ | 2        | Di_2 ▼  | 255.255.255.255                                                     | Do_2 ▼  |
| Ena. ▼ | 3        | Di_3 ▼  | 255.255.255.255                                                     | Do_3 ▼  |
| Ena. ▼ | 4        | Di_4 ▼  | 255.255.255.255                                                     | Do_4 ▼  |
| Ena. ▼ | 5        | Di_5 ▼  | 255.255.255.255                                                     | Do_5 ▼  |
| Ena. ▼ | 6        | Di_6 ▼  | 255.255.255.255                                                     | Do_6 ▼  |
| Ena. ▼ | 7        | Di_7 ▼  | 255.255.255.255                                                     | Do_7 ▼  |
| Ena. ▼ | 8        | Di_8 ▼  | 255.255.255.255                                                     | Do_8 ▼  |
| Dis. ▼ | 9        | Di_1 ▼  | 255.255.255.255                                                     | Do_1 ▼  |
| Dis. ▼ | 10       | Di_1 ▼  | 255.255.255.255                                                     | Do_1 ▼  |
| Dis. ▼ | 11       | Di_1 ▼  | 255.255.255.255                                                     | Do_1 ▼  |
| Dis. ▼ | 12       | Di_1 ▼  | 255.255.255.255                                                     | Do_1 ▼  |
| Dis. ▼ | 13       | Di_1 ▼  | 255.255.255.255                                                     | Do_1 ▼  |
| Dis. ▼ | 14       | Di_1 ▼  | 255.255.255.255                                                     | Do_1 ▼  |
| Dis. ▼ | 15       | Di_1 ▼  | 255.255.255.255                                                     | Do_1 ▼  |
| Dis. ▼ | 16       | Di_1 ▼  | 255.255.255.255                                                     | Do_1 ▼  |
| Dis. ▼ | 17       | Di_1 ▼  | 255.255.255.255                                                     | Do_1 ▼  |
| Dis. ▼ | 18       | Di_1 ▼  | 255.255.255.255                                                     | Do_1 ▼  |
| Dis. ▼ | 19       | Di_1 ▼  | 255.255.255.255                                                     | Do_1 ▼  |
| Dis. ▼ | 20       | Di_1 ▼  | 255.255.255.255                                                     | Do_1 ▼  |
| Dis. ▼ | 21       | Di_1 ▼  | 255.255.255.255                                                     | Do_1 ▼  |
| Dis. ▼ | 22       | Di_1 ▼  | 255.255.255.255                                                     | Do_1 ▼  |
| Dis. ▼ | 23       | Di_1 ▼  | 255.255.255.255                                                     | Do_1 ▼  |
| Dis. ▼ | 24       | Di_1 ▼  | 255.255.255.255                                                     | Do_1 ▼  |
| Dis. ▼ | 25       | Di_1 ▼  | 255.255.255.255                                                     | Do_1 ▼  |
| Dis. ▼ | 26       | Di_1 ▼  | 255.255.255.255                                                     | Do_1 ▼  |
| Dis. ▼ | 27       | Di_1 ▼  | 255.255.255.255                                                     | Do_1 ▼  |
| Dis. ▼ | 28       | Di_1 ▼  | 255.255.255.255                                                     | Do_1 ▼  |
| Dis. ▼ | 29       | Di_1 ▼  | 255.255.255.255                                                     | Do_1 ▼  |
| Dis. ▼ | 30       | Di_1 ▼  | 255.255.255.255                                                     | Do_1 ▼  |
| Dis. ▼ | 31       | Di_1 ▼  | 255.255.255.255                                                     | Do_1 ▼  |
| Dis. ▼ | 32       | Di_1 ▼  | 255.255.255.255                                                     | Do_1 ▼  |

The "**Automatic configuration**" button allows you to prepare the rules to receive all the inputs on the outputs of the device in use.

### En.

Selects whether the copy rule is active or not.

### Rem. Ch.

Selects the status of which remote channel should be received by the local device.

### Remote IP

Selects the IP address of the remote device from which to receive the input status.

If the channel must be received simultaneously by all the devices (broadcast), enter the broadcast address (255.255.255.255) as the IP address.

### Loc. Ch.

Selects the copy destination of the remote input value.

## **ATTENTION!**

**IT IS ALSO POSSIBLE TO COPY SOME I/O OF THE SAME DEVICE (FOR EXAMPLE, COPY THE I01 INPUT TO D01) BY ENTERING THE IP OF THE DEVICE AS REMOTE IP. HOWEVER, THE ETHERNET PORT MUST BE CORRECTLY CONNECTED.**

### 18.1. P2P CONFIGURATION EXAMPLE

In the following example we have No.2 devices and we want to copy the status of digital input 1 of the first to the digital output of the second.

The IP address of Device 1 is 192.168.1.10

The IP address of Device 2 is 192.168.1.11

Let's move to device 1 with IP address 192.168.1.10 and select the sending of digital input 1 to the remote address 192.168.1.11 of device 2 this way:

#### DEVICE 1

| En.    | Rule Nr. | Loc.Ch. | Remote.Ip<br>Use<br>255.255.255.255<br>for send<br>to all devices | Rem.Port | En.           | Tick<br>(mS) |
|--------|----------|---------|-------------------------------------------------------------------|----------|---------------|--------------|
| Ena. ▼ | 1        | Di_1 ▼  | 192.168.1.11                                                      | 50026    | Timed+Event ▼ | 1000         |

Now let's move on to device 2 and first configure the P2P server communication port on 50026:

Status  
Setup  
Setup2  
Input Test  
Output Test  
P2P Client  
P2P Server

### Setup page(1/2):

|                                            | CURRENT | UPDATED   |
|--------------------------------------------|---------|-----------|
| DHCP (ETH)                                 | Enabled | Enabled ▼ |
| DISCOVERY PROTOCOL(ETH)                    | Enabled | Enabled ▼ |
| MODBUS SERVER PORT (ETH)                   | 502     | 502       |
| MODBUS SERVER STATION ADDRESS (ETH)        | 20      | 20        |
| MODBUS PASSTHROUGH (ETH)                   | Enabled | Enabled ▼ |
| MODBUS SERVER/PASSTHROUGH T.OUT(sec) (ETH) | 60      | 60        |
| P2P SERVER PORT (ETH)                      | 50026   | 50026     |
| WEBSERVER USER NAME                        | admin   | admin     |

And we now configure the P2P server, the channel to be received from 192.168.1.10 is Di\_1 and must be copied to Do\_1:

#### DEVICE 2

| En.    | Rule Nr. | Rem.Ch. | Remote.Ip<br>Use<br>255.255.255.255<br>for receive<br>from all devices | Loc.Ch. |
|--------|----------|---------|------------------------------------------------------------------------|---------|
| Ena. ▼ | 1        | Di_1 ▼  | 192.168.1.10                                                           | Do_1 ▼  |

With this configuration, each time digital input 1 of device 1 (192.168.1.10) changes status, a packet will be sent to device 2 (192.168.1.11) which will copy it to digital output 1.

After 1 second, the same packet will be sent cyclically.

## 18.2. P2P EXECUTION TIME

The switching time depends on the client device model and the server device model in addition to the congestion of the ethernet network.

For example, for the R-16DI8DO model, the switching time of the remote digital output as a response to an incoming event into another R-16DI8DO is about 20 ms (daisy chain connection of 2 devices, 1 set rule).

As regards the analog models, the refresh time of the digital inputs/outputs and analog inputs typical of the device must also be considered.

## 19. UPDATING THE FIRMWARE AND SAVING/OPENING A CONFIGURATION

The firmware update can be performed via the web server in the appropriate section.

Via the web server it is possible to save or open a saved configuration.



# ATTENTION!

**NOT TO DAMAGE THE DEVICE DO NOT REMOVE THE POWER SUPPLY DURING THE FIRMWARE UPDATE OPERATION.**

## 20. MODBUS RTU/ MODBUS TCP-IP REGISTERS

The following abbreviations are used in the register tables:

|                 |                                                                                                                                                            |
|-----------------|------------------------------------------------------------------------------------------------------------------------------------------------------------|
| MS              | Most Significant                                                                                                                                           |
| LS              | Least Significant                                                                                                                                          |
| MSBIT           | Most Significant Bit                                                                                                                                       |
| LSBIT           | Least Significant Bit                                                                                                                                      |
| MMSW            | "Most" Most Significant Word (16bit )                                                                                                                      |
| MSW             | Most Significant Word (16bit )                                                                                                                             |
| LSW             | Least Significant Word (16bit)                                                                                                                             |
| LLSW            | "Least" Least Significant Word (16bit)                                                                                                                     |
| RO              | Read Only                                                                                                                                                  |
| RW              | Register in RAM or Fe-RAM Writable infinite times.                                                                                                         |
| RW*             | Flash Read-Write: REGISTERS CONTAINED IN THE FLASH MEMORY: WRITABLE AT THE MAXIMUM ABOUT 10000 TIMES.                                                      |
| UNSIGNED 16 BIT | Unsigned integer register that can take values from 0 to 65535                                                                                             |
| SIGNED 16 BIT   | Signed integer register that can take values from -32768 to +32767                                                                                         |
| UNSIGNED 32 BIT | Unsigned integer register that can take values from 0 to +4294967296                                                                                       |
| SIGNED 32 BIT   | Signed integer register that can take values from -2147483648 to 2147483647                                                                                |
| UNSIGNED 64 BIT | Unsigned integer register that can take values from 0 to 18.446.744.073.709.551.615                                                                        |
| SIGNED 64 BIT   | Signed integer register that can take values from -2 <sup>63</sup> to 2 <sup>63</sup> -1                                                                   |
| FLOAT 32 BIT    | Single-precision, 32-bit floating point register (IEEE 754)<br><a href="https://en.wikipedia.org/wiki/IEEE_754">https://en.wikipedia.org/wiki/IEEE_754</a> |
| BIT             | Boolean register, which can take values 0 (false) or 1 (true)                                                                                              |



## 20.1. NUMBERING OF "0-BASED" OR "1-BASED" MODBUS ADDRESSES

According to the Modbus standard the Holding Registers are addressable from 0 to 65535, there are 2 different conventions for numbering the addresses: "0-BASED" and "1-BASED".

For greater clarity, Seneca shows its register tables in both conventions.



# ATTENTION!

**CAREFULLY READ THE DOCUMENTATION OF THE MODBUS MASTER DEVICE IN ORDER TO UNDERSTAND WHICH OF THE TWO CONVENTIONS THE MANUFACTURER HAS DECIDED TO USE**

## 20.2. NUMBERING OF MODBUS ADDRESSES WITH "0-BASED" CONVENTION

The numbering is:

| <b>HOLDING REGISTER MODBUS ADDRESS (OFFSET)</b> | <b>MEANING</b>  |
|-------------------------------------------------|-----------------|
| 0                                               | FIRST REGISTER  |
| 1                                               | SECOND REGISTER |
| 2                                               | THIRD REGISTER  |
| 3                                               | FOURTH REGISTER |
| 4                                               | FIFTH REGISTER  |

Therefore, the first register is at address 0.

In the following tables, this convention is indicated with **"ADDRESS OFFSET"**.

## 20.3. NUMBERING OF MODBUS ADDRESSES WITH "1 BASED" CONVENTION (STANDARD)

The numbering is that established by the Modbus consortium and is of the type:

| <b>HOLDING REGISTER MODBUS ADDRESS 4x</b> | <b>MEANING</b>  |
|-------------------------------------------|-----------------|
| 40001                                     | FIRST REGISTER  |
| 40002                                     | SECOND REGISTER |
| 40003                                     | THIRD REGISTER  |
| 40004                                     | FOURTH REGISTER |
| 40005                                     | FIFTH REGISTER  |

In the following tables this convention is indicated with **"ADDRESS 4x"** since a 4 is added to the address so that the first Modbus register is 40001.

A further convention is also possible where the number 4 is omitted in front of the register address:

| <b>HOLDING MODBUS ADDRESS<br/>WITHOUT 4x</b> | <b>MEANING</b>  |
|----------------------------------------------|-----------------|
| 1                                            | FIRST REGISTER  |
| 2                                            | SECOND REGISTER |
| 3                                            | THIRD REGISTER  |
| 4                                            | FOURTH REGISTER |
| 5                                            | FIFTH REGISTER  |

#### 20.4. BIT CONVENTION WITHIN A MODBUS HOLDING REGISTER

A Modbus Holding Register consists of 16 bits with the following convention:

|           |           |           |           |           |           |          |          |          |          |          |          |          |          |          |          |
|-----------|-----------|-----------|-----------|-----------|-----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|
| BIT<br>15 | BIT<br>14 | BIT<br>13 | BIT<br>12 | BIT<br>11 | BIT<br>10 | BIT<br>9 | BIT<br>8 | BIT<br>7 | BIT<br>6 | BIT<br>5 | BIT<br>4 | BIT<br>3 | BIT<br>2 | BIT<br>1 | BIT<br>0 |
|-----------|-----------|-----------|-----------|-----------|-----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|

For instance, if the value of the register in decimal is

12300

the value 12300 in hexadecimal is:

0x300C

the hexadecimal 0x300C in binary value is:

11 0000 0000 1100

So, using the above convention, we get:

|           |           |           |           |           |           |          |          |          |          |          |          |          |          |          |          |
|-----------|-----------|-----------|-----------|-----------|-----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|
| BIT<br>15 | BIT<br>14 | BIT<br>13 | BIT<br>12 | BIT<br>11 | BIT<br>10 | BIT<br>9 | BIT<br>8 | BIT<br>7 | BIT<br>6 | BIT<br>5 | BIT<br>4 | BIT<br>3 | BIT<br>2 | BIT<br>1 | BIT<br>0 |
| 0         | 0         | 1         | 1         | 0         | 0         | 0        | 0        | 0        | 0        | 0        | 0        | 1        | 1        | 0        | 0        |

#### 20.5. MSB and LSB BYTE CONVENTION WITHIN A MODBUS HOLDING REGISTER

A Modbus Holding Register consists of 16 bits with the following convention:

|           |           |           |           |           |           |          |          |          |          |          |          |          |          |          |          |
|-----------|-----------|-----------|-----------|-----------|-----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|
| BIT<br>15 | BIT<br>14 | BIT<br>13 | BIT<br>12 | BIT<br>11 | BIT<br>10 | BIT<br>9 | BIT<br>8 | BIT<br>7 | BIT<br>6 | BIT<br>5 | BIT<br>4 | BIT<br>3 | BIT<br>2 | BIT<br>1 | BIT<br>0 |
|-----------|-----------|-----------|-----------|-----------|-----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|

LSB Byte (Least Significant Byte) defines the 8 bits ranging from Bit 0 to Bit 7 included, we define MSB Byte (Most Significant Byte) the 8 bits ranging from Bit 8 to Bit 15 inclusive:

|           |           |           |           |           |           |          |          |          |          |          |          |          |          |          |          |
|-----------|-----------|-----------|-----------|-----------|-----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|
| BIT<br>15 | BIT<br>14 | BIT<br>13 | BIT<br>12 | BIT<br>11 | BIT<br>10 | BIT<br>9 | BIT<br>8 | BIT<br>7 | BIT<br>6 | BIT<br>5 | BIT<br>4 | BIT<br>3 | BIT<br>2 | BIT<br>1 | BIT<br>0 |
| BYTE MSB  |           |           |           |           |           |          |          | BYTE LSB |          |          |          |          |          |          |          |

## 20.6. REPRESENTATION OF A 32-BIT VALUE IN TWO CONSECUTIVE MODBUS HOLDING REGISTERS

The representation of a 32-bit value in the Modbus Holding Registers is made using 2 consecutive Holding Registers (a Holding Register is a 16-bit register). To obtain the 32-bit value it is therefore necessary to read two consecutive registers:

For example, if register 40064 contains the 16 most significant bits (MSW) while register 40065 contains the least significant 16 bits (LSW), the 32-bit value is obtained by composing the 2 registers:

|                             |           |           |           |           |           |          |          |          |          |          |          |          |          |          |          |
|-----------------------------|-----------|-----------|-----------|-----------|-----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|
| BIT<br>15                   | BIT<br>14 | BIT<br>13 | BIT<br>12 | BIT<br>11 | BIT<br>10 | BIT<br>9 | BIT<br>8 | BIT<br>7 | BIT<br>6 | BIT<br>5 | BIT<br>4 | BIT<br>3 | BIT<br>2 | BIT<br>1 | BIT<br>0 |
| 40064 MOST SIGNIFICANT WORD |           |           |           |           |           |          |          |          |          |          |          |          |          |          |          |

|                              |           |           |           |           |           |          |          |          |          |          |          |          |          |          |          |
|------------------------------|-----------|-----------|-----------|-----------|-----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|
| BIT<br>15                    | BIT<br>14 | BIT<br>13 | BIT<br>12 | BIT<br>11 | BIT<br>10 | BIT<br>9 | BIT<br>8 | BIT<br>7 | BIT<br>6 | BIT<br>5 | BIT<br>4 | BIT<br>3 | BIT<br>2 | BIT<br>1 | BIT<br>0 |
| 40065 LEAST SIGNIFICANT WORD |           |           |           |           |           |          |          |          |          |          |          |          |          |          |          |

$$Value_{32bit} = Register_{LSW} + (Register_{MSW} * 65536)$$

In the reading registers it is possible to swap the most significant word with the least significant word, therefore it is possible to obtain 40064 as LSW and 40065 as MSW.

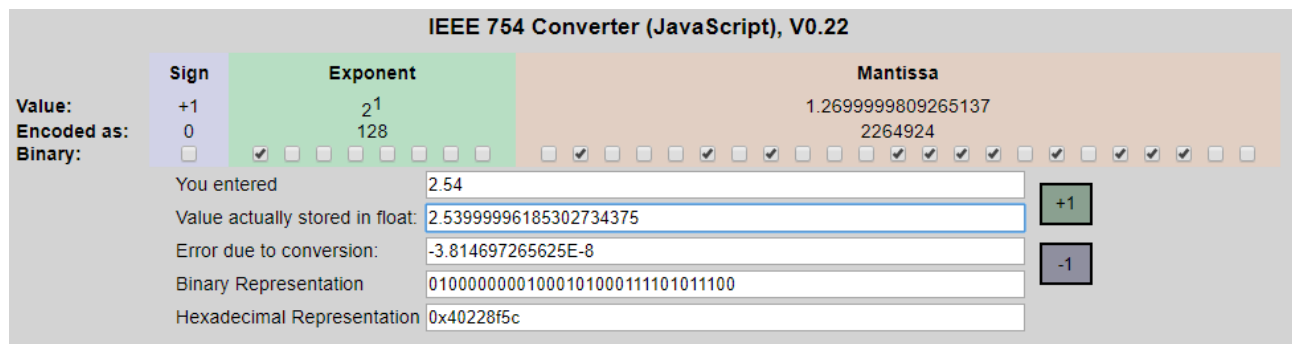
## 20.7. TYPE OF 32-BIT FLOATING POINT DATA (IEEE 754)

The IEEE 754 standard ([https://en.wikipedia.org/wiki/IEEE\\_754](https://en.wikipedia.org/wiki/IEEE_754)) defines the format for representing floating point numbers.

As already mentioned, since it is a 32-bit data type, its representation occupies two 16-bit holding registers.

To obtain a binary/hexadecimal conversion of a floating point value it is possible to refer to an online converter at this address:

<http://www.h-schmidt.net/FloatConverter/IEEE754.html>



Using the last representation the value 2.54 is represented at 32 bits as:

0x40228F5C

Since we have 16-bit registers available, the value must be divided into MSW and LSW:

0x4022 (16418 decimal) are the 16 most significant bits (MSW) while 0x8F5C (36700 decimal) are the 16 least significant bits (LSW).

## 20.8. **SUPPORTED MODBUS COMMUNICATION PROTOCOLS**

The Modbus communication protocols supported are:

- Modbus RTU Slave (from the RS485 port)
- Modbus TCP-IP Server (from Ethernet ports) 8 clients max

## 20.9. **SUPPORTED MODBUS FUNCTION CODES**

The following Modbus functions are supported:

- Read Holding Register (function 3)
- Read Coil Status (function 1)
- Write Coil (function 5)
- Write Multiple Coil (function 15)
- Write Single Register (function 6)
- Write Multiple Registers (function 16)



### **ATTENTION!**

**All 32-bit values are contained in 2 consecutive registers**



### **ATTENTION!**

**Any registers with RW\* (in flash memory) can be written up to 10000 times  
The PLC/Master Modbus programmer must not exceed this limit**

## 21. MODBUS REGISTER TABLE FOR THE R-32DIDO PRODUCT

### 21.1. R-32DIDO: MODBUS 4X HOLDING REGISTERS TABLE (FUNCTION CODE 3)

| ADDRESS<br>(4x) | OFFSET<br>(4x) | REGISTER                       | CHANNEL | DESCRIPTION                          | W/R | TYPE               |
|-----------------|----------------|--------------------------------|---------|--------------------------------------|-----|--------------------|
| 40001           | 0              | MACHINE-ID                     | -       | Device identification                | RO  | UNSIGNED<br>16 BIT |
| 40002           | 1              | FW REVISION<br>(Maior/Minor)   | -       | Fw Revision                          | RO  | UNSIGNED<br>16 BIT |
| 40003           | 2              | FW REVISION<br>(Fix/Build)     | -       | Fw Revision                          | RO  | UNSIGNED<br>16 BIT |
| 40004           | 3              | FW CODE                        | -       | Fw Code                              | RO  | UNSIGNED<br>16 BIT |
| 40005           | 4              | RESERVED                       | -       | -                                    | RO  | UNSIGNED<br>16 BIT |
| 40006           | 5              | RESERVED                       | -       | -                                    | RO  | UNSIGNED<br>16 BIT |
| 40007           | 6              | BOARD-ID                       | -       | Hw Revision                          | RO  | UNSIGNED<br>16 BIT |
| 40008           | 7              | BOOT REVISION<br>(Maior/Minor) | -       | Bootloader Revision                  | RO  | UNSIGNED<br>16 BIT |
| 40009           | 8              | BOOT REVISION<br>(Fix/Build)   | -       | Bootloader Revision                  | RO  | UNSIGNED<br>16 BIT |
| 40010           | 9              | RESERVED                       | -       | -                                    | RO  | UNSIGNED<br>16 BIT |
| 40011           | 10             | RESERVED                       | -       | -                                    | RO  | UNSIGNED<br>16 BIT |
| 40012           | 11             | RESERVED                       | -       | -                                    | RO  | UNSIGNED<br>16 BIT |
| 40013           | 12             | COMMAND_AUX<br>_3H             | -       | Aux Command Register                 | RW  | UNSIGNED<br>16 BIT |
| 40014           | 13             | COMMAND_AUX<br>_3L             | -       | Aux Command Register                 | RW  | UNSIGNED<br>16 BIT |
| 40015           | 14             | COMMAND_AUX<br>_2              | -       | Aux Command Register                 | RW  | UNSIGNED<br>16 BIT |
| 40016           | 15             | COMMAND_AUX<br>_1              | -       | Aux Command Register                 | RW  | UNSIGNED<br>16 BIT |
| 40017           | 16             | COMMAND                        | -       | Aux Command Register                 | RW  | UNSIGNED<br>16 BIT |
| 40018           | 17             | STATUS                         | -       | Device Status                        | RW  | UNSIGNED<br>16 BIT |
| 40019           | 18             | RESERVED                       | -       | -                                    | RW  | UNSIGNED<br>16 BIT |
| 40020           | 19             | RESERVED                       | -       | -                                    | RW  | UNSIGNED<br>16 BIT |
| 40021           | 20             | DIGITAL I/O                    | 16..1   | Digital IO Value [Channel<br>16...1] | RW  | UNSIGNED<br>16 BIT |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>(4x)</b> | <b>REGISTER</b> | <b>CHANNEL</b> | <b>DESCRIPTION</b>                 | <b>W/R</b> | <b>TYPE</b>        |
|-------------------------|------------------------|-----------------|----------------|------------------------------------|------------|--------------------|
| <b>40022</b>            | 21                     | DIGITAL I/O     | 32..17         | Digital IO Value [Channel 32...17] | RW         | UNSIGNED<br>16 BIT |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>(4x)</b> | <b>REGISTER</b>    | <b>CHANNEL</b> | <b>DESCRIPTION</b>       | <b>W/R</b> | <b>TYPE</b>        |
|-------------------------|------------------------|--------------------|----------------|--------------------------|------------|--------------------|
| <b>40101</b>            | 100                    | COUNTER MSW<br>DIN | 1              | CHANNEL COUNTER<br>VALUE | RW         | UNSIGNED<br>32 BIT |
| <b>40102</b>            | 101                    | COUNTER LSW<br>DIN |                |                          | RW         |                    |
| <b>40103</b>            | 102                    | COUNTER MSW<br>DIN | 2              | CHANNEL COUNTER<br>VALUE | RW         | UNSIGNED<br>32 BIT |
| <b>40104</b>            | 103                    | COUNTER LSW<br>DIN |                |                          | RW         |                    |
| <b>40105</b>            | 104                    | COUNTER MSW<br>DIN | 3              | CHANNEL COUNTER<br>VALUE | RW         | UNSIGNED<br>32 BIT |
| <b>40106</b>            | 105                    | COUNTER LSW<br>DIN |                |                          | RW         |                    |
| <b>40107</b>            | 106                    | COUNTER MSW<br>DIN | 4              | CHANNEL COUNTER<br>VALUE | RW         | UNSIGNED<br>32 BIT |
| <b>40108</b>            | 107                    | COUNTER LSW<br>DIN |                |                          | RW         |                    |
| <b>40109</b>            | 108                    | COUNTER MSW<br>DIN | 5              | CHANNEL COUNTER<br>VALUE | RW         | UNSIGNED<br>32 BIT |
| <b>40110</b>            | 109                    | COUNTER LSW<br>DIN |                |                          | RW         |                    |
| <b>40111</b>            | 110                    | COUNTER MSW<br>DIN | 6              | CHANNEL COUNTER<br>VALUE | RW         | UNSIGNED<br>32 BIT |
| <b>40112</b>            | 111                    | COUNTER LSW<br>DIN |                |                          | RW         |                    |
| <b>40113</b>            | 112                    | COUNTER MSW<br>DIN | 7              | CHANNEL COUNTER<br>VALUE | RW         | UNSIGNED<br>32 BIT |
| <b>40114</b>            | 113                    | COUNTER LSW<br>DIN |                |                          | RW         |                    |
| <b>40115</b>            | 114                    | COUNTER MSW<br>DIN | 8              | CHANNEL COUNTER<br>VALUE | RW         | UNSIGNED<br>32 BIT |
| <b>40116</b>            | 115                    | COUNTER LSW<br>DIN |                |                          | RW         |                    |
| <b>40117</b>            | 116                    | COUNTER MSW<br>DIN | 9              | CHANNEL COUNTER<br>VALUE | RW         | UNSIGNED<br>32 BIT |
| <b>40118</b>            | 117                    | COUNTER LSW<br>DIN |                |                          | RW         |                    |
| <b>40119</b>            | 118                    | COUNTER MSW<br>DIN | 10             | CHANNEL COUNTER<br>VALUE | RW         | UNSIGNED<br>32 BIT |
| <b>40120</b>            | 119                    | COUNTER LSW<br>DIN |                |                          | RW         |                    |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFEST<br/>(4x)</b> | <b>REGISTER</b>    | <b>CHANNEL</b> | <b>DESCRIPTION</b>       | <b>W/R</b> | <b>TYPE</b>        |
|-------------------------|------------------------|--------------------|----------------|--------------------------|------------|--------------------|
| <b>40121</b>            | 120                    | COUNTER MSW<br>DIN | 11             | CHANNEL COUNTER<br>VALUE | RW         | UNSIGNED<br>32 BIT |
| <b>40122</b>            | 121                    | COUNTER LSW<br>DIN |                |                          | RW         |                    |
| <b>40123</b>            | 122                    | COUNTER MSW<br>DIN | 12             | CHANNEL COUNTER<br>VALUE | RW         | UNSIGNED<br>32 BIT |
| <b>40124</b>            | 123                    | COUNTER LSW<br>DIN |                |                          | RW         |                    |
| <b>40125</b>            | 124                    | COUNTER MSW<br>DIN | 13             | CHANNEL COUNTER<br>VALUE | RW         | UNSIGNED<br>32 BIT |
| <b>40126</b>            | 125                    | COUNTER LSW<br>DIN |                |                          | RW         |                    |
| <b>40127</b>            | 126                    | COUNTER MSW<br>DIN | 14             | CHANNEL COUNTER<br>VALUE | RW         | UNSIGNED<br>32 BIT |
| <b>40128</b>            | 127                    | COUNTER LSW<br>DIN |                |                          | RW         |                    |
| <b>40129</b>            | 128                    | COUNTER MSW<br>DIN | 15             | CHANNEL COUNTER<br>VALUE | RW         | UNSIGNED<br>32 BIT |
| <b>40130</b>            | 129                    | COUNTER LSW<br>DIN |                |                          | RW         |                    |
| <b>40131</b>            | 130                    | COUNTER MSW<br>DIN | 16             | CHANNEL COUNTER<br>VALUE | RW         | UNSIGNED<br>32 BIT |
| <b>40132</b>            | 131                    | COUNTER LSW<br>DIN |                |                          | RW         |                    |
| <b>40133</b>            | 132                    | COUNTER MSW<br>DIN | 17             | CHANNEL COUNTER<br>VALUE | RW         | UNSIGNED<br>32 BIT |
| <b>40134</b>            | 133                    | COUNTER LSW<br>DIN |                |                          | RW         |                    |
| <b>40135</b>            | 134                    | COUNTER MSW<br>DIN | 18             | CHANNEL COUNTER<br>VALUE | RW         | UNSIGNED<br>32 BIT |
| <b>40136</b>            | 135                    | COUNTER LSW<br>DIN |                |                          | RW         |                    |
| <b>40137</b>            | 136                    | COUNTER MSW<br>DIN | 19             | CHANNEL COUNTER<br>VALUE | RW         | UNSIGNED<br>32 BIT |
| <b>40138</b>            | 137                    | COUNTER LSW<br>DIN |                |                          | RW         |                    |
| <b>40139</b>            | 138                    | COUNTER MSW<br>DIN | 20             | CHANNEL COUNTER<br>VALUE | RW         | UNSIGNED<br>32 BIT |
| <b>40140</b>            | 139                    | COUNTER LSW<br>DIN |                |                          | RW         |                    |
| <b>40141</b>            | 140                    | COUNTER MSW<br>DIN | 21             | CHANNEL COUNTER<br>VALUE | RW         | UNSIGNED<br>32 BIT |
| <b>40142</b>            | 141                    | COUNTER LSW<br>DIN |                |                          | RW         |                    |
| <b>40143</b>            | 142                    | COUNTER MSW<br>DIN | 22             | CHANNEL COUNTER<br>VALUE | RW         | UNSIGNED<br>32 BIT |



| <b>ADDRESS<br/>(4x)</b> | <b>OFFEST<br/>(4x)</b> | <b>REGISTER</b>    | <b>CHANNEL</b> | <b>DESCRIPTION</b>       | <b>W/R</b> | <b>TYPE</b>        |
|-------------------------|------------------------|--------------------|----------------|--------------------------|------------|--------------------|
| <b>40144</b>            | 143                    | COUNTER LSW<br>DIN |                |                          | RW         |                    |
| <b>40145</b>            | 144                    | COUNTER MSW<br>DIN | 23             | CHANNEL COUNTER<br>VALUE | RW         | UNSIGNED<br>32 BIT |
| <b>40146</b>            | 145                    | COUNTER LSW<br>DIN |                |                          | RW         |                    |
| <b>40147</b>            | 146                    | COUNTER MSW<br>DIN | 24             | CHANNEL COUNTER<br>VALUE | RW         | UNSIGNED<br>32 BIT |
| <b>40148</b>            | 147                    | COUNTER LSW<br>DIN |                |                          | RW         |                    |
| <b>40149</b>            | 148                    | COUNTER MSW<br>DIN | 25             | CHANNEL COUNTER<br>VALUE | RW         | UNSIGNED<br>32 BIT |
| <b>40150</b>            | 149                    | COUNTER LSW<br>DIN |                |                          | RW         |                    |
| <b>40151</b>            | 150                    | COUNTER MSW<br>DIN | 26             | CHANNEL COUNTER<br>VALUE | RW         | UNSIGNED<br>32 BIT |
| <b>40152</b>            | 151                    | COUNTER LSW<br>DIN |                |                          | RW         |                    |
| <b>40153</b>            | 152                    | COUNTER MSW<br>DIN | 27             | CHANNEL COUNTER<br>VALUE | RW         | UNSIGNED<br>32 BIT |
| <b>40154</b>            | 153                    | COUNTER LSW<br>DIN |                |                          | RW         |                    |
| <b>40155</b>            | 154                    | COUNTER MSW<br>DIN | 28             | CHANNEL COUNTER<br>VALUE | RW         | UNSIGNED<br>32 BIT |
| <b>40156</b>            | 155                    | COUNTER LSW<br>DIN |                |                          | RW         |                    |
| <b>40157</b>            | 156                    | COUNTER MSW<br>DIN | 29             | CHANNEL COUNTER<br>VALUE | RW         | UNSIGNED<br>32 BIT |
| <b>40158</b>            | 157                    | COUNTER LSW<br>DIN |                |                          | RW         |                    |
| <b>40159</b>            | 158                    | COUNTER MSW<br>DIN | 30             | CHANNEL COUNTER<br>VALUE | RW         | UNSIGNED<br>32 BIT |
| <b>40160</b>            | 159                    | COUNTER LSW<br>DIN |                |                          | RW         |                    |
| <b>40161</b>            | 160                    | COUNTER MSW<br>DIN | 31             | CHANNEL COUNTER<br>VALUE | RW         | UNSIGNED<br>32 BIT |
| <b>40162</b>            | 161                    | COUNTER LSW<br>DIN |                |                          | RW         |                    |
| <b>40163</b>            | 162                    | COUNTER MSW<br>DIN | 32             | CHANNEL COUNTER<br>VALUE | RW         | UNSIGNED<br>32 BIT |
| <b>40164</b>            | 163                    | COUNTER LSW<br>DIN |                |                          | RW         |                    |
| <b>40165</b>            | 164                    | PERIOD             | 1              | PERIOD [ms]              | RW         | FLOAT 32 BIT       |
| <b>40166</b>            | 165                    |                    |                |                          | RW         |                    |
| <b>40167</b>            | 166                    | PERIOD             | 2              | PERIOD [ms]              | RW         | FLOAT 32 BIT       |
| <b>40168</b>            | 167                    |                    |                |                          | RW         |                    |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFEST<br/>(4x)</b> | <b>REGISTER</b> | <b>CHANNEL</b> | <b>DESCRIPTION</b> | <b>W/R</b> | <b>TYPE</b>  |
|-------------------------|------------------------|-----------------|----------------|--------------------|------------|--------------|
| 40169                   | 168                    | PERIOD          | 3              | PERIOD [ms]        | RW         | FLOAT 32 BIT |
| 40170                   | 169                    |                 |                |                    | RW         |              |
| 40171                   | 170                    | PERIOD          | 4              | PERIOD [ms]        | RW         | FLOAT 32 BIT |
| 40172                   | 171                    |                 |                |                    | RW         |              |
| 40173                   | 172                    | PERIOD          | 5              | PERIOD [ms]        | RW         | FLOAT 32 BIT |
| 40174                   | 173                    |                 |                |                    | RW         |              |
| 40175                   | 174                    | PERIOD          | 6              | PERIOD [ms]        | RW         | FLOAT 32 BIT |
| 40176                   | 175                    |                 |                |                    | RW         |              |
| 40177                   | 176                    | PERIOD          | 7              | PERIOD [ms]        | RW         | FLOAT 32 BIT |
| 40178                   | 177                    |                 |                |                    | RW         |              |
| 40179                   | 178                    | PERIOD          | 8              | PERIOD [ms]        | RW         | FLOAT 32 BIT |
| 40180                   | 179                    |                 |                |                    | RW         |              |
| 40181                   | 180                    | PERIOD          | 9              | PERIOD [ms]        | RW         | FLOAT 32 BIT |
| 40182                   | 181                    |                 |                |                    | RW         |              |
| 40183                   | 182                    | PERIOD          | 10             | PERIOD [ms]        | RW         | FLOAT 32 BIT |
| 40184                   | 183                    |                 |                |                    | RW         |              |
| 40185                   | 184                    | PERIOD          | 11             | PERIOD [ms]        | RW         | FLOAT 32 BIT |
| 40186                   | 185                    |                 |                |                    | RW         |              |
| 40187                   | 186                    | PERIOD          | 12             | PERIOD [ms]        | RW         | FLOAT 32 BIT |
| 40188                   | 187                    |                 |                |                    | RW         |              |
| 40189                   | 188                    | PERIOD          | 13             | PERIOD [ms]        | RW         | FLOAT 32 BIT |
| 40190                   | 189                    |                 |                |                    | RW         |              |
| 40191                   | 190                    | PERIOD          | 14             | PERIOD [ms]        | RW         | FLOAT 32 BIT |
| 40192                   | 191                    |                 |                |                    | RW         |              |
| 40193                   | 192                    | PERIOD          | 15             | PERIOD [ms]        | RW         | FLOAT 32 BIT |
| 40194                   | 193                    |                 |                |                    | RW         |              |
| 40195                   | 194                    | PERIOD          | 16             | PERIOD [ms]        | RW         | FLOAT 32 BIT |
| 40196                   | 195                    |                 |                |                    | RW         |              |
| 40197                   | 196                    | PERIOD          | 17             | PERIOD [ms]        | RW         | FLOAT 32 BIT |
| 40198                   | 197                    |                 |                |                    | RW         |              |
| 40199                   | 198                    | PERIOD          | 18             | PERIOD [ms]        | RW         | FLOAT 32 BIT |
| 40200                   | 199                    |                 |                |                    | RW         |              |
| 40201                   | 200                    | PERIOD          | 19             | PERIOD [ms]        | RW         | FLOAT 32 BIT |
| 40202                   | 201                    |                 |                |                    | RW         |              |
| 40203                   | 202                    | PERIOD          | 20             | PERIOD [ms]        | RW         | FLOAT 32 BIT |
| 40204                   | 203                    |                 |                |                    | RW         |              |
| 40205                   | 204                    | PERIOD          | 21             | PERIOD [ms]        | RW         | FLOAT 32 BIT |
| 40206                   | 205                    |                 |                |                    | RW         |              |
| 40207                   | 206                    | PERIOD          | 22             | PERIOD [ms]        | RW         | FLOAT 32 BIT |
| 40208                   | 207                    |                 |                |                    | RW         |              |
| 40209                   | 208                    | PERIOD          | 23             | PERIOD [ms]        | RW         | FLOAT 32 BIT |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFEST<br/>(4x)</b> | <b>REGISTER</b> | <b>CHANNEL</b> | <b>DESCRIPTION</b> | <b>W/R</b> | <b>TYPE</b>  |
|-------------------------|------------------------|-----------------|----------------|--------------------|------------|--------------|
| 40210                   | 209                    |                 |                |                    | RW         |              |
| 40211                   | 210                    | PERIOD          | 24             | PERIOD [ms]        | RW         | FLOAT 32 BIT |
| 40212                   | 211                    |                 |                |                    | RW         |              |
| 40213                   | 212                    | PERIOD          | 25             | PERIOD [ms]        | RW         | FLOAT 32 BIT |
| 40214                   | 213                    |                 |                |                    | RW         |              |
| 40215                   | 214                    | PERIOD          | 26             | PERIOD [ms]        | RW         | FLOAT 32 BIT |
| 40216                   | 215                    |                 |                |                    | RW         |              |
| 40217                   | 216                    | PERIOD          | 27             | PERIOD [ms]        | RW         | FLOAT 32 BIT |
| 40218                   | 217                    |                 |                |                    | RW         |              |
| 40219                   | 218                    | PERIOD          | 28             | PERIOD [ms]        | RW         | FLOAT 32 BIT |
| 40220                   | 219                    |                 |                |                    | RW         |              |
| 40221                   | 220                    | PERIOD          | 29             | PERIOD [ms]        | RW         | FLOAT 32 BIT |
| 40222                   | 221                    |                 |                |                    | RW         |              |
| 40223                   | 222                    | PERIOD          | 30             | PERIOD [ms]        | RW         | FLOAT 32 BIT |
| 40224                   | 223                    |                 |                |                    | RW         |              |
| 40225                   | 224                    | PERIOD          | 31             | PERIOD [ms]        | RW         | FLOAT 32 BIT |
| 40226                   | 225                    |                 |                |                    | RW         |              |
| 40227                   | 226                    | PERIOD          | 32             | PERIOD [ms]        | RW         | FLOAT 32 BIT |
| 40228                   | 227                    |                 |                |                    | RW         |              |
| 40229                   | 228                    | FREQUENCY       | 1              | FREQUENCY [Hz]     | RW         | FLOAT 32 BIT |
| 40230                   | 229                    |                 |                |                    | RW         |              |
| 40231                   | 230                    | FREQUENCY       | 2              | FREQUENCY [Hz]     | RW         | FLOAT 32 BIT |
| 40232                   | 231                    |                 |                |                    | RW         |              |
| 40233                   | 232                    | FREQUENCY       | 3              | FREQUENCY [Hz]     | RW         | FLOAT 32 BIT |
| 40234                   | 233                    |                 |                |                    | RW         |              |
| 40235                   | 234                    | FREQUENCY       | 4              | FREQUENCY [Hz]     | RW         | FLOAT 32 BIT |
| 40236                   | 235                    |                 |                |                    | RW         |              |
| 40237                   | 236                    | FREQUENCY       | 5              | FREQUENCY [Hz]     | RW         | FLOAT 32 BIT |
| 40238                   | 237                    |                 |                |                    | RW         |              |
| 40239                   | 238                    | FREQUENCY       | 6              | FREQUENCY [Hz]     | RW         | FLOAT 32 BIT |
| 40240                   | 239                    |                 |                |                    | RW         |              |
| 40241                   | 240                    | FREQUENCY       | 7              | FREQUENCY [Hz]     | RW         | FLOAT 32 BIT |
| 40242                   | 241                    |                 |                |                    | RW         |              |
| 40243                   | 242                    | FREQUENCY       | 8              | FREQUENCY [Hz]     | RW         | FLOAT 32 BIT |
| 40244                   | 243                    |                 |                |                    | RW         |              |
| 40245                   | 244                    | FREQUENCY       | 9              | FREQUENCY [Hz]     | RW         | FLOAT 32 BIT |
| 40246                   | 245                    |                 |                |                    | RW         |              |
| 40247                   | 246                    | FREQUENCY       | 10             | FREQUENCY [Hz]     | RW         | FLOAT 32 BIT |
| 40248                   | 247                    |                 |                |                    | RW         |              |
| 40249                   | 248                    | FREQUENCY       | 11             | FREQUENCY [Hz]     | RW         | FLOAT 32 BIT |
| 40250                   | 249                    |                 |                |                    | RW         |              |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>(4x)</b> | <b>REGISTER</b> | <b>CHANNEL</b> | <b>DESCRIPTION</b> | <b>W/R</b> | <b>TYPE</b>  |
|-------------------------|------------------------|-----------------|----------------|--------------------|------------|--------------|
| 40251                   | 250                    | FREQUENCY       | 12             | FREQUENCY [Hz]     | RW         | FLOAT 32 BIT |
| 40252                   | 251                    |                 |                |                    | RW         |              |
| 40253                   | 252                    | FREQUENCY       | 13             | FREQUENCY [Hz]     | RW         | FLOAT 32 BIT |
| 40254                   | 253                    |                 |                |                    | RW         |              |
| 40255                   | 254                    | FREQUENCY       | 14             | FREQUENCY [Hz]     | RW         | FLOAT 32 BIT |
| 40256                   | 255                    |                 |                |                    | RW         |              |
| 40257                   | 256                    | FREQUENCY       | 15             | FREQUENCY [Hz]     | RW         | FLOAT 32 BIT |
| 40258                   | 257                    |                 |                |                    | RW         |              |
| 40259                   | 258                    | FREQUENCY       | 16             | FREQUENCY [Hz]     | RW         | FLOAT 32 BIT |
| 40260                   | 259                    |                 |                |                    | RW         |              |
| 40261                   | 260                    | FREQUENCY       | 17             | FREQUENCY [Hz]     | RW         | FLOAT 32 BIT |
| 40262                   | 261                    |                 |                |                    | RW         |              |
| 40263                   | 262                    | FREQUENCY       | 18             | FREQUENCY [Hz]     | RW         | FLOAT 32 BIT |
| 40264                   | 263                    |                 |                |                    | RW         |              |
| 40265                   | 264                    | FREQUENCY       | 19             | FREQUENCY [Hz]     | RW         | FLOAT 32 BIT |
| 40266                   | 265                    |                 |                |                    | RW         |              |
| 40267                   | 266                    | FREQUENCY       | 20             | FREQUENCY [Hz]     | RW         | FLOAT 32 BIT |
| 40268                   | 267                    |                 |                |                    | RW         |              |
| 40269                   | 268                    | FREQUENCY       | 21             | FREQUENCY [Hz]     | RW         | FLOAT 32 BIT |
| 40270                   | 269                    |                 |                |                    | RW         |              |
| 40271                   | 270                    | FREQUENCY       | 22             | FREQUENCY [Hz]     | RW         | FLOAT 32 BIT |
| 40272                   | 271                    |                 |                |                    | RW         |              |
| 40273                   | 272                    | FREQUENCY       | 23             | FREQUENCY [Hz]     | RW         | FLOAT 32 BIT |
| 40274                   | 273                    |                 |                |                    | RW         |              |
| 40275                   | 274                    | FREQUENCY       | 24             | FREQUENCY [Hz]     | RW         | FLOAT 32 BIT |
| 40276                   | 275                    |                 |                |                    | RW         |              |
| 40277                   | 276                    | FREQUENCY       | 25             | FREQUENCY [Hz]     | RW         | FLOAT 32 BIT |
| 40278                   | 277                    |                 |                |                    | RW         |              |
| 40279                   | 278                    | FREQUENCY       | 26             | FREQUENCY [Hz]     | RW         | FLOAT 32 BIT |
| 40280                   | 279                    |                 |                |                    | RW         |              |
| 40281                   | 280                    | FREQUENCY       | 27             | FREQUENCY [Hz]     | RW         | FLOAT 32 BIT |
| 40282                   | 281                    |                 |                |                    | RW         |              |
| 40283                   | 282                    | FREQUENCY       | 28             | FREQUENCY [Hz]     | RW         | FLOAT 32 BIT |
| 40284                   | 283                    |                 |                |                    | RW         |              |
| 40285                   | 284                    | FREQUENCY       | 29             | FREQUENCY [Hz]     | RW         | FLOAT 32 BIT |
| 40286                   | 285                    |                 |                |                    | RW         |              |
| 40287                   | 286                    | FREQUENCY       | 30             | FREQUENCY [Hz]     | RW         | FLOAT 32 BIT |
| 40288                   | 287                    |                 |                |                    | RW         |              |
| 40289                   | 288                    | FREQUENCY       | 31             | FREQUENCY [Hz]     | RW         | FLOAT 32 BIT |
| 40290                   | 289                    |                 |                |                    | RW         |              |
| 40291                   | 290                    | FREQUENCY       | 32             | FREQUENCY [Hz]     | RW         | FLOAT 32 BIT |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>(4x)</b> | <b>REGISTER</b> | <b>CHANNEL</b> | <b>DESCRIPTION</b> | <b>W/R</b> | <b>TYPE</b> |
|-------------------------|------------------------|-----------------|----------------|--------------------|------------|-------------|
| 40292                   | 291                    |                 |                |                    | RW         |             |

## 21.2. R-32DIDO: MODBUS CONFIGURATION REGISTERS TABLE 4x HOLDING REGISTERS (FUNCTION CODE 3)

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>(4x)</b> | <b>REGISTER</b>                                           | <b>CHANNEL</b> | <b>DESCRIPTION</b>                                                                 | <b>W/R</b> | <b>TYPE</b>        |
|-------------------------|------------------------|-----------------------------------------------------------|----------------|------------------------------------------------------------------------------------|------------|--------------------|
| <b>45001-<br/>45006</b> | 5000-<br>5005          | MAC Address                                               | -              | MAC Address                                                                        | R          | UNSIGNED<br>16 BIT |
| <b>45007</b>            | 5007                   | DHCP mode                                                 |                | 0=DHCP OFF, else DHCP<br>ON                                                        | RW         | UNSIGNED<br>16 BIT |
| <b>45008-<br/>45011</b> | 5008-<br>5010          | Static IP address                                         |                | Static IP address                                                                  | RW         | 4 BYTE             |
| <b>45012-<br/>45015</b> | 5011-<br>5014          | Static mask<br>address                                    |                | Static mask address                                                                | RW         | 4 BYTE             |
| <b>45016-<br/>45019</b> | 5015-<br>5018          | Static Gateway<br>address                                 |                | Static Gateway address                                                             | RW         | 4 BYTE             |
| <b>45020</b>            | 5019                   | Enable change IP<br>address from<br>Discovery<br>protocol |                | 0=enabled, else disabled                                                           | RW         | UNSIGNED<br>16 BIT |
| <b>45021</b>            | 5020                   | Modbus TCP/IP<br>server port                              |                | Modbus TCP/IP server<br>port                                                       | RW         | UNSIGNED<br>16 BIT |
| <b>45022</b>            | 5021                   | Modbus TCP/IP<br>device address                           |                | Modbus TCP/IP device<br>address                                                    | RW         | UNSIGNED<br>16 BIT |
| <b>45023</b>            | 5022                   | Modbus<br>Passthrough                                     |                | 0=disabled, 1=enabled                                                              | RW         | UNSIGNED<br>16 BIT |
| <b>45024</b>            | 5023                   | Modbus TCP/IP<br>timeout [ms]                             |                |                                                                                    | RW         | UNSIGNED<br>16 BIT |
| <b>45025</b>            | 5024                   | P2P Server port                                           |                |                                                                                    | RW         | UNSIGNED<br>16 BIT |
| <b>45026-<br/>45041</b> | 5025-<br>5040          | Webserver user<br>name                                    |                | string max 15 chars,<br>terminated by NULL                                         | RW         | 16 BYTE            |
| <b>45042-<br/>45057</b> | 5041-<br>5056          | Webserver<br>password                                     |                | string max 15 chars,<br>terminated by NULL                                         | RW         | 16 BYTE            |
| <b>45058</b>            | 5057                   | Webserver port                                            |                |                                                                                    | RW         | UNSIGNED<br>16 BIT |
| <b>45059</b>            | 5058                   | Baudrate RS485                                            |                | 0=1200<br>1=2400<br>2=4800<br>3=9600<br>4=19200<br>5=38400<br>6=57600,<br>7=115200 | RW         | UNSIGNED<br>16 BIT |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>(4x)</b> | <b>REGISTER</b>                         | <b>CHANNEL</b> | <b>DESCRIPTION</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         | <b>W/R</b> | <b>TYPE</b>        |
|-------------------------|------------------------|-----------------------------------------|----------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|------------|--------------------|
| <b>45060</b>            | 5059                   | NOT USED                                |                |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            | RW         | UNSIGNED<br>16 BIT |
| <b>45061</b>            | 5060                   | Parity RS485                            |                | 0=NONE<br>1=ODD<br>2=EVEN                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  | RW         | UNSIGNED<br>16 BIT |
| <b>45062</b>            | 5061                   | Bit Stop RS485                          |                | 0=1 BIT DI STOP<br>1=2 BIT STOP                                                                                                                                                                                                                                                                                                                                                                                                                                                                            | RW         | UNSIGNED<br>16 BIT |
| <b>45063</b>            | 5062                   | Modbus RS485<br>slave device<br>address |                |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            | RW         | UNSIGNED<br>16 BIT |
| <b>45064</b>            | 5063                   | Timeout [ms]<br>Modbus RS485<br>slave   |                |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            | RW         | UNSIGNED<br>16 BIT |
| <b>45065</b>            | 5064                   | Digital I/O<br>Configuration            | 1              | bit 0 : 0=input, 1=output<br>bit 1 : 0=input not<br>inverted, 1=input inverted<br>bit 2 : 0=output not<br>inverted, 1=output<br>inverted<br>bit 4,3 :<br>00=output watchdog<br>disabled<br>01=out watchdog enabled,<br>refresh on modbus<br>communication<br>10=output watchdog<br>enabled, refresh on<br>modbus 40020-40021<br>write registers<br>bit 5 : 0=watchdog expired<br>open digital output, 1=<br>watchdog expired close<br>digital output<br>bit 15-8 : value in [s] of the<br>watchdog timeout | RW         | UNSIGNED<br>16 BIT |
| <b>45066</b>            | 5065                   | Digital I/O<br>Configuration            | 2              | See 45065 for definitions                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  | RW         | UNSIGNED<br>16 BIT |
| <b>45067</b>            | 5066                   | Digital I/O<br>Configuration            | 3              | See 45065 for definitions                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  | RW         | UNSIGNED<br>16 BIT |
| <b>45068</b>            | 5067                   | Digital I/O<br>Configuration            | 4              | See 45065 for definitions                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  | RW         | UNSIGNED<br>16 BIT |
| <b>45069</b>            | 5068                   | Digital I/O<br>Configuration            | 5              | See 45065 for definitions                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  | RW         | UNSIGNED<br>16 BIT |
| <b>45070</b>            | 5069                   | Digital I/O<br>Configuration            | 6              | See 45065 for definitions                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  | RW         | UNSIGNED<br>16 BIT |
| <b>45071</b>            | 5070                   | Digital I/O<br>Configuration            | 7              | See 45065 for definitions                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  | RW         | UNSIGNED<br>16 BIT |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>(4x)</b> | <b>REGISTER</b>           | <b>CHANNEL</b> | <b>DESCRIPTION</b>        | <b>W/R</b> | <b>TYPE</b>     |
|-------------------------|------------------------|---------------------------|----------------|---------------------------|------------|-----------------|
| <b>45072</b>            | 5071                   | Digital I/O Configuration | 8              | See 45065 for definitions | RW         | UNSIGNED 16 BIT |
| <b>45073</b>            | 5072                   | Digital I/O Configuration | 9              | See 45065 for definitions | RW         | UNSIGNED 16 BIT |
| <b>45074</b>            | 5073                   | Digital I/O Configuration | 10             | See 45065 for definitions | RW         | UNSIGNED 16 BIT |
| <b>45075</b>            | 5074                   | Digital I/O Configuration | 11             | See 45065 for definitions | RW         | UNSIGNED 16 BIT |
| <b>45076</b>            | 5075                   | Digital I/O Configuration | 12             | See 45065 for definitions | RW         | UNSIGNED 16 BIT |
| <b>45077</b>            | 5076                   | Digital I/O Configuration | 13             | See 45065 for definitions | RW         | UNSIGNED 16 BIT |
| <b>45078</b>            | 5077                   | Digital I/O Configuration | 14             | See 45065 for definitions | RW         | UNSIGNED 16 BIT |
| <b>45079</b>            | 5078                   | Digital I/O Configuration | 15             | See 45065 for definitions | RW         | UNSIGNED 16 BIT |
| <b>45080</b>            | 5079                   | Digital I/O Configuration | 16             | See 45065 for definitions | RW         | UNSIGNED 16 BIT |
| <b>45081</b>            | 5080                   | Digital I/O Configuration | 17             | See 45065 for definitions | RW         | UNSIGNED 16 BIT |
| <b>45082</b>            | 5081                   | Digital I/O Configuration | 18             | See 45065 for definitions | RW         | UNSIGNED 16 BIT |
| <b>45083</b>            | 5082                   | Digital I/O Configuration | 19             | See 45065 for definitions | RW         | UNSIGNED 16 BIT |
| <b>45084</b>            | 5083                   | Digital I/O Configuration | 20             | See 45065 for definitions | RW         | UNSIGNED 16 BIT |
| <b>45085</b>            | 5084                   | Digital I/O Configuration | 21             | See 45065 for definitions | RW         | UNSIGNED 16 BIT |
| <b>45086</b>            | 5085                   | Digital I/O Configuration | 22             | See 45065 for definitions | RW         | UNSIGNED 16 BIT |
| <b>45087</b>            | 5086                   | Digital I/O Configuration | 23             | See 45065 for definitions | RW         | UNSIGNED 16 BIT |
| <b>45088</b>            | 5087                   | Digital I/O Configuration | 24             | See 45065 for definitions | RW         | UNSIGNED 16 BIT |
| <b>45089</b>            | 5088                   | Digital I/O Configuration | 25             | See 45065 for definitions | RW         | UNSIGNED 16 BIT |
| <b>45090</b>            | 5089                   | Digital I/O Configuration | 26             | See 45065 for definitions | RW         | UNSIGNED 16 BIT |
| <b>45091</b>            | 5090                   | Digital I/O Configuration | 27             | See 45065 for definitions | RW         | UNSIGNED 16 BIT |
| <b>45092</b>            | 5091                   | Digital I/O Configuration | 28             | See 45065 for definitions | RW         | UNSIGNED 16 BIT |
| <b>45093</b>            | 5092                   | Digital I/O Configuration | 29             | See 45065 for definitions | RW         | UNSIGNED 16 BIT |
| <b>45094</b>            | 5093                   | Digital I/O Configuration | 30             | See 45065 for definitions | RW         | UNSIGNED 16 BIT |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>(4x)</b> | <b>REGISTER</b>                 | <b>CHANNEL</b> | <b>DESCRIPTION</b>                                                     | <b>W/R</b> | <b>TYPE</b>     |
|-------------------------|------------------------|---------------------------------|----------------|------------------------------------------------------------------------|------------|-----------------|
| <b>45095</b>            | 5094                   | Digital I/O Configuration       | 31             | See 45065 for definitions                                              | RW         | UNSIGNED 16 BIT |
| <b>45096</b>            | 5095                   | Digital I/O Configuration       | 32             | See 45065 for definitions                                              | RW         | UNSIGNED 16 BIT |
| <b>45097</b>            | 5096                   | Digital Input filter value [ms] | -              |                                                                        | RW         | UNSIGNED 16 BIT |
| <b>45501</b>            | 5500                   | P2P CLIENT RULE                 | 1              | 0=disabled, else enabled                                               | RW         | UNSIGNED 16 BIT |
| <b>45502</b>            | 5501                   | P2P CLIENT RULE                 | 1              | local channel value Din1..Din32 (0..31)                                | RW         | UNSIGNED 16 BIT |
| <b>45503-45506</b>      | 5502-5505              | P2P CLIENT RULE                 | 1              | remote ip address                                                      | RW         | 4 BYTE          |
| <b>45507</b>            | 5506                   | P2P CLIENT RULE                 | 1              | remote tcp/ip port                                                     | RW         | UNSIGNED 16 BIT |
| <b>45508</b>            | 5507                   | P2P CLIENT RULE                 | 1              | 0=enabled send on timeout, 1=enebaled send on timeout or changed value | RW         | UNSIGNED 16 BIT |
| <b>45509</b>            | 5508                   | P2P CLIENT RULE                 | 1              | timeout value [ms]                                                     | RW         | UNSIGNED 16 BIT |
| <b>45513</b>            | 5512                   | P2P CLIENT RULE                 | 1              | ack packet 0=disabled, 1=enabled                                       | RW         | UNSIGNED 16 BIT |
| <b>45533</b>            | 5532                   | P2P CLIENT RULE                 | 2              | 0=disabled, else enabled                                               | RW         | UNSIGNED 16 BIT |
| <b>45534</b>            | 5533                   | P2P CLIENT RULE                 | 2              | local channel value Din1..Din32 (0..31)                                | RW         | UNSIGNED 16 BIT |
| <b>45535-45538</b>      | 5534-5537              | P2P CLIENT RULE                 | 2              | remote ip address                                                      | RW         | 4 BYTE          |
| <b>45539</b>            | 5538                   | P2P CLIENT RULE                 | 2              | remote tcp/ip port                                                     | RW         | UNSIGNED 16 BIT |
| <b>45540</b>            | 5539                   | P2P CLIENT RULE                 | 2              | 0=enabled send on timeout, 1=enebaled send on timeout or changed value | RW         | UNSIGNED 16 BIT |
| <b>45541</b>            | 5540                   | P2P CLIENT RULE                 | 2              | timeout value [ms]                                                     | RW         | UNSIGNED 16 BIT |
| <b>45545</b>            | 5544                   | P2P CLIENT RULE                 | 2              | ack packet 0=disabled, 1=enabled                                       | RW         | UNSIGNED 16 BIT |
| <b>45565</b>            | 5564                   | P2P CLIENT RULE                 | 3              | 0=disabled, else enabled                                               | RW         | UNSIGNED 16 BIT |
| <b>45566</b>            | 5565                   | P2P CLIENT RULE                 | 3              | local channel value Din1..Din32 (0..31)                                | RW         | UNSIGNED 16 BIT |
| <b>45567-45570</b>      | 5566-5569              | P2P CLIENT RULE                 | 3              | remote ip address                                                      | RW         | 4 BYTE          |
| <b>45571</b>            | 5570                   | P2P CLIENT RULE                 | 3              | remote tcp/ip port                                                     | RW         | UNSIGNED 16 BIT |



| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>(4x)</b> | <b>REGISTER</b> | <b>CHANNEL</b> | <b>DESCRIPTION</b>                                                     | <b>W/R</b> | <b>TYPE</b>     |
|-------------------------|------------------------|-----------------|----------------|------------------------------------------------------------------------|------------|-----------------|
| <b>45572</b>            | 5571                   | P2P CLIENT RULE | 3              | 0=enabled send on timeout, 1=enebaled send on timeout or changed value | RW         | UNSIGNED 16 BIT |
| <b>45573</b>            | 5572                   | P2P CLIENT RULE | 3              | timeout value [ms]                                                     | RW         | UNSIGNED 16 BIT |
| <b>45577</b>            | 5576                   | P2P CLIENT RULE | 3              | ack packet 0=disabled, 1=enabled                                       | RW         | UNSIGNED 16 BIT |
| <b>45597</b>            | 5596                   | P2P CLIENT RULE | 4              | 0=disabled, else enabled                                               | RW         | UNSIGNED 16 BIT |
| <b>45598</b>            | 5597                   | P2P CLIENT RULE | 4              | local channel value Din1..Din32 (0..31)                                | RW         | UNSIGNED 16 BIT |
| <b>45599-45602</b>      | 5598-5601              | P2P CLIENT RULE | 4              | remote ip address                                                      | RW         | 4 BYTE          |
| <b>45603</b>            | 5602                   | P2P CLIENT RULE | 4              | remote tcp/ip port                                                     | RW         | UNSIGNED 16 BIT |
| <b>45604</b>            | 5603                   | P2P CLIENT RULE | 4              | 0=enabled send on timeout, 1=enebaled send on timeout or changed value | RW         | UNSIGNED 16 BIT |
| <b>45605</b>            | 5604                   | P2P CLIENT RULE | 4              | timeout value [ms]                                                     | RW         | UNSIGNED 16 BIT |
| <b>45609</b>            | 5608                   | P2P CLIENT RULE | 4              | ack packet 0=disabled, 1=enabled                                       | RW         | UNSIGNED 16 BIT |
| <b>45629</b>            | 5628                   | P2P CLIENT RULE | 5              | 0=disabled, else enabled                                               | RW         | UNSIGNED 16 BIT |
| <b>45630</b>            | 5629                   | P2P CLIENT RULE | 5              | local channel value Din1..Din32 (0..31)                                | RW         | UNSIGNED 16 BIT |
| <b>45631-45634</b>      | 5630-5633              | P2P CLIENT RULE | 5              | remote ip address                                                      | RW         | 4 BYTE          |
| <b>45635</b>            | 5634                   | P2P CLIENT RULE | 5              | remote tcp/ip port                                                     | RW         | UNSIGNED 16 BIT |
| <b>45636</b>            | 5635                   | P2P CLIENT RULE | 5              | 0=enabled send on timeout, 1=enebaled send on timeout or changed value | RW         | UNSIGNED 16 BIT |
| <b>45637</b>            | 5636                   | P2P CLIENT RULE | 5              | timeout value [ms]                                                     | RW         | UNSIGNED 16 BIT |
| <b>45641</b>            | 5640                   | P2P CLIENT RULE | 5              | ack packet 0=disabled, 1=enabled                                       | RW         | UNSIGNED 16 BIT |
| <b>45661</b>            | 5660                   | P2P CLIENT RULE | 6              | 0=disabled, else enabled                                               | RW         | UNSIGNED 16 BIT |
| <b>45662</b>            | 5661                   | P2P CLIENT RULE | 6              | local channel value Din1..Din32 (0..31)                                | RW         | UNSIGNED 16 BIT |
| <b>45663-45666</b>      | 5662-5665              | P2P CLIENT RULE | 6              | remote ip address                                                      | RW         | 4 BYTE          |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>(4x)</b> | <b>REGISTER</b> | <b>CHANNEL</b> | <b>DESCRIPTION</b>                                                              | <b>W/R</b> | <b>TYPE</b>        |
|-------------------------|------------------------|-----------------|----------------|---------------------------------------------------------------------------------|------------|--------------------|
| <b>45667</b>            | 5666                   | P2P CLIENT RULE | 6              | remote tcp/ip port                                                              | RW         | UNSIGNED<br>16 BIT |
| <b>45668</b>            | 5667                   | P2P CLIENT RULE | 6              | 0=enabled send on<br>timeout, 1=enebaled send<br>on timeout or changed<br>value | RW         | UNSIGNED<br>16 BIT |
| <b>45669</b>            | 5668                   | P2P CLIENT RULE | 6              | timeout value [ms]                                                              | RW         | UNSIGNED<br>16 BIT |
| <b>45673</b>            | 5672                   | P2P CLIENT RULE | 6              | ack packet 0=disabled,<br>1=enabled                                             | RW         | UNSIGNED<br>16 BIT |
| <b>45693</b>            | 5692                   | P2P CLIENT RULE | 7              | 0=disabled, else enabled                                                        | RW         | UNSIGNED<br>16 BIT |
| <b>45694</b>            | 5693                   | P2P CLIENT RULE | 7              | local channel value<br>Din1..Din32 (0..31)                                      | RW         | UNSIGNED<br>16 BIT |
| <b>45695-<br/>45698</b> | 5694-<br>5697          | P2P CLIENT RULE | 7              | remote ip address                                                               | RW         | 4 BYTE             |
| <b>45699</b>            | 5698                   | P2P CLIENT RULE | 7              | remote tcp/ip port                                                              | RW         | UNSIGNED<br>16 BIT |
| <b>45700</b>            | 5699                   | P2P CLIENT RULE | 7              | 0=enabled send on<br>timeout, 1=enebaled send<br>on timeout or changed<br>value | RW         | UNSIGNED<br>16 BIT |
| <b>45701</b>            | 5700                   | P2P CLIENT RULE | 7              | timeout value [ms]                                                              | RW         | UNSIGNED<br>16 BIT |
| <b>45705</b>            | 5704                   | P2P CLIENT RULE | 7              | ack packet 0=disabled,<br>1=enabled                                             | RW         | UNSIGNED<br>16 BIT |
| <b>45725</b>            | 5724                   | P2P CLIENT RULE | 8              | 0=disabled, else enabled                                                        | RW         | UNSIGNED<br>16 BIT |
| <b>45726</b>            | 5725                   | P2P CLIENT RULE | 8              | local channel value<br>Din1..Din32 (0..31)                                      | RW         | UNSIGNED<br>16 BIT |
| <b>45727-<br/>45730</b> | 5726-<br>5729          | P2P CLIENT RULE | 8              | remote ip address                                                               | RW         | 4 BYTE             |
| <b>45731</b>            | 5730                   | P2P CLIENT RULE | 8              | remote tcp/ip port                                                              | RW         | UNSIGNED<br>16 BIT |
| <b>45732</b>            | 5731                   | P2P CLIENT RULE | 8              | 0=enabled send on<br>timeout, 1=enebaled send<br>on timeout or changed<br>value | RW         | UNSIGNED<br>16 BIT |
| <b>45733</b>            | 5732                   | P2P CLIENT RULE | 8              | timeout value [ms]                                                              | RW         | UNSIGNED<br>16 BIT |
| <b>45737</b>            | 5736                   | P2P CLIENT RULE | 8              | ack packet 0=disabled,<br>1=enabled                                             | RW         | UNSIGNED<br>16 BIT |
| <b>45757</b>            | 5756                   | P2P CLIENT RULE | 9              | 0=disabled, else enabled                                                        | RW         | UNSIGNED<br>16 BIT |
| <b>45758</b>            | 5757                   | P2P CLIENT RULE | 9              | local channel value<br>Din1..Din32 (0..31)                                      | RW         | UNSIGNED<br>16 BIT |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>(4x)</b> | <b>REGISTER</b> | <b>CHANNEL</b> | <b>DESCRIPTION</b>                                                     | <b>W/R</b> | <b>TYPE</b>        |
|-------------------------|------------------------|-----------------|----------------|------------------------------------------------------------------------|------------|--------------------|
| <b>45759-45762</b>      | 5758-5761              | P2P CLIENT RULE | 9              | remote ip address                                                      | RW         | 4 BYTE             |
| <b>45763</b>            | 5762                   | P2P CLIENT RULE | 9              | remote tcp/ip port                                                     | RW         | UNSIGNED<br>16 BIT |
| <b>45764</b>            | 5763                   | P2P CLIENT RULE | 9              | 0=enabled send on timeout, 1=enebaled send on timeout or changed value | RW         | UNSIGNED<br>16 BIT |
| <b>45765</b>            | 5764                   | P2P CLIENT RULE | 9              | timeout value [ms]                                                     | RW         | UNSIGNED<br>16 BIT |
| <b>45769</b>            | 5768                   | P2P CLIENT RULE | 9              | ack packet 0=disabled, 1=enabled                                       | RW         | UNSIGNED<br>16 BIT |
| <b>45789</b>            | 5788                   | P2P CLIENT RULE | 10             | 0=disabled, else enabled                                               | RW         | UNSIGNED<br>16 BIT |
| <b>45790</b>            | 5789                   | P2P CLIENT RULE | 10             | local channel value Din1..Din32 (0..31)                                | RW         | UNSIGNED<br>16 BIT |
| <b>45791-45794</b>      | 5790-5793              | P2P CLIENT RULE | 10             | remote ip address                                                      | RW         | 4 BYTE             |
| <b>45795</b>            | 5794                   | P2P CLIENT RULE | 10             | remote tcp/ip port                                                     | RW         | UNSIGNED<br>16 BIT |
| <b>45796</b>            | 5795                   | P2P CLIENT RULE | 10             | 0=enabled send on timeout, 1=enebaled send on timeout or changed value | RW         | UNSIGNED<br>16 BIT |
| <b>45797</b>            | 5796                   | P2P CLIENT RULE | 10             | timeout value [ms]                                                     | RW         | UNSIGNED<br>16 BIT |
| <b>45801</b>            | 5800                   | P2P CLIENT RULE | 10             | ack packet 0=disabled, 1=enabled                                       | RW         | UNSIGNED<br>16 BIT |
| <b>45821</b>            | 5820                   | P2P CLIENT RULE | 11             | 0=disabled, else enabled                                               | RW         | UNSIGNED<br>16 BIT |
| <b>45822</b>            | 5821                   | P2P CLIENT RULE | 11             | local channel value Din1..Din32 (0..31)                                | RW         | UNSIGNED<br>16 BIT |
| <b>45823-45826</b>      | 5822-5825              | P2P CLIENT RULE | 11             | remote ip address                                                      | RW         | 4 BYTE             |
| <b>45827</b>            | 5826                   | P2P CLIENT RULE | 11             | remote tcp/ip port                                                     | RW         | UNSIGNED<br>16 BIT |
| <b>45828</b>            | 5827                   | P2P CLIENT RULE | 11             | 0=enabled send on timeout, 1=enebaled send on timeout or changed value | RW         | UNSIGNED<br>16 BIT |
| <b>45829</b>            | 5828                   | P2P CLIENT RULE | 11             | timeout value [ms]                                                     | RW         | UNSIGNED<br>16 BIT |
| <b>45833</b>            | 5832                   | P2P CLIENT RULE | 11             | ack packet 0=disabled, 1=enabled                                       | RW         | UNSIGNED<br>16 BIT |
| <b>45853</b>            | 5852                   | P2P CLIENT RULE | 12             | 0=disabled, else enabled                                               | RW         | UNSIGNED<br>16 BIT |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>(4x)</b> | <b>REGISTER</b> | <b>CHANNEL</b> | <b>DESCRIPTION</b>                                                              | <b>W/R</b> | <b>TYPE</b>        |
|-------------------------|------------------------|-----------------|----------------|---------------------------------------------------------------------------------|------------|--------------------|
| <b>45854</b>            | 5853                   | P2P CLIENT RULE | 12             | local channel value<br>Din1..Din32 (0..31)                                      | RW         | UNSIGNED<br>16 BIT |
| <b>45855-<br/>45858</b> | 5854-<br>5857          | P2P CLIENT RULE | 12             | remote ip address                                                               | RW         | 4 BYTE             |
| <b>45859</b>            | 5858                   | P2P CLIENT RULE | 12             | remote tcp/ip port                                                              | RW         | UNSIGNED<br>16 BIT |
| <b>45860</b>            | 5859                   | P2P CLIENT RULE | 12             | 0=enabled send on<br>timeout, 1=enebaled send<br>on timeout or changed<br>value | RW         | UNSIGNED<br>16 BIT |
| <b>45861</b>            | 5860                   | P2P CLIENT RULE | 12             | timeout value [ms]                                                              | RW         | UNSIGNED<br>16 BIT |
| <b>45865</b>            | 5864                   | P2P CLIENT RULE | 12             | ack packet 0=disabled,<br>1=enabled                                             | RW         | UNSIGNED<br>16 BIT |
| <b>45885</b>            | 5884                   | P2P CLIENT RULE | 13             | 0=disabled, else enabled                                                        | RW         | UNSIGNED<br>16 BIT |
| <b>45886</b>            | 5885                   | P2P CLIENT RULE | 13             | local channel value<br>Din1..Din32 (0..31)                                      | RW         | UNSIGNED<br>16 BIT |
| <b>45887-<br/>45890</b> | 5886-<br>5888          | P2P CLIENT RULE | 13             | remote ip address                                                               | RW         | 4 BYTE             |
| <b>45891</b>            | 5890                   | P2P CLIENT RULE | 13             | remote tcp/ip port                                                              | RW         | UNSIGNED<br>16 BIT |
| <b>45892</b>            | 5891                   | P2P CLIENT RULE | 13             | 0=enabled send on<br>timeout, 1=enebaled send<br>on timeout or changed<br>value | RW         | UNSIGNED<br>16 BIT |
| <b>45893</b>            | 5892                   | P2P CLIENT RULE | 13             | timeout value [ms]                                                              | RW         | UNSIGNED<br>16 BIT |
| <b>45897</b>            | 5896                   | P2P CLIENT RULE | 13             | ack packet 0=disabled,<br>1=enabled                                             | RW         | UNSIGNED<br>16 BIT |
| <b>45917</b>            | 5916                   | P2P CLIENT RULE | 14             | 0=disabled, else enabled                                                        | RW         | UNSIGNED<br>16 BIT |
| <b>45918</b>            | 5917                   | P2P CLIENT RULE | 14             | local channel value<br>Din1..Din32 (0..31)                                      | RW         | UNSIGNED<br>16 BIT |
| <b>45919-<br/>45922</b> | 5918-<br>5921          | P2P CLIENT RULE | 14             | remote ip address                                                               | RW         | 4 BYTE             |
| <b>45923</b>            | 5922                   | P2P CLIENT RULE | 14             | remote tcp/ip port                                                              | RW         | UNSIGNED<br>16 BIT |
| <b>45924</b>            | 5923                   | P2P CLIENT RULE | 14             | 0=enabled send on<br>timeout, 1=enebaled send<br>on timeout or changed<br>value | RW         | UNSIGNED<br>16 BIT |
| <b>45925</b>            | 5924                   | P2P CLIENT RULE | 14             | timeout value [ms]                                                              | RW         | UNSIGNED<br>16 BIT |
| <b>45929</b>            | 5928                   | P2P CLIENT RULE | 14             | ack packet 0=disabled,<br>1=enabled                                             | RW         | UNSIGNED<br>16 BIT |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>(4x)</b> | <b>REGISTER</b> | <b>CHANNEL</b> | <b>DESCRIPTION</b>                                                              | <b>W/R</b> | <b>TYPE</b>        |
|-------------------------|------------------------|-----------------|----------------|---------------------------------------------------------------------------------|------------|--------------------|
| <b>45949</b>            | 5948                   | P2P CLIENT RULE | 15             | 0=disabled, else enabled                                                        | RW         | UNSIGNED<br>16 BIT |
| <b>45950</b>            | 5949                   | P2P CLIENT RULE | 15             | local channel value<br>Din1..Din32 (0..31)                                      | RW         | UNSIGNED<br>16 BIT |
| <b>45951-<br/>45954</b> | 5950-<br>5953          | P2P CLIENT RULE | 15             | remote ip address                                                               | RW         | 4 BYTE             |
| <b>45955</b>            | 5954                   | P2P CLIENT RULE | 15             | remote tcp/ip port                                                              | RW         | UNSIGNED<br>16 BIT |
| <b>45956</b>            | 5955                   | P2P CLIENT RULE | 15             | 0=enabled send on<br>timeout, 1=enebaled send<br>on timeout or changed<br>value | RW         | UNSIGNED<br>16 BIT |
| <b>45957</b>            | 5956                   | P2P CLIENT RULE | 15             | timeout value [ms]                                                              | RW         | UNSIGNED<br>16 BIT |
| <b>45961</b>            | 5960                   | P2P CLIENT RULE | 15             | ack packet 0=disabled,<br>1=enabled                                             | RW         | UNSIGNED<br>16 BIT |
| <b>45981</b>            | 5980                   | P2P CLIENT RULE | 16             | 0=disabled, else enabled                                                        | RW         | UNSIGNED<br>16 BIT |
| <b>45982</b>            | 5981                   | P2P CLIENT RULE | 16             | local channel value<br>Din1..Din32 (0..31)                                      | RW         | UNSIGNED<br>16 BIT |
| <b>45983-<br/>45986</b> | 5982-<br>5985          | P2P CLIENT RULE | 16             | remote ip address                                                               | RW         | 4 BYTE             |
| <b>45987</b>            | 5986                   | P2P CLIENT RULE | 16             | remote tcp/ip port                                                              | RW         | UNSIGNED<br>16 BIT |
| <b>45988</b>            | 5987                   | P2P CLIENT RULE | 16             | 0=enabled send on<br>timeout, 1=enebaled send<br>on timeout or changed<br>value | RW         | UNSIGNED<br>16 BIT |
| <b>45989</b>            | 5988                   | P2P CLIENT RULE | 16             | timeout value [ms]                                                              | RW         | UNSIGNED<br>16 BIT |
| <b>45993</b>            | 5992                   | P2P CLIENT RULE | 16             | ack packet 0=disabled,<br>1=enabled                                             | RW         | UNSIGNED<br>16 BIT |
| <b>46013</b>            | 6012                   | P2P CLIENT RULE | 17             | 0=disabled, else enabled                                                        | RW         | UNSIGNED<br>16 BIT |
| <b>46014</b>            | 6013                   | P2P CLIENT RULE | 17             | local channel value<br>Din1..Din32 (0..31)                                      | RW         | UNSIGNED<br>16 BIT |
| <b>46015-<br/>46018</b> | 6014-<br>6017          | P2P CLIENT RULE | 17             | remote ip address                                                               | RW         | 4 BYTE             |
| <b>46019</b>            | 6018                   | P2P CLIENT RULE | 17             | remote tcp/ip port                                                              | RW         | UNSIGNED<br>16 BIT |
| <b>46020</b>            | 6019                   | P2P CLIENT RULE | 17             | 0=enabled send on<br>timeout, 1=enebaled send<br>on timeout or changed<br>value | RW         | UNSIGNED<br>16 BIT |
| <b>46021</b>            | 6020                   | P2P CLIENT RULE | 17             | timeout value [ms]                                                              | RW         | UNSIGNED<br>16 BIT |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>(4x)</b> | <b>REGISTER</b> | <b>CHANNEL</b> | <b>DESCRIPTION</b>                                                              | <b>W/R</b> | <b>TYPE</b>        |
|-------------------------|------------------------|-----------------|----------------|---------------------------------------------------------------------------------|------------|--------------------|
| <b>46025</b>            | 6024                   | P2P CLIENT RULE | 17             | ack packet 0=disabled,<br>1=enabled                                             | RW         | UNSIGNED<br>16 BIT |
| <b>46045</b>            | 6044                   | P2P CLIENT RULE | 18             | 0=disabled, else enabled                                                        | RW         | UNSIGNED<br>16 BIT |
| <b>46046</b>            | 6045                   | P2P CLIENT RULE | 18             | local channel value<br>Din1..Din32 (0..31)                                      | RW         | UNSIGNED<br>16 BIT |
| <b>46047-<br/>46050</b> | 6026-<br>6049          | P2P CLIENT RULE | 18             | remote ip address                                                               | RW         | 4 BYTE             |
| <b>46051</b>            | 6050                   | P2P CLIENT RULE | 18             | remote tcp/ip port                                                              | RW         | UNSIGNED<br>16 BIT |
| <b>46052</b>            | 6051                   | P2P CLIENT RULE | 18             | 0=enabled send on<br>timeout, 1=enebaled send<br>on timeout or changed<br>value | RW         | UNSIGNED<br>16 BIT |
| <b>46053</b>            | 6052                   | P2P CLIENT RULE | 18             | timeout value [ms]                                                              | RW         | UNSIGNED<br>16 BIT |
| <b>46057</b>            | 6056                   | P2P CLIENT RULE | 18             | ack packet 0=disabled,<br>1=enabled                                             | RW         | UNSIGNED<br>16 BIT |
| <b>46077</b>            | 6076                   | P2P CLIENT RULE | 19             | 0=disabled, else enabled                                                        | RW         | UNSIGNED<br>16 BIT |
| <b>46078</b>            | 6077                   | P2P CLIENT RULE | 19             | local channel value<br>Din1..Din32 (0..31)                                      | RW         | UNSIGNED<br>16 BIT |
| <b>46079-<br/>46082</b> | 6078-<br>6081          | P2P CLIENT RULE | 19             | remote ip address                                                               | RW         | 4 BYTE             |
| <b>46083</b>            | 6082                   | P2P CLIENT RULE | 19             | remote tcp/ip port                                                              | RW         | UNSIGNED<br>16 BIT |
| <b>46084</b>            | 6083                   | P2P CLIENT RULE | 19             | 0=enabled send on<br>timeout, 1=enebaled send<br>on timeout or changed<br>value | RW         | UNSIGNED<br>16 BIT |
| <b>46085</b>            | 6084                   | P2P CLIENT RULE | 19             | timeout value [ms]                                                              | RW         | UNSIGNED<br>16 BIT |
| <b>46089</b>            | 6088                   | P2P CLIENT RULE | 19             | ack packet 0=disabled,<br>1=enabled                                             | RW         | UNSIGNED<br>16 BIT |
| <b>46109</b>            | 6108                   | P2P CLIENT RULE | 20             | 0=disabled, else enabled                                                        | RW         | UNSIGNED<br>16 BIT |
| <b>46110</b>            | 6109                   | P2P CLIENT RULE | 20             | local channel value<br>Din1..Din32 (0..31)                                      | RW         | UNSIGNED<br>16 BIT |
| <b>46111-<br/>46114</b> | 6110-<br>6113          | P2P CLIENT RULE | 20             | remote ip address                                                               | RW         | 4 BYTE             |
| <b>46115</b>            | 6114                   | P2P CLIENT RULE | 20             | remote tcp/ip port                                                              | RW         | UNSIGNED<br>16 BIT |
| <b>46116</b>            | 6115                   | P2P CLIENT RULE | 20             | 0=enabled send on<br>timeout, 1=enebaled send<br>on timeout or changed<br>value | RW         | UNSIGNED<br>16 BIT |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>(4x)</b> | <b>REGISTER</b> | <b>CHANNEL</b> | <b>DESCRIPTION</b>                                                              | <b>W/R</b> | <b>TYPE</b>        |
|-------------------------|------------------------|-----------------|----------------|---------------------------------------------------------------------------------|------------|--------------------|
| <b>46117</b>            | 6116                   | P2P CLIENT RULE | 20             | timeout value [ms]                                                              | RW         | UNSIGNED<br>16 BIT |
| <b>46121</b>            | 6120                   | P2P CLIENT RULE | 20             | ack packet 0=disabled,<br>1=enabled                                             | RW         | UNSIGNED<br>16 BIT |
| <b>46141</b>            | 6140                   | P2P CLIENT RULE | 21             | 0=disabled, else enabled                                                        | RW         | UNSIGNED<br>16 BIT |
| <b>46142</b>            | 6141                   | P2P CLIENT RULE | 21             | local channel value<br>Din1..Din32 (0..31)                                      | RW         | UNSIGNED<br>16 BIT |
| <b>46143-<br/>46146</b> | 6142-<br>6145          | P2P CLIENT RULE | 21             | remote ip address                                                               | RW         | 4 BYTE             |
| <b>46147</b>            | 6146                   | P2P CLIENT RULE | 21             | remote tcp/ip port                                                              | RW         | UNSIGNED<br>16 BIT |
| <b>46148</b>            | 6147                   | P2P CLIENT RULE | 21             | 0=enabled send on<br>timeout, 1=enebaled send<br>on timeout or changed<br>value | RW         | UNSIGNED<br>16 BIT |
| <b>46149</b>            | 6148                   | P2P CLIENT RULE | 21             | timeout value [ms]                                                              | RW         | UNSIGNED<br>16 BIT |
| <b>46153</b>            | 6152                   | P2P CLIENT RULE | 21             | ack packet 0=disabled,<br>1=enabled                                             | RW         | UNSIGNED<br>16 BIT |
| <b>46173</b>            | 6172                   | P2P CLIENT RULE | 22             | 0=disabled, else enabled                                                        | RW         | UNSIGNED<br>16 BIT |
| <b>46174</b>            | 6173                   | P2P CLIENT RULE | 22             | local channel value<br>Din1..Din32 (0..31)                                      | RW         | UNSIGNED<br>16 BIT |
| <b>46175-<br/>46178</b> | 6174-<br>6177          | P2P CLIENT RULE | 22             | remote ip address                                                               | RW         | 4 BYTE             |
| <b>46179</b>            | 6178                   | P2P CLIENT RULE | 22             | remote tcp/ip port                                                              | RW         | UNSIGNED<br>16 BIT |
| <b>46180</b>            | 6179                   | P2P CLIENT RULE | 22             | 0=enabled send on<br>timeout, 1=enebaled send<br>on timeout or changed<br>value | RW         | UNSIGNED<br>16 BIT |
| <b>46181</b>            | 6180                   | P2P CLIENT RULE | 22             | timeout value [ms]                                                              | RW         | UNSIGNED<br>16 BIT |
| <b>46185</b>            | 6184                   | P2P CLIENT RULE | 22             | ack packet 0=disabled,<br>1=enabled                                             | RW         | UNSIGNED<br>16 BIT |
| <b>46205</b>            | 6204                   | P2P CLIENT RULE | 23             | 0=disabled, else enabled                                                        | RW         | UNSIGNED<br>16 BIT |
| <b>46206</b>            | 6205                   | P2P CLIENT RULE | 23             | local channel value<br>Din1..Din32 (0..31)                                      | RW         | UNSIGNED<br>16 BIT |
| <b>46207-<br/>46210</b> | 6206-<br>6209          | P2P CLIENT RULE | 23             | remote ip address                                                               | RW         | 4 BYTE             |
| <b>46211</b>            | 6210                   | P2P CLIENT RULE | 23             | remote tcp/ip port                                                              | RW         | UNSIGNED<br>16 BIT |
| <b>46212</b>            | 6211                   | P2P CLIENT RULE | 23             | 0=enabled send on<br>timeout, 1=enebaled send                                   | RW         | UNSIGNED<br>16 BIT |



| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>(4x)</b> | <b>REGISTER</b> | <b>CHANNEL</b> | <b>DESCRIPTION</b>                                                              | <b>W/R</b> | <b>TYPE</b>        |
|-------------------------|------------------------|-----------------|----------------|---------------------------------------------------------------------------------|------------|--------------------|
|                         |                        |                 |                | on timeout or changed value                                                     |            |                    |
| <b>46213</b>            | 6212                   | P2P CLIENT RULE | 23             | timeout value [ms]                                                              | RW         | UNSIGNED<br>16 BIT |
| <b>46217</b>            | 6216                   | P2P CLIENT RULE | 23             | ack packet 0=disabled,<br>1=enabled                                             | RW         | UNSIGNED<br>16 BIT |
| <b>46237</b>            | 6236                   | P2P CLIENT RULE | 24             | 0=disabled, else enabled                                                        | RW         | UNSIGNED<br>16 BIT |
| <b>46238</b>            | 6237                   | P2P CLIENT RULE | 24             | local channel value<br>Din1..Din32 (0..31)                                      | RW         | UNSIGNED<br>16 BIT |
| <b>46239-<br/>46242</b> | 6238-<br>6241          | P2P CLIENT RULE | 24             | remote ip address                                                               | RW         | 4 BYTE             |
| <b>46243</b>            | 6242                   | P2P CLIENT RULE | 24             | remote tcp/ip port                                                              | RW         | UNSIGNED<br>16 BIT |
| <b>46244</b>            | 6243                   | P2P CLIENT RULE | 24             | 0=enabled send on<br>timeout, 1=enebaled send<br>on timeout or changed<br>value | RW         | UNSIGNED<br>16 BIT |
| <b>46245</b>            | 6244                   | P2P CLIENT RULE | 24             | timeout value [ms]                                                              | RW         | UNSIGNED<br>16 BIT |
| <b>46249</b>            | 6248                   | P2P CLIENT RULE | 24             | ack packet 0=disabled,<br>1=enabled                                             | RW         | UNSIGNED<br>16 BIT |
| <b>46269</b>            | 6268                   | P2P CLIENT RULE | 25             | 0=disabled, else enabled                                                        | RW         | UNSIGNED<br>16 BIT |
| <b>46270</b>            | 6269                   | P2P CLIENT RULE | 25             | local channel value<br>Din1..Din32 (0..31)                                      | RW         | UNSIGNED<br>16 BIT |
| <b>46271-<br/>46274</b> | 6270-<br>6273          | P2P CLIENT RULE | 25             | remote ip address                                                               | RW         | 4 BYTE             |
| <b>46275</b>            | 6274                   | P2P CLIENT RULE | 25             | remote tcp/ip port                                                              | RW         | UNSIGNED<br>16 BIT |
| <b>46276</b>            | 6275                   | P2P CLIENT RULE | 25             | 0=enabled send on<br>timeout, 1=enebaled send<br>on timeout or changed<br>value | RW         | UNSIGNED<br>16 BIT |
| <b>46277</b>            | 6276                   | P2P CLIENT RULE | 25             | timeout value [ms]                                                              | RW         | UNSIGNED<br>16 BIT |
| <b>46281</b>            | 6280                   | P2P CLIENT RULE | 25             | ack packet 0=disabled,<br>1=enabled                                             | RW         | UNSIGNED<br>16 BIT |
| <b>46301</b>            | 6300                   | P2P CLIENT RULE | 26             | 0=disabled, else enabled                                                        | RW         | UNSIGNED<br>16 BIT |
| <b>46302</b>            | 6301                   | P2P CLIENT RULE | 26             | local channel value<br>Din1..Din32 (0..31)                                      | RW         | UNSIGNED<br>16 BIT |
| <b>46303-<br/>46306</b> | 6302-<br>6305          | P2P CLIENT RULE | 26             | remote ip address                                                               | RW         | 4 BYTE             |
| <b>46307</b>            | 6306                   | P2P CLIENT RULE | 26             | remote tcp/ip port                                                              | RW         | UNSIGNED<br>16 BIT |



| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>(4x)</b> | <b>REGISTER</b> | <b>CHANNEL</b> | <b>DESCRIPTION</b>                                                     | <b>W/R</b> | <b>TYPE</b>     |
|-------------------------|------------------------|-----------------|----------------|------------------------------------------------------------------------|------------|-----------------|
| <b>46308</b>            | 6307                   | P2P CLIENT RULE | 26             | 0=enabled send on timeout, 1=enebaled send on timeout or changed value | RW         | UNSIGNED 16 BIT |
| <b>46309</b>            | 6308                   | P2P CLIENT RULE | 26             | timeout value [ms]                                                     | RW         | UNSIGNED 16 BIT |
| <b>46313</b>            | 6312                   | P2P CLIENT RULE | 26             | ack packet 0=disabled, 1=enabled                                       | RW         | UNSIGNED 16 BIT |
| <b>46333</b>            | 6332                   | P2P CLIENT RULE | 27             | 0=disabled, else enabled                                               | RW         | UNSIGNED 16 BIT |
| <b>46334</b>            | 6333                   | P2P CLIENT RULE | 27             | local channel value Din1..Din32 (0..31)                                | RW         | UNSIGNED 16 BIT |
| <b>46335-46338</b>      | 6334-6337              | P2P CLIENT RULE | 27             | remote ip address                                                      | RW         | 4 BYTE          |
| <b>46339</b>            | 6338                   | P2P CLIENT RULE | 27             | remote tcp/ip port                                                     | RW         | UNSIGNED 16 BIT |
| <b>46340</b>            | 6339                   | P2P CLIENT RULE | 27             | 0=enabled send on timeout, 1=enebaled send on timeout or changed value | RW         | UNSIGNED 16 BIT |
| <b>46341</b>            | 6340                   | P2P CLIENT RULE | 27             | timeout value [ms]                                                     | RW         | UNSIGNED 16 BIT |
| <b>46345</b>            | 6344                   | P2P CLIENT RULE | 27             | ack packet 0=disabled, 1=enabled                                       | RW         | UNSIGNED 16 BIT |
| <b>46365</b>            | 6364                   | P2P CLIENT RULE | 28             | 0=disabled, else enabled                                               | RW         | UNSIGNED 16 BIT |
| <b>46366</b>            | 6365                   | P2P CLIENT RULE | 28             | local channel value Din1..Din32 (0..31)                                | RW         | UNSIGNED 16 BIT |
| <b>46367-46370</b>      | 6366-6369              | P2P CLIENT RULE | 28             | remote ip address                                                      | RW         | 4 BYTE          |
| <b>46371</b>            | 6370                   | P2P CLIENT RULE | 28             | remote tcp/ip port                                                     | RW         | UNSIGNED 16 BIT |
| <b>46372</b>            | 6371                   | P2P CLIENT RULE | 28             | 0=enabled send on timeout, 1=enebaled send on timeout or changed value | RW         | UNSIGNED 16 BIT |
| <b>46373</b>            | 6372                   | P2P CLIENT RULE | 28             | timeout value [ms]                                                     | RW         | UNSIGNED 16 BIT |
| <b>46377</b>            | 6376                   | P2P CLIENT RULE | 28             | ack packet 0=disabled, 1=enabled                                       | RW         | UNSIGNED 16 BIT |
| <b>46397</b>            | 6396                   | P2P CLIENT RULE | 29             | 0=disabled, else enabled                                               | RW         | UNSIGNED 16 BIT |
| <b>46398</b>            | 6397                   | P2P CLIENT RULE | 29             | local channel value Din1..Din32 (0..31)                                | RW         | UNSIGNED 16 BIT |
| <b>46399-46402</b>      | 6398-6401              | P2P CLIENT RULE | 29             | remote ip address                                                      | RW         | 4 BYTE          |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>(4x)</b> | <b>REGISTER</b> | <b>CHANNEL</b> | <b>DESCRIPTION</b>                                                              | <b>W/R</b> | <b>TYPE</b>        |
|-------------------------|------------------------|-----------------|----------------|---------------------------------------------------------------------------------|------------|--------------------|
| <b>46403</b>            | 6402                   | P2P CLIENT RULE | 29             | remote tcp/ip port                                                              | RW         | UNSIGNED<br>16 BIT |
| <b>46404</b>            | 6403                   | P2P CLIENT RULE | 29             | 0=enabled send on<br>timeout, 1=enebaled send<br>on timeout or changed<br>value | RW         | UNSIGNED<br>16 BIT |
| <b>46405</b>            | 6404                   | P2P CLIENT RULE | 29             | timeout value [ms]                                                              | RW         | UNSIGNED<br>16 BIT |
| <b>46409</b>            | 6408                   | P2P CLIENT RULE | 29             | ack packet 0=disabled,<br>1=enabled                                             | RW         | UNSIGNED<br>16 BIT |
| <b>46429</b>            | 6428                   | P2P CLIENT RULE | 30             | 0=disabled, else enabled                                                        | RW         | UNSIGNED<br>16 BIT |
| <b>46430</b>            | 6429                   | P2P CLIENT RULE | 30             | local channel value<br>Din1..Din32 (0..31)                                      | RW         | UNSIGNED<br>16 BIT |
| <b>46431-<br/>46434</b> | 6430-<br>6433          | P2P CLIENT RULE | 30             | remote ip address                                                               | RW         | 4 BYTE             |
| <b>46435</b>            | 6434                   | P2P CLIENT RULE | 30             | remote tcp/ip port                                                              | RW         | UNSIGNED<br>16 BIT |
| <b>46436</b>            | 6435                   | P2P CLIENT RULE | 30             | 0=enabled send on<br>timeout, 1=enebaled send<br>on timeout or changed<br>value | RW         | UNSIGNED<br>16 BIT |
| <b>46437</b>            | 6436                   | P2P CLIENT RULE | 30             | timeout value [ms]                                                              | RW         | UNSIGNED<br>16 BIT |
| <b>46441</b>            | 6440                   | P2P CLIENT RULE | 30             | ack packet 0=disabled,<br>1=enabled                                             | RW         | UNSIGNED<br>16 BIT |
| <b>46461</b>            | 6460                   | P2P CLIENT RULE | 31             | 0=disabled, else enabled                                                        | RW         | UNSIGNED<br>16 BIT |
| <b>46462</b>            | 6461                   | P2P CLIENT RULE | 31             | local channel value<br>Din1..Din32 (0..31)                                      | RW         | UNSIGNED<br>16 BIT |
| <b>46463-<br/>46466</b> | 6462-<br>6465          | P2P CLIENT RULE | 31             | remote ip address                                                               | RW         | 4 BYTE             |
| <b>46467</b>            | 6466                   | P2P CLIENT RULE | 31             | remote tcp/ip port                                                              | RW         | UNSIGNED<br>16 BIT |
| <b>46468</b>            | 6467                   | P2P CLIENT RULE | 31             | 0=enabled send on<br>timeout, 1=enebaled send<br>on timeout or changed<br>value | RW         | UNSIGNED<br>16 BIT |
| <b>46469</b>            | 6468                   | P2P CLIENT RULE | 31             | timeout value [ms]                                                              | RW         | UNSIGNED<br>16 BIT |
| <b>46473</b>            | 6472                   | P2P CLIENT RULE | 32             | ack packet 0=disabled,<br>1=enabled                                             | RW         | UNSIGNED<br>16 BIT |
| <b>46493</b>            | 6492                   | P2P CLIENT RULE | 32             | 0=disabled, else enabled                                                        | RW         | UNSIGNED<br>16 BIT |
| <b>46494</b>            | 6493                   | P2P CLIENT RULE | 32             | local channel value<br>Din1..Din32 (0..31)                                      | RW         | UNSIGNED<br>16 BIT |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>(4x)</b> | <b>REGISTER</b> | <b>CHANNEL</b> | <b>DESCRIPTION</b>                                                    | <b>W/R</b> | <b>TYPE</b>        |
|-------------------------|------------------------|-----------------|----------------|-----------------------------------------------------------------------|------------|--------------------|
| <b>46495-46498</b>      | 6494-6497              | P2P CLIENT RULE | 32             | remote ip address                                                     | RW         | 4 BYTE             |
| <b>46499</b>            | 6498                   | P2P CLIENT RULE | 32             | remote tcp/ip port                                                    | RW         | UNSIGNED<br>16 BIT |
| <b>46500</b>            | 6499                   | P2P CLIENT RULE | 32             | 0=enabled send on timeout, 1=enebale send on timeout or changed value | RW         | UNSIGNED<br>16 BIT |
| <b>46501</b>            | 6500                   | P2P CLIENT RULE | 32             | timeout value [ms]                                                    | RW         | UNSIGNED<br>16 BIT |
| <b>46505</b>            | 6504                   | P2P CLIENT RULE | 32             | ack packet 0=disabled, 1=enabled                                      | RW         | UNSIGNED<br>16 BIT |
| <b>47001</b>            | 7000                   | P2P SERVER RULE | 1              | 0=disabled, else enabled                                              | RW         | UNSIGNED<br>16 BIT |
| <b>47002</b>            | 7001                   | P2P SERVER RULE | 1              | remote channel value<br>Din1..Din32 (0..31)                           | RW         | UNSIGNED<br>16 BIT |
| <b>47003-47006</b>      | 7002-7005              | P2P SERVER RULE | 1              | remote ip address                                                     | RW         | 4 BYTE             |
| <b>47007</b>            | 7006                   | P2P SERVER RULE | 1              | local channel value<br>Din1..Din32 (0..31)                            | RW         | UNSIGNED<br>16 BIT |
| <b>47017</b>            | 7016                   | P2P SERVER RULE | 2              | 0=disabled, else enabled                                              | RW         | UNSIGNED<br>16 BIT |
| <b>47018</b>            | 7017                   | P2P SERVER RULE | 2              | remote channel value<br>Din1..Din32 (0..31)                           | RW         | UNSIGNED<br>16 BIT |
| <b>47019-47022</b>      | 7018-7021              | P2P SERVER RULE | 2              | remote ip address                                                     | RW         | 4 BYTE             |
| <b>47023</b>            | 7022                   | P2P SERVER RULE | 2              | local channel value<br>Din1..Din32 (0..31)                            | RW         | UNSIGNED<br>16 BIT |
| <b>47033</b>            | 7032                   | P2P SERVER RULE | 3              | 0=disabled, else enabled                                              | RW         | UNSIGNED<br>16 BIT |
| <b>47034</b>            | 7033                   | P2P SERVER RULE | 3              | remote channel value<br>Din1..Din32 (0..31)                           | RW         | UNSIGNED<br>16 BIT |
| <b>47035-47038</b>      | 7034-7037              | P2P SERVER RULE | 3              | remote ip address                                                     | RW         | 4 BYTE             |
| <b>47039</b>            | 7038                   | P2P SERVER RULE | 3              | local channel value<br>Din1..Din32 (0..31)                            | RW         | UNSIGNED<br>16 BIT |
| <b>47049</b>            | 7048                   | P2P SERVER RULE | 4              | 0=disabled, else enabled                                              | RW         | UNSIGNED<br>16 BIT |
| <b>47050</b>            | 7049                   | P2P SERVER RULE | 4              | remote channel value<br>Din1..Din32 (0..31)                           | RW         | UNSIGNED<br>16 BIT |
| <b>47051-47054</b>      | 7050-7053              | P2P SERVER RULE | 4              | remote ip address                                                     | RW         | 4 BYTE             |
| <b>47055</b>            | 7054                   | P2P SERVER RULE | 4              | local channel value<br>Din1..Din32 (0..31)                            | RW         | UNSIGNED<br>16 BIT |
| <b>47065</b>            | 7064                   | P2P SERVER RULE | 5              | 0=disabled, else enabled                                              | RW         | UNSIGNED<br>16 BIT |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>(4x)</b> | <b>REGISTER</b> | <b>CHANNEL</b> | <b>DESCRIPTION</b>                          | <b>W/R</b> | <b>TYPE</b>        |
|-------------------------|------------------------|-----------------|----------------|---------------------------------------------|------------|--------------------|
| <b>47066</b>            | 7065                   | P2P SERVER RULE | 5              | remote channel value<br>Din1..Din32 (0..31) | RW         | UNSIGNED<br>16 BIT |
| <b>47067-<br/>47070</b> | 7066-<br>7069          | P2P SERVER RULE | 5              | remote ip address                           | RW         | 4 BYTE             |
| <b>47071</b>            | 7070                   | P2P SERVER RULE | 5              | local channel value<br>Din1..Din32 (0..31)  | RW         | UNSIGNED<br>16 BIT |
| <b>47081</b>            | 7080                   | P2P SERVER RULE | 6              | 0=disabled, else enabled                    | RW         | UNSIGNED<br>16 BIT |
| <b>47082</b>            | 7081                   | P2P SERVER RULE | 6              | remote channel value<br>Din1..Din32 (0..31) | RW         | UNSIGNED<br>16 BIT |
| <b>47083-<br/>47086</b> | 7082-<br>7085          | P2P SERVER RULE | 6              | remote ip address                           | RW         | 4 BYTE             |
| <b>47087</b>            | 7086                   | P2P SERVER RULE | 6              | local channel value<br>Din1..Din32 (0..31)  | RW         | UNSIGNED<br>16 BIT |
| <b>47097</b>            | 7096                   | P2P SERVER RULE | 7              | 0=disabled, else enabled                    | RW         | UNSIGNED<br>16 BIT |
| <b>47098</b>            | 7097                   | P2P SERVER RULE | 7              | remote channel value<br>Din1..Din32 (0..31) | RW         | UNSIGNED<br>16 BIT |
| <b>47099-<br/>47102</b> | 7098-<br>7101          | P2P SERVER RULE | 7              | remote ip address                           | RW         | 4 BYTE             |
| <b>47103</b>            | 7102                   | P2P SERVER RULE | 7              | local channel value<br>Din1..Din32 (0..31)  | RW         | UNSIGNED<br>16 BIT |
| <b>47113</b>            | 7112                   | P2P SERVER RULE | 8              | 0=disabled, else enabled                    | RW         | UNSIGNED<br>16 BIT |
| <b>47114</b>            | 7113                   | P2P SERVER RULE | 8              | remote channel value<br>Din1..Din32 (0..31) | RW         | UNSIGNED<br>16 BIT |
| <b>47115-<br/>47118</b> | 7114-<br>7117          | P2P SERVER RULE | 8              | remote ip address                           | RW         | 4 BYTE             |
| <b>47119</b>            | 7118                   | P2P SERVER RULE | 8              | local channel value<br>Din1..Din32 (0..31)  | RW         | UNSIGNED<br>16 BIT |
| <b>47129</b>            | 7128                   | P2P SERVER RULE | 9              | 0=disabled, else enabled                    | RW         | UNSIGNED<br>16 BIT |
| <b>47130</b>            | 7129                   | P2P SERVER RULE | 9              | remote channel value<br>Din1..Din32 (0..31) | RW         | UNSIGNED<br>16 BIT |
| <b>47131-<br/>47134</b> | 7130-<br>7133          | P2P SERVER RULE | 9              | remote ip address                           | RW         | 4 BYTE             |
| <b>47135</b>            | 7134                   | P2P SERVER RULE | 9              | local channel value<br>Din1..Din32 (0..31)  | RW         | UNSIGNED<br>16 BIT |
| <b>47145</b>            | 7144                   | P2P SERVER RULE | 10             | 0=disabled, else enabled                    | RW         | UNSIGNED<br>16 BIT |
| <b>47146</b>            | 7145                   | P2P SERVER RULE | 10             | remote channel value<br>Din1..Din32 (0..31) | RW         | UNSIGNED<br>16 BIT |
| <b>47147-<br/>47150</b> | 7146-<br>7149          | P2P SERVER RULE | 10             | remote ip address                           | RW         | 4 BYTE             |
| <b>47151</b>            | 7150                   | P2P SERVER RULE | 10             | local channel value<br>Din1..Din32 (0..31)  | RW         | UNSIGNED<br>16 BIT |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>(4x)</b> | <b>REGISTER</b> | <b>CHANNEL</b> | <b>DESCRIPTION</b>                          | <b>W/R</b> | <b>TYPE</b>        |
|-------------------------|------------------------|-----------------|----------------|---------------------------------------------|------------|--------------------|
| <b>47161</b>            | 7160                   | P2P SERVER RULE | 11             | 0=disabled, else enabled                    | RW         | UNSIGNED<br>16 BIT |
| <b>47162</b>            | 7161                   | P2P SERVER RULE | 11             | remote channel value<br>Din1..Din32 (0..31) | RW         | UNSIGNED<br>16 BIT |
| <b>47163-<br/>47166</b> | 7162-<br>7165          | P2P SERVER RULE | 11             | remote ip address                           | RW         | 4 BYTE             |
| <b>47167</b>            | 7166                   | P2P SERVER RULE | 11             | local channel value<br>Din1..Din32 (0..31)  | RW         | UNSIGNED<br>16 BIT |
| <b>47177</b>            | 7176                   | P2P SERVER RULE | 12             | 0=disabled, else enabled                    | RW         | UNSIGNED<br>16 BIT |
| <b>47178</b>            | 7177                   | P2P SERVER RULE | 12             | remote channel value<br>Din1..Din32 (0..31) | RW         | UNSIGNED<br>16 BIT |
| <b>47179-<br/>47182</b> | 7178-<br>7181          | P2P SERVER RULE | 12             | remote ip address                           | RW         | 4 BYTE             |
| <b>47183</b>            | 7182                   | P2P SERVER RULE | 12             | local channel value<br>Din1..Din32 (0..31)  | RW         | UNSIGNED<br>16 BIT |
| <b>47193</b>            | 7192                   | P2P SERVER RULE | 13             | 0=disabled, else enabled                    | RW         | UNSIGNED<br>16 BIT |
| <b>47194</b>            | 7193                   | P2P SERVER RULE | 13             | remote channel value<br>Din1..Din32 (0..31) | RW         | UNSIGNED<br>16 BIT |
| <b>47195-<br/>47198</b> | 7194-<br>7197          | P2P SERVER RULE | 13             | remote ip address                           | RW         | 4 BYTE             |
| <b>47199</b>            | 7198                   | P2P SERVER RULE | 13             | local channel value<br>Din1..Din32 (0..31)  | RW         | UNSIGNED<br>16 BIT |
| <b>47209</b>            | 7208                   | P2P SERVER RULE | 14             | 0=disabled, else enabled                    | RW         | UNSIGNED<br>16 BIT |
| <b>47210</b>            | 7209                   | P2P SERVER RULE | 14             | remote channel value<br>Din1..Din32 (0..31) | RW         | UNSIGNED<br>16 BIT |
| <b>47211-<br/>47214</b> | 7210-<br>7213          | P2P SERVER RULE | 14             | remote ip address                           | RW         | 4 BYTE             |
| <b>47215</b>            | 7214                   | P2P SERVER RULE | 14             | local channel value<br>Din1..Din32 (0..31)  | RW         | UNSIGNED<br>16 BIT |
| <b>47225</b>            | 7224                   | P2P SERVER RULE | 15             | 0=disabled, else enabled                    | RW         | UNSIGNED<br>16 BIT |
| <b>47226</b>            | 7225                   | P2P SERVER RULE | 15             | remote channel value<br>Din1..Din32 (0..31) | RW         | UNSIGNED<br>16 BIT |
| <b>47227-<br/>47230</b> | 7226-<br>7229          | P2P SERVER RULE | 15             | remote ip address                           | RW         | 4 BYTE             |
| <b>47231</b>            | 7230                   | P2P SERVER RULE | 15             | local channel value<br>Din1..Din32 (0..31)  | RW         | UNSIGNED<br>16 BIT |
| <b>47241</b>            | 7240                   | P2P SERVER RULE | 16             | 0=disabled, else enabled                    | RW         | UNSIGNED<br>16 BIT |
| <b>47242</b>            | 7241                   | P2P SERVER RULE | 16             | remote channel value<br>Din1..Din32 (0..31) | RW         | UNSIGNED<br>16 BIT |
| <b>47243-<br/>47246</b> | 7232-<br>45            | P2P SERVER RULE | 16             | remote ip address                           | RW         | 4 BYTE             |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>(4x)</b> | <b>REGISTER</b> | <b>CHANNEL</b> | <b>DESCRIPTION</b>                          | <b>W/R</b> | <b>TYPE</b>        |
|-------------------------|------------------------|-----------------|----------------|---------------------------------------------|------------|--------------------|
| <b>47247</b>            | 7246                   | P2P SERVER RULE | 16             | local channel value<br>Din1..Din32 (0..31)  | RW         | UNSIGNED<br>16 BIT |
| <b>47257</b>            | 7256                   | P2P SERVER RULE | 17             | 0=disabled, else enabled                    | RW         | UNSIGNED<br>16 BIT |
| <b>47258</b>            | 7257                   | P2P SERVER RULE | 17             | remote channel value<br>Din1..Din32 (0..31) | RW         | UNSIGNED<br>16 BIT |
| <b>47259-<br/>47262</b> | 7258-<br>7261          | P2P SERVER RULE | 17             | remote ip address                           | RW         | 4 BYTE             |
| <b>47263</b>            | 7262                   | P2P SERVER RULE | 17             | local channel value<br>Din1..Din32 (0..31)  | RW         | UNSIGNED<br>16 BIT |
| <b>47273</b>            | 7272                   | P2P SERVER RULE | 18             | 0=disabled, else enabled                    | RW         | UNSIGNED<br>16 BIT |
| <b>47274</b>            | 7273                   | P2P SERVER RULE | 18             | remote channel value<br>Din1..Din32 (0..31) | RW         | UNSIGNED<br>16 BIT |
| <b>47275-<br/>47278</b> | 7274-<br>7277          | P2P SERVER RULE | 18             | remote ip address                           | RW         | 4 BYTE             |
| <b>47279</b>            | 7278                   | P2P SERVER RULE | 18             | local channel value<br>Din1..Din32 (0..31)  | RW         | UNSIGNED<br>16 BIT |
| <b>47289</b>            | 7288                   | P2P SERVER RULE | 19             | 0=disabled, else enabled                    | RW         | UNSIGNED<br>16 BIT |
| <b>47290</b>            | 7289                   | P2P SERVER RULE | 19             | remote channel value<br>Din1..Din32 (0..31) | RW         | UNSIGNED<br>16 BIT |
| <b>47291-<br/>47294</b> | 7290-<br>7293          | P2P SERVER RULE | 19             | remote ip address                           | RW         | 4 BYTE             |
| <b>47295</b>            | 7294                   | P2P SERVER RULE | 19             | local channel value<br>Din1..Din32 (0..31)  | RW         | UNSIGNED<br>16 BIT |
| <b>47305</b>            | 7304                   | P2P SERVER RULE | 20             | 0=disabled, else enabled                    | RW         | UNSIGNED<br>16 BIT |
| <b>47306</b>            | 7305                   | P2P SERVER RULE | 20             | remote channel value<br>Din1..Din32 (0..31) | RW         | UNSIGNED<br>16 BIT |
| <b>47307-<br/>47310</b> | 7306-<br>7309          | P2P SERVER RULE | 20             | remote ip address                           | RW         | 4 BYTE             |
| <b>47311</b>            | 7310                   | P2P SERVER RULE | 20             | local channel value<br>Din1..Din32 (0..31)  | RW         | UNSIGNED<br>16 BIT |
| <b>47321</b>            | 7320                   | P2P SERVER RULE | 21             | 0=disabled, else enabled                    | RW         | UNSIGNED<br>16 BIT |
| <b>47322</b>            | 7321                   | P2P SERVER RULE | 21             | remote channel value<br>Din1..Din32 (0..31) | RW         | UNSIGNED<br>16 BIT |
| <b>47323-<br/>47326</b> | 7322-<br>7325          | P2P SERVER RULE | 21             | remote ip address                           | RW         | 4 BYTE             |
| <b>47327</b>            | 7326                   | P2P SERVER RULE | 21             | local channel value<br>Din1..Din32 (0..31)  | RW         | UNSIGNED<br>16 BIT |
| <b>47337</b>            | 7336                   | P2P SERVER RULE | 22             | 0=disabled, else enabled                    | RW         | UNSIGNED<br>16 BIT |
| <b>47338</b>            | 7337                   | P2P SERVER RULE | 22             | remote channel value<br>Din1..Din32 (0..31) | RW         | UNSIGNED<br>16 BIT |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>(4x)</b> | <b>REGISTER</b> | <b>CHANNEL</b> | <b>DESCRIPTION</b>                          | <b>W/R</b> | <b>TYPE</b>        |
|-------------------------|------------------------|-----------------|----------------|---------------------------------------------|------------|--------------------|
| <b>47339-47342</b>      | 7338-7341              | P2P SERVER RULE | 22             | remote ip address                           | RW         | 4 BYTE             |
| <b>47343</b>            | 7342                   | P2P SERVER RULE | 22             | local channel value<br>Din1..Din32 (0..31)  | RW         | UNSIGNED<br>16 BIT |
| <b>47353</b>            | 7352                   | P2P SERVER RULE | 23             | 0=disabled, else enabled                    | RW         | UNSIGNED<br>16 BIT |
| <b>47354</b>            | 7353                   | P2P SERVER RULE | 23             | remote channel value<br>Din1..Din32 (0..31) | RW         | UNSIGNED<br>16 BIT |
| <b>47355-47358</b>      | 7354-7357              | P2P SERVER RULE | 23             | remote ip address                           | RW         | 4 BYTE             |
| <b>47359</b>            | 7358                   | P2P SERVER RULE | 23             | local channel value<br>Din1..Din32 (0..31)  | RW         | UNSIGNED<br>16 BIT |
| <b>47369</b>            | 7368                   | P2P SERVER RULE | 24             | 0=disabled, else enabled                    | RW         | UNSIGNED<br>16 BIT |
| <b>47370</b>            | 7369                   | P2P SERVER RULE | 24             | remote channel value<br>Din1..Din32 (0..31) | RW         | UNSIGNED<br>16 BIT |
| <b>47371-47374</b>      | 7370-7373              | P2P SERVER RULE | 24             | remote ip address                           | RW         | 4 BYTE             |
| <b>47375</b>            | 7374                   | P2P SERVER RULE | 24             | local channel value<br>Din1..Din32 (0..31)  | RW         | UNSIGNED<br>16 BIT |
| <b>47385</b>            | 7384                   | P2P SERVER RULE | 25             | 0=disabled, else enabled                    | RW         | UNSIGNED<br>16 BIT |
| <b>47386</b>            | 7385                   | P2P SERVER RULE | 25             | remote channel value<br>Din1..Din32 (0..31) | RW         | UNSIGNED<br>16 BIT |
| <b>47387-47390</b>      | 7386-7389              | P2P SERVER RULE | 25             | remote ip address                           | RW         | 4 BYTE             |
| <b>47391</b>            | 7390                   | P2P SERVER RULE | 25             | local channel value<br>Din1..Din32 (0..31)  | RW         | UNSIGNED<br>16 BIT |
| <b>47401</b>            | 7400                   | P2P SERVER RULE | 26             | 0=disabled, else enabled                    | RW         | UNSIGNED<br>16 BIT |
| <b>47402</b>            | 7401                   | P2P SERVER RULE | 26             | remote channel value<br>Din1..Din32 (0..31) | RW         | UNSIGNED<br>16 BIT |
| <b>47403-47406</b>      | 7402-7405              | P2P SERVER RULE | 26             | remote ip address                           | RW         | 4 BYTE             |
| <b>47407</b>            | 7406                   | P2P SERVER RULE | 26             | local channel value<br>Din1..Din32 (0..31)  | RW         | UNSIGNED<br>16 BIT |
| <b>47417</b>            | 7416                   | P2P SERVER RULE | 27             | 0=disabled, else enabled                    | RW         | UNSIGNED<br>16 BIT |
| <b>47418</b>            | 7417                   | P2P SERVER RULE | 27             | remote channel value<br>Din1..Din32 (0..31) | RW         | UNSIGNED<br>16 BIT |
| <b>47419-47422</b>      | 7418-7421              | P2P SERVER RULE | 27             | remote ip address                           | RW         | 4 BYTE             |
| <b>47423</b>            | 7422                   | P2P SERVER RULE | 27             | local channel value<br>Din1..Din32 (0..31)  | RW         | UNSIGNED<br>16 BIT |
| <b>47433</b>            | 7432                   | P2P SERVER RULE | 28             | 0=disabled, else enabled                    | RW         | UNSIGNED<br>16 BIT |



| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>(4x)</b> | <b>REGISTER</b> | <b>CHANNEL</b> | <b>DESCRIPTION</b>                          | <b>W/R</b> | <b>TYPE</b>        |
|-------------------------|------------------------|-----------------|----------------|---------------------------------------------|------------|--------------------|
| <b>47434</b>            | 7433                   | P2P SERVER RULE | 28             | remote channel value<br>Din1..Din32 (0..31) | RW         | UNSIGNED<br>16 BIT |
| <b>47435-<br/>47438</b> | 7434-<br>7437          | P2P SERVER RULE | 28             | remote ip address                           | RW         | 4 BYTE             |
| <b>47439</b>            | 7438                   | P2P SERVER RULE | 28             | local channel value<br>Din1..Din32 (0..31)  | RW         | UNSIGNED<br>16 BIT |
| <b>47449</b>            | 7448                   | P2P SERVER RULE | 29             | 0=disabled, else enabled                    | RW         | UNSIGNED<br>16 BIT |
| <b>47450</b>            | 7449                   | P2P SERVER RULE | 29             | remote channel value<br>Din1..Din32 (0..31) | RW         | UNSIGNED<br>16 BIT |
| <b>47451-<br/>47454</b> | 7450-<br>7453          | P2P SERVER RULE | 29             | remote ip address                           | RW         | 4 BYTE             |
| <b>47455</b>            | 7454                   | P2P SERVER RULE | 29             | local channel value<br>Din1..Din32 (0..31)  | RW         | UNSIGNED<br>16 BIT |
| <b>47465</b>            | 7464                   | P2P SERVER RULE | 30             | 0=disabled, else enabled                    | RW         | UNSIGNED<br>16 BIT |
| <b>47466</b>            | 7465                   | P2P SERVER RULE | 30             | remote channel value<br>Din1..Din32 (0..31) | RW         | UNSIGNED<br>16 BIT |
| <b>47467-<br/>47470</b> | 7466-<br>7469          | P2P SERVER RULE | 30             | remote ip address                           | RW         | 4 BYTE             |
| <b>47471</b>            | 7470                   | P2P SERVER RULE | 30             | local channel value<br>Din1..Din32 (0..31)  | RW         | UNSIGNED<br>16 BIT |
| <b>47481</b>            | 7480                   | P2P SERVER RULE | 31             | 0=disabled, else enabled                    | RW         | UNSIGNED<br>16 BIT |
| <b>47482</b>            | 7481                   | P2P SERVER RULE | 31             | remote channel value<br>Din1..Din32 (0..31) | RW         | UNSIGNED<br>16 BIT |
| <b>47483-<br/>47486</b> | 7482-<br>7485          | P2P SERVER RULE | 31             | remote ip address                           | RW         | 4 BYTE             |
| <b>47487</b>            | 7486                   | P2P SERVER RULE | 31             | local channel value<br>Din1..Din32 (0..31)  | RW         | UNSIGNED<br>16 BIT |
| <b>47497</b>            | 7496                   | P2P SERVER RULE | 32             | 0=disabled, else enabled                    | RW         | UNSIGNED<br>16 BIT |
| <b>47498</b>            | 7497                   | P2P SERVER RULE | 32             | remote channel value<br>Din1..Din32 (0..31) | RW         | UNSIGNED<br>16 BIT |
| <b>47499-<br/>47502</b> | 7498-<br>7501          | P2P SERVER RULE | 32             | remote ip address                           | RW         | 4 BYTE             |
| <b>47503</b>            | 7502                   | P2P SERVER RULE | 32             | local channel value<br>Din1..Din32 (0..31)  | RW         | UNSIGNED<br>16 BIT |



**21.3. R-32DIDO: TABLE OF MODBUS REGISTERS 0x COIL STATUS (FUNCTION CODE 1)**

| <b>ADDRESS (0x)</b> | <b>ADDRESS (0x) OFFSET</b> | <b>REGISTER</b> | <b>CHANNEL</b> | <b>DESCRIPTION</b> | <b>W/R</b> | <b>TYPE</b> |
|---------------------|----------------------------|-----------------|----------------|--------------------|------------|-------------|
| <b>1</b>            | <b>0</b>                   | DIGITAL I/O     | <b>1</b>       | DIGITAL I/O        | RW         | BIT         |
| <b>2</b>            | <b>1</b>                   | DIGITAL I/O     | <b>2</b>       | DIGITAL I/O        | RW         | BIT         |
| <b>3</b>            | <b>2</b>                   | DIGITAL I/O     | <b>3</b>       | DIGITAL I/O        | RW         | BIT         |
| <b>4</b>            | <b>3</b>                   | DIGITAL I/O     | <b>4</b>       | DIGITAL I/O        | RW         | BIT         |
| <b>5</b>            | <b>4</b>                   | DIGITAL I/O     | <b>5</b>       | DIGITAL I/O        | RW         | BIT         |
| <b>6</b>            | <b>5</b>                   | DIGITAL I/O     | <b>6</b>       | DIGITAL I/O        | RW         | BIT         |
| <b>7</b>            | <b>6</b>                   | DIGITAL I/O     | <b>7</b>       | DIGITAL I/O        | RW         | BIT         |
| <b>8</b>            | <b>7</b>                   | DIGITAL I/O     | <b>8</b>       | DIGITAL I/O        | RW         | BIT         |
| <b>9</b>            | <b>8</b>                   | DIGITAL I/O     | <b>9</b>       | DIGITAL I/O        | RW         | BIT         |
| <b>10</b>           | <b>9</b>                   | DIGITAL I/O     | <b>10</b>      | DIGITAL I/O        | RW         | BIT         |
| <b>11</b>           | <b>10</b>                  | DIGITAL I/O     | <b>11</b>      | DIGITAL I/O        | RW         | BIT         |
| <b>12</b>           | <b>11</b>                  | DIGITAL I/O     | <b>12</b>      | DIGITAL I/O        | RW         | BIT         |
| <b>13</b>           | <b>12</b>                  | DIGITAL I/O     | <b>13</b>      | DIGITAL I/O        | RW         | BIT         |
| <b>14</b>           | <b>13</b>                  | DIGITAL I/O     | <b>14</b>      | DIGITAL I/O        | RW         | BIT         |
| <b>15</b>           | <b>14</b>                  | DIGITAL I/O     | <b>15</b>      | DIGITAL I/O        | RW         | BIT         |
| <b>16</b>           | <b>15</b>                  | DIGITAL I/O     | <b>16</b>      | DIGITAL I/O        | RW         | BIT         |
| <b>17</b>           | <b>16</b>                  | DIGITAL I/O     | <b>17</b>      | DIGITAL I/O        | RW         | BIT         |
| <b>18</b>           | <b>17</b>                  | DIGITAL I/O     | <b>18</b>      | DIGITAL I/O        | RW         | BIT         |
| <b>19</b>           | <b>18</b>                  | DIGITAL I/O     | <b>19</b>      | DIGITAL I/O        | RW         | BIT         |
| <b>20</b>           | <b>19</b>                  | DIGITAL I/O     | <b>20</b>      | DIGITAL I/O        | RW         | BIT         |
| <b>21</b>           | <b>20</b>                  | DIGITAL I/O     | <b>21</b>      | DIGITAL I/O        | RW         | BIT         |
| <b>22</b>           | <b>21</b>                  | DIGITAL I/O     | <b>22</b>      | DIGITAL I/O        | RW         | BIT         |
| <b>23</b>           | <b>22</b>                  | DIGITAL I/O     | <b>23</b>      | DIGITAL I/O        | RW         | BIT         |
| <b>24</b>           | <b>23</b>                  | DIGITAL I/O     | <b>24</b>      | DIGITAL I/O        | RW         | BIT         |
| <b>25</b>           | <b>24</b>                  | DIGITAL I/O     | <b>25</b>      | DIGITAL I/O        | RW         | BIT         |
| <b>26</b>           | <b>25</b>                  | DIGITAL I/O     | <b>26</b>      | DIGITAL I/O        | RW         | BIT         |
| <b>27</b>           | <b>26</b>                  | DIGITAL I/O     | <b>27</b>      | DIGITAL I/O        | RW         | BIT         |
| <b>28</b>           | <b>27</b>                  | DIGITAL I/O     | <b>28</b>      | DIGITAL I/O        | RW         | BIT         |
| <b>29</b>           | <b>28</b>                  | DIGITAL I/O     | <b>29</b>      | DIGITAL I/O        | RW         | BIT         |
| <b>30</b>           | <b>29</b>                  | DIGITAL I/O     | <b>30</b>      | DIGITAL I/O        | RW         | BIT         |
| <b>31</b>           | <b>30</b>                  | DIGITAL I/O     | <b>31</b>      | DIGITAL I/O        | RW         | BIT         |
| <b>32</b>           | <b>31</b>                  | DIGITAL I/O     | <b>32</b>      | DIGITAL I/O        | RW         | BIT         |

**21.4. R-32DIDO: TABLE OF MODBUS REGISTERS 1x INPUT STATUS (FUNCTION CODE 2)**

| <b>ADDRESS (1x)</b> | <b>ADDRESS (0x) OFFSET</b> | <b>REGISTER</b> | <b>CHANNEL</b> | <b>DESCRIPTION</b> | <b>W/R</b> | <b>TYPE</b> |
|---------------------|----------------------------|-----------------|----------------|--------------------|------------|-------------|
| 10001               | 0                          | DIGITAL I/O     | 1              | DIGITAL I/O        | RW         | BIT         |
| 10002               | 1                          | DIGITAL I/O     | 2              | DIGITAL I/O        | RW         | BIT         |
| 10003               | 2                          | DIGITAL I/O     | 3              | DIGITAL I/O        | RW         | BIT         |
| 10004               | 3                          | DIGITAL I/O     | 4              | DIGITAL I/O        | RW         | BIT         |
| 10005               | 4                          | DIGITAL I/O     | 5              | DIGITAL I/O        | RW         | BIT         |
| 10006               | 5                          | DIGITAL I/O     | 6              | DIGITAL I/O        | RW         | BIT         |
| 10007               | 6                          | DIGITAL I/O     | 7              | DIGITAL I/O        | RW         | BIT         |
| 10008               | 7                          | DIGITAL I/O     | 8              | DIGITAL I/O        | RW         | BIT         |
| 10009               | 8                          | DIGITAL I/O     | 9              | DIGITAL I/O        | RW         | BIT         |
| 10010               | 9                          | DIGITAL I/O     | 10             | DIGITAL I/O        | RW         | BIT         |
| 10011               | 10                         | DIGITAL I/O     | 11             | DIGITAL I/O        | RW         | BIT         |
| 10012               | 11                         | DIGITAL I/O     | 12             | DIGITAL I/O        | RW         | BIT         |
| 10013               | 12                         | DIGITAL I/O     | 13             | DIGITAL I/O        | RW         | BIT         |
| 10014               | 13                         | DIGITAL I/O     | 14             | DIGITAL I/O        | RW         | BIT         |
| 10015               | 14                         | DIGITAL I/O     | 15             | DIGITAL I/O        | RW         | BIT         |
| 10016               | 15                         | DIGITAL I/O     | 16             | DIGITAL I/O        | RW         | BIT         |
| 10017               | 16                         | DIGITAL I/O     | 17             | DIGITAL I/O        | RW         | BIT         |
| 10018               | 17                         | DIGITAL I/O     | 18             | DIGITAL I/O        | RW         | BIT         |
| 10019               | 18                         | DIGITAL I/O     | 19             | DIGITAL I/O        | RW         | BIT         |
| 10020               | 19                         | DIGITAL I/O     | 20             | DIGITAL I/O        | RW         | BIT         |
| 10021               | 20                         | DIGITAL I/O     | 21             | DIGITAL I/O        | RW         | BIT         |
| 10022               | 21                         | DIGITAL I/O     | 22             | DIGITAL I/O        | RW         | BIT         |
| 10023               | 22                         | DIGITAL I/O     | 23             | DIGITAL I/O        | RW         | BIT         |
| 10024               | 23                         | DIGITAL I/O     | 24             | DIGITAL I/O        | RW         | BIT         |
| 10025               | 24                         | DIGITAL I/O     | 25             | DIGITAL I/O        | RW         | BIT         |
| 10026               | 25                         | DIGITAL I/O     | 26             | DIGITAL I/O        | RW         | BIT         |
| 10027               | 26                         | DIGITAL I/O     | 27             | DIGITAL I/O        | RW         | BIT         |
| 10028               | 27                         | DIGITAL I/O     | 28             | DIGITAL I/O        | RW         | BIT         |
| 10029               | 28                         | DIGITAL I/O     | 29             | DIGITAL I/O        | RW         | BIT         |
| 10030               | 29                         | DIGITAL I/O     | 30             | DIGITAL I/O        | RW         | BIT         |
| 10031               | 30                         | DIGITAL I/O     | 31             | DIGITAL I/O        | RW         | BIT         |
| 10032               | 31                         | DIGITAL I/O     | 32             | DIGITAL I/O        | RW         | BIT         |

## 22. MODBUS REGISTER TABLE FOR THE R-16DI-8DO PRODUCT

### 22.1. R-16DI-8DO: MODBUS 4X HOLDING REGISTERS TABLE (FUNCTION CODE 3)

| ADDRESS<br>(4x) | OFFSET ADDRESS<br>(4x) | REGISTER          | CHANNEL | DESCRIPTION           | W/<br>R | TYPE        |
|-----------------|------------------------|-------------------|---------|-----------------------|---------|-------------|
| 40001           | 0                      | MACHINE-ID        | -       | DEVICE IDENTIFICATION | RO      | UNSIGNED 16 |
| 40002           | 1                      | FIRMWARE REVISION | -       | FIRMWARE REVISION     | RO      | UNSIGNED 16 |

| ADDRESS<br>(4x) | OFFSET ADDRESS<br>(4x) | REGISTER                  | CHANNEL  | DESCRIPTION                                                                                                                                                                                                | W/R | TYPE        |
|-----------------|------------------------|---------------------------|----------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----|-------------|
| 40017           | 16                     | COMMAND                   | -        | COMMAND REGISTER                                                                                                                                                                                           | RW  | UNSIGNED 16 |
| 40018           | 17                     | RESERVED                  | -        | RESERVED                                                                                                                                                                                                   | RO  | UNSIGNED 16 |
| 40019           | 18                     | RESERVED                  | -        | RESERVED                                                                                                                                                                                                   | RO  | UNSIGNED 16 |
| 40020           | 19                     | RESERVED                  | -        | RESERVED                                                                                                                                                                                                   | RO  | UNSIGNED 16 |
| 40021           | 20                     | DIGITAL INPUT<br>[16...1] | [1...16] | DIGITAL INPUTS [16... 1]<br>THE LEAST SIGNIFICANT BIT IS RELATIVE TO I01<br>EXAMPLE:<br>5 decimal =<br>0000 0000<br>0000 0101<br>binary => I01 =<br>High, I02 =<br>LOW, I03 =<br>HIGH, I04... I16<br>= LOW | RO  | UNSIGNED 16 |
| 40022           | 21                     | RESERVED                  | -        | RESERVED                                                                                                                                                                                                   | RO  | UNSIGNED 16 |
| 40023           | 22                     | DIGITAL OUT<br>[8...1]    | [8...1]  | DIGITAL OUTPUTS [8... 1]<br>THE LEAST SIGNIFICANT BIT IS RELATIVE TO D01<br>EXAMPLE:                                                                                                                       | RW  | UNSIGNED 16 |

|  |  |  |  |                                                                                                           |  |  |
|--|--|--|--|-----------------------------------------------------------------------------------------------------------|--|--|
|  |  |  |  | 5 decimal =<br>0000 0000<br>0000 0101<br>binary =><br>D01=High,<br>D02=LOW,<br>D03=HIGH,<br>D04...D08=LOW |  |  |
|--|--|--|--|-----------------------------------------------------------------------------------------------------------|--|--|

| ADDRESS<br>(4x) | OFFSET<br>ADDRESS (4x) | REGISTER                 | CHANNEL | DESCRIPTION                                                                                                                                                                                       | W/<br>R | TYPE           |
|-----------------|------------------------|--------------------------|---------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------|----------------|
| 40101           | 100                    | RESET_COUNTER<br>[1..16] | 16..1   | RESET A BIT OF THE i-TH<br>COUNTER<br>THE LEAST SIGNIFICANT<br>BIT RELATES<br>TO COUNTER 1<br>EXAMPLE:<br>5 decimal = 0000 0000<br>0000 0101 binary =><br>Resets the value of<br>counters 1 and 3 | RW      | UNSIGNED<br>16 |
| 40102           | 101                    | RESERVED                 | -       |                                                                                                                                                                                                   | RW      | UNSIGNED<br>16 |
| 40103           | 102                    | COUNTER                  | 1       | LSW                                                                                                                                                                                               | RW      | UNSIGNED<br>32 |
| 40104           | 103                    |                          |         | MSW                                                                                                                                                                                               | RW      |                |
| 40105           | 104                    | COUNTER                  | 2       | LSW                                                                                                                                                                                               | RW      | UNSIGNED<br>32 |
| 40106           | 105                    |                          |         | MSW                                                                                                                                                                                               | RW      |                |
| 40107           | 106                    | COUNTER                  | 3       | LSW                                                                                                                                                                                               | RW      | UNSIGNED<br>32 |
| 40108           | 107                    |                          |         | MSW                                                                                                                                                                                               | RW      |                |
| 40109           | 108                    | COUNTER                  | 4       | LSW                                                                                                                                                                                               | RW      | UNSIGNED<br>32 |
| 40110           | 109                    |                          |         | MSW                                                                                                                                                                                               | RW      |                |
| 40111           | 110                    | COUNTER                  | 5       | LSW                                                                                                                                                                                               | RW      | UNSIGNED<br>32 |
| 40112           | 111                    |                          |         | MSW                                                                                                                                                                                               | RW      |                |
| 40113           | 112                    | COUNTER                  | 6       | LSW                                                                                                                                                                                               | RW      | UNSIGNED<br>32 |
| 40114           | 113                    |                          |         | MSW                                                                                                                                                                                               | RW      |                |
| 40115           | 114                    | COUNTER                  | 7       | LSW                                                                                                                                                                                               | RW      | UNSIGNED<br>32 |
| 40116           | 115                    |                          |         | MSW                                                                                                                                                                                               | RW      |                |
| 40117           | 116                    | COUNTER                  | 8       | LSW                                                                                                                                                                                               | RW      | UNSIGNED<br>32 |
| 40118           | 117                    |                          |         | MSW                                                                                                                                                                                               | RW      |                |
| 40119           | 118                    | COUNTER                  | 9       | LSW                                                                                                                                                                                               | RW      | UNSIGNED<br>32 |
| 40120           | 119                    |                          |         | MSW                                                                                                                                                                                               | RW      |                |
| 40121           | 120                    | COUNTER                  | 10      | LSW                                                                                                                                                                                               | RW      | UNSIGNED<br>32 |
| 40122           | 121                    |                          |         | MSW                                                                                                                                                                                               | RW      |                |
| 40123           | 122                    | COUNTER                  | 11      | LSW                                                                                                                                                                                               | RW      | UNSIGNED<br>32 |
| 40124           | 123                    |                          |         | MSW                                                                                                                                                                                               | RW      |                |

|              |     |         |    |     |    |                |
|--------------|-----|---------|----|-----|----|----------------|
| <b>40125</b> | 124 | COUNTER | 12 | LSW | RW | UNSIGNED<br>32 |
| <b>40126</b> | 125 |         |    | MSW | RW |                |
| <b>40127</b> | 126 | COUNTER | 13 | LSW | RW | UNSIGNED<br>32 |
| <b>40128</b> | 127 |         |    | MSW | RW |                |
| <b>40129</b> | 128 | COUNTER | 14 | LSW | RW | UNSIGNED<br>32 |
| <b>40130</b> | 129 |         |    | MSW | RW |                |
| <b>40131</b> | 130 | COUNTER | 15 | LSW | RW | UNSIGNED<br>32 |
| <b>40132</b> | 131 |         |    | MSW | RW |                |
| <b>40133</b> | 132 | COUNTER | 16 | LSW | RW | UNSIGNED<br>32 |
| <b>40134</b> | 133 |         |    | MSW | RW |                |

| <b>ADDRESS (4x)</b> | <b>OFFSET ADDRESS (4x)</b> | <b>REGISTER</b>     | <b>CHANNEL</b> | <b>DESCRIPTION</b>                              | <b>W/R</b> | <b>TYPE</b>    |
|---------------------|----------------------------|---------------------|----------------|-------------------------------------------------|------------|----------------|
| <b>40201</b>        | 200                        | INT MEASURE<br>TLOW | 1              | Integer<br>measure of<br>Tlow in<br>[ms*10] LSW | RO         | UNSIGNED<br>32 |
| <b>40202</b>        | 201                        |                     |                | Integer<br>measure of<br>Tlow in<br>[ms*10] MSW | RO         |                |
| <b>40203</b>        | 202                        | INT MEASURE<br>TLOW | 2              | Integer<br>measure of<br>Tlow in<br>[ms*10] LSW | RO         | UNSIGNED<br>32 |
| <b>40204</b>        | 203                        |                     |                | Integer<br>measure of<br>Tlow in<br>[ms*10] MSW | RO         |                |
| <b>40205</b>        | 204                        | INT MEASURE<br>TLOW | 3              | Integer<br>measure of<br>Tlow in<br>[ms*10] LSW | RO         | UNSIGNED<br>32 |
| <b>40206</b>        | 205                        |                     |                | Integer<br>measure of<br>Tlow in<br>[ms*10] MSW | RO         |                |
| <b>40207</b>        | 206                        | INT MEASURE<br>TLOW | 4              | Integer<br>measure of<br>Tlow in<br>[ms*10] LSW | RO         | UNSIGNED<br>32 |
| <b>40208</b>        | 207                        |                     |                | Integer<br>measure of<br>Tlow in<br>[ms*10] MSW | RO         |                |
| <b>40209</b>        | 208                        | INT MEASURE<br>TLOW | 5              | Integer<br>measure of                           | RO         | UNSIGNED<br>32 |

|              |     |                     |    |                                                 |    |                |
|--------------|-----|---------------------|----|-------------------------------------------------|----|----------------|
|              |     |                     |    | Flow in<br>[ms*10] LSW                          |    |                |
| <b>40210</b> | 209 |                     |    | Integer<br>measure of<br>Flow in<br>[ms*10] MSW | RO |                |
| <b>40211</b> | 210 | INT MEASURE<br>TLOW | 6  | Integer<br>measure of<br>Flow in<br>[ms*10] LSW | RO | UNSIGNED<br>32 |
| <b>40212</b> | 211 |                     |    | Integer<br>measure of<br>Flow in<br>[ms*10] MSW | RO |                |
| <b>40213</b> | 212 | INT MEASURE<br>TLOW | 7  | Integer<br>measure of<br>Flow in<br>[ms*10] LSW | RO | UNSIGNED<br>32 |
| <b>40214</b> | 213 |                     |    | Integer<br>measure of<br>Flow in<br>[ms*10] MSW | RO |                |
| <b>40215</b> | 214 | INT MEASURE<br>TLOW | 8  | Integer<br>measure of<br>Flow in<br>[ms*10] LSW | RO | UNSIGNED<br>32 |
| <b>40216</b> | 215 |                     |    | Integer<br>measure of<br>Flow in<br>[ms*10] MSW | RO |                |
| <b>40217</b> | 216 | INT MEASURE<br>TLOW | 9  | Integer<br>measure of<br>Flow in<br>[ms*10] LSW | RO | UNSIGNED<br>32 |
| <b>40218</b> | 217 |                     |    | Integer<br>measure of<br>Flow in<br>[ms*10] MSW | RO |                |
| <b>40219</b> | 218 | INT MEASURE<br>TLOW | 10 | Integer<br>measure of<br>Flow in<br>[ms*10] LSW | RO | UNSIGNED<br>32 |
| <b>40220</b> | 219 |                     |    | Integer<br>measure of<br>Flow in<br>[ms*10] MSW | RO |                |
| <b>40221</b> | 220 | INT MEASURE<br>TLOW | 11 | Integer<br>measure of                           | RO | UNSIGNED<br>32 |

|              |     |                     |    |                                                 |    |                |
|--------------|-----|---------------------|----|-------------------------------------------------|----|----------------|
|              |     |                     |    | Flow in<br>[ms*10] LSW                          |    |                |
| <b>40222</b> | 221 |                     |    | Integer<br>measure of<br>Flow in<br>[ms*10] MSW | RO |                |
| <b>40223</b> | 222 | INT MEASURE<br>TLOW | 12 | Integer<br>measure of<br>Flow in<br>[ms*10] LSW | RO | UNSIGNED<br>32 |
| <b>40224</b> | 223 |                     |    | Integer<br>measure of<br>Flow in<br>[ms*10] MSW | RO |                |
| <b>40225</b> | 224 | INT MEASURE<br>TLOW | 13 | Integer<br>measure of<br>Flow in<br>[ms*10] LSW | RO | UNSIGNED<br>32 |
| <b>40226</b> | 225 |                     |    | Integer<br>measure of<br>Flow in<br>[ms*10] MSW | RO |                |
| <b>40227</b> | 226 | INT MEASURE<br>TLOW | 14 | Integer<br>measure of<br>Flow in<br>[ms*10] LSW | RO | UNSIGNED<br>32 |
| <b>40228</b> | 227 |                     |    | Integer<br>measure of<br>Flow in<br>[ms*10] MSW | RO |                |
| <b>40229</b> | 228 | INT MEASURE<br>TLOW | 15 | Integer<br>measure of<br>Flow in<br>[ms*10] LSW | RO | UNSIGNED<br>32 |
| <b>40230</b> | 229 |                     |    | Integer<br>measure of<br>Flow in<br>[ms*10] MSW | RO |                |
| <b>40231</b> | 230 | INT MEASURE<br>TLOW | 16 | Integer<br>measure of<br>Flow in<br>[ms*10] LSW | RO | UNSIGNED<br>32 |
| <b>40232</b> | 231 |                     |    | Integer<br>measure of<br>Flow in<br>[ms*10] MSW | RO |                |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET ADDRESS<br/>(4x)</b> | <b>REGISTER</b>      | <b>CHANNEL</b> | <b>DESCRIPTION</b>                                  | <b>W/<br/>R</b> | <b>TYPE</b> |
|-------------------------|--------------------------------|----------------------|----------------|-----------------------------------------------------|-----------------|-------------|
| <b>40233</b>            | 232                            | INT MEASURE<br>THIGH | 1              | Integer<br>measure of<br>Thigh in<br>[ms*10] LSW    | RO              | UNSIGNED 32 |
| <b>40234</b>            | 233                            |                      |                | Integer<br>measure of<br>Thigh in<br>[ms*10]<br>MSW | RO              |             |
| <b>40235</b>            | 234                            | INT MEASURE<br>THIGH | 2              | Integer<br>measure of<br>Thigh in<br>[ms*10] LSW    | RO              | UNSIGNED 32 |
| <b>40236</b>            | 235                            |                      |                | Integer<br>measure of<br>Thigh in<br>[ms*10]<br>MSW | RO              |             |
| <b>40237</b>            | 236                            | INT MEASURE<br>THIGH | 3              | Integer<br>measure of<br>Thigh in<br>[ms*10] LSW    | RO              | UNSIGNED 32 |
| <b>40238</b>            | 237                            |                      |                | Integer<br>measure of<br>Thigh in<br>[ms*10]<br>MSW | RO              |             |
| <b>40239</b>            | 238                            | INT MEASURE<br>THIGH | 4              | Integer<br>measure of<br>Thigh in<br>[ms*10] LSW    | RO              | UNSIGNED 32 |
| <b>40240</b>            | 239                            |                      |                | Integer<br>measure of<br>Thigh in<br>[ms*10]<br>MSW | RO              |             |
| <b>40241</b>            | 240                            | INT MEASURE<br>THIGH | 5              | Integer<br>measure of<br>Thigh in<br>[ms*10] LSW    | RO              | UNSIGNED 32 |
| <b>40242</b>            | 241                            |                      |                | Integer<br>measure of<br>Thigh in<br>[ms*10]<br>MSW | RO              |             |
| <b>40243</b>            | 242                            | INT MEASURE<br>THIGH | 6              | Integer<br>measure of                               | RO              | UNSIGNED 32 |



|              |     |                      |    |                                                     |    |                 |
|--------------|-----|----------------------|----|-----------------------------------------------------|----|-----------------|
|              |     |                      |    | Thigh in<br>[ms*10] LSW                             |    |                 |
| <b>40244</b> | 243 |                      |    | Integer<br>measure of<br>Thigh in<br>[ms*10]<br>MSW | RO |                 |
| <b>40245</b> | 244 | INT MEASURE<br>THIGH | 7  | Integer<br>measure of<br>Thigh in<br>[ms*10] LSW    | RO | UNSIGNE<br>D 32 |
| <b>40246</b> | 245 |                      |    | Integer<br>measure of<br>Thigh in<br>[ms*10]<br>MSW | RO |                 |
| <b>40247</b> | 246 | INT MEASURE<br>THIGH | 8  | Integer<br>measure of<br>Thigh in<br>[ms*10] LSW    | RO | UNSIGNE<br>D 32 |
| <b>40248</b> | 247 |                      |    | Integer<br>measure of<br>Thigh in<br>[ms*10]<br>MSW | RO |                 |
| <b>40249</b> | 248 | INT MEASURE<br>THIGH | 9  | Integer<br>measure of<br>Thigh in<br>[ms*10] LSW    | RO | UNSIGNE<br>D 32 |
| <b>40250</b> | 249 |                      |    | Integer<br>measure of<br>Thigh in<br>[ms*10]<br>MSW | RO |                 |
| <b>40251</b> | 250 | INT MEASURE<br>THIGH | 10 | Integer<br>measure of<br>Thigh in<br>[ms*10] LSW    | RO | UNSIGNE<br>D 32 |
| <b>40252</b> | 251 |                      |    | Integer<br>measure of<br>Thigh in<br>[ms*10]<br>MSW | RO |                 |
| <b>40253</b> | 252 | INT MEASURE<br>THIGH | 11 | Integer<br>measure of<br>Thigh in<br>[ms*10] LSW    | RO | UNSIGNE<br>D 32 |
| <b>40254</b> | 253 |                      |    | Integer<br>measure of                               | RO |                 |

|              |     |                      |    |                                                     |    |                 |
|--------------|-----|----------------------|----|-----------------------------------------------------|----|-----------------|
|              |     |                      |    | Thigh in<br>[ms*10]<br>MSW                          |    |                 |
| <b>40255</b> | 254 | INT MEASURE<br>THIGH | 12 | Integer<br>measure of<br>Thigh in<br>[ms*10] LSW    | RO | UNSIGNE<br>D 32 |
| <b>40256</b> | 255 |                      |    | Integer<br>measure of<br>Thigh in<br>[ms*10]<br>MSW | RO |                 |
| <b>40257</b> | 256 | INT MEASURE<br>THIGH | 13 | Integer<br>measure of<br>Thigh in<br>[ms*10] LSW    | RO | UNSIGNE<br>D 32 |
| <b>40258</b> | 257 |                      |    | Integer<br>measure of<br>Thigh in<br>[ms*10]<br>MSW | RO |                 |
| <b>40259</b> | 258 | INT MEASURE<br>THIGH | 14 | Integer<br>measure of<br>Thigh in<br>[ms*10] LSW    | RO | UNSIGNE<br>D 32 |
| <b>40260</b> | 259 |                      |    | Integer<br>measure of<br>Thigh in<br>[ms*10]<br>MSW | RO |                 |
| <b>40261</b> | 260 | INT MEASURE<br>THIGH | 15 | Integer<br>measure of<br>Thigh in<br>[ms*10] LSW    | RO | UNSIGNE<br>D 32 |
| <b>40262</b> | 261 |                      |    | Integer<br>measure of<br>Thigh in<br>[ms*10]<br>MSW | RO |                 |
| <b>40263</b> | 262 | INT MEASURE<br>THIGH | 16 | Integer<br>measure of<br>Thigh in<br>[ms*10] LSW    | RO | UNSIGNE<br>D 32 |
| <b>40264</b> | 263 |                      |    | Integer<br>measure of<br>Thigh in<br>[ms*10]<br>MSW | RO |                 |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET ADDRESS<br/>(4x)</b> | <b>REGISTER</b>    | <b>CHANNEL</b> | <b>DESCRIPTION</b>                 | <b>W/<br/>R</b> | <b>TYPE</b> |
|-------------------------|--------------------------------|--------------------|----------------|------------------------------------|-----------------|-------------|
| <b>40265</b>            | 264                            | INT MEASURE PERIOD | 1              | Integer Period Measure [ms*10] LSW | RO              | UNSIGNED 32 |
| <b>40266</b>            | 265                            |                    |                | Integer Period Measure [ms*10] MSW | RO              |             |
| <b>40267</b>            | 266                            | INT MEASURE PERIOD | 2              | Integer Period Measure [ms*10] LSW | RO              | UNSIGNED 32 |
| <b>40268</b>            | 267                            |                    |                | Integer Period Measure [ms*10] MSW | RO              |             |
| <b>40269</b>            | 268                            | INT MEASURE PERIOD | 3              | Integer Period Measure [ms*10] LSW | RO              | UNSIGNED 32 |
| <b>40270</b>            | 269                            |                    |                | Integer Period Measure [ms*10] MSW | RO              |             |
| <b>40271</b>            | 270                            | INT MEASURE PERIOD | 4              | Integer Period Measure [ms*10] LSW | RO              | UNSIGNED 32 |
| <b>40272</b>            | 271                            |                    |                | Integer Period Measure [ms*10] MSW | RO              |             |
| <b>40273</b>            | 272                            | INT MEASURE PERIOD | 5              | Integer Period Measure [ms*10] LSW | RO              | UNSIGNED 32 |
| <b>40274</b>            | 273                            |                    |                | Integer Period Measure [ms*10] MSW | RO              |             |

|              |     |                    |    |                                    |    |             |
|--------------|-----|--------------------|----|------------------------------------|----|-------------|
| <b>40275</b> | 274 | INT MEASURE PERIOD | 6  | Integer Period Measure [ms*10] LSW | RO | UNSIGNED 32 |
| <b>40276</b> | 275 |                    |    | Integer Period Measure [ms*10] MSW | RO |             |
| <b>40277</b> | 276 | INT MEASURE PERIOD | 7  | Integer Period Measure [ms*10] LSW | RO | UNSIGNED 32 |
| <b>40278</b> | 277 |                    |    | Integer Period Measure [ms*10] MSW | RO |             |
| <b>40279</b> | 278 | INT MEASURE PERIOD | 8  | Integer Period Measure [ms*10] LSW | RO | UNSIGNED 32 |
| <b>40280</b> | 279 |                    |    | Integer Period Measure [ms*10] MSW | RO |             |
| <b>40281</b> | 280 | INT MEASURE PERIOD | 9  | Integer Period Measure [ms*10] LSW | RO | UNSIGNED 32 |
| <b>40282</b> | 281 |                    |    | Integer Period Measure [ms*10] MSW | RO |             |
| <b>40283</b> | 282 | INT MEASURE PERIOD | 10 | Integer Period Measure [ms*10] LSW | RO | UNSIGNED 32 |
| <b>40284</b> | 283 |                    |    | Integer Period Measure [ms*10] MSW | RO |             |
| <b>40285</b> | 284 | INT MEASURE PERIOD | 11 | Integer Period Measure [ms*10] LSW | RO | UNSIGNED 32 |

|              |     |                    |    |                                    |    |              |
|--------------|-----|--------------------|----|------------------------------------|----|--------------|
| <b>40286</b> | 285 |                    |    | Integer Period Measure [ms*10] MSW | RO |              |
| <b>40287</b> | 286 | INT MEASURE PERIOD | 12 | Integer Period Measure [ms*10] LSW | RO | UNSIGNE D 32 |
| <b>40288</b> | 287 |                    |    | Integer Period Measure [ms*10] MSW | RO |              |
| <b>40289</b> | 288 | INT MEASURE PERIOD | 13 | Integer Period Measure [ms*10] LSW | RO | UNSIGNE D 32 |
| <b>40290</b> | 289 |                    |    | Integer Period Measure [ms*10] MSW | RO |              |
| <b>40291</b> | 290 | INT MEASURE PERIOD | 14 | Integer Period Measure [ms*10] LSW | RO | UNSIGNE D 32 |
| <b>40292</b> | 291 |                    |    | Integer Period Measure [ms*10] MSW | RO |              |
| <b>40293</b> | 292 | INT MEASURE PERIOD | 15 | Integer Period Measure [ms*10] LSW | RO | UNSIGNE D 32 |
| <b>40294</b> | 293 |                    |    | Integer Period Measure [ms*10] MSW | RO |              |
| <b>40295</b> | 294 | INT MEASURE PERIOD | 16 | Integer Period Measure [ms*10] LSW | RO | UNSIGNE D 32 |
| <b>40296</b> | 295 |                    |    | Integer Period Measure             | RO |              |

|  |  |  |  |                |  |  |
|--|--|--|--|----------------|--|--|
|  |  |  |  | [ms*10]<br>MSW |  |  |
|--|--|--|--|----------------|--|--|

| <b>ADDRESS (4x)</b> | <b>OFFSET ADDRESS (4x)</b> | <b>REGISTER</b>        | <b>CHANNEL</b> | <b>DESCRIPTION</b>                             | <b>W/R</b> | <b>TYPE</b>    |
|---------------------|----------------------------|------------------------|----------------|------------------------------------------------|------------|----------------|
| <b>40297</b>        | 296                        | INT<br>MEASURE<br>FREQ | 1              | Integer measure of<br>the frequency in<br>[Hz] | RO         | UNSIGNED<br>16 |
| <b>40298</b>        | 297                        | INT<br>MEASURE<br>FREQ | 2              | Integer measure of<br>the frequency in<br>[Hz] | RO         | UNSIGNED<br>16 |
| <b>40299</b>        | 298                        | INT<br>MEASURE<br>FREQ | 3              | Integer measure of<br>the frequency in<br>[Hz] | RO         | UNSIGNED<br>16 |
| <b>40300</b>        | 299                        | INT<br>MEASURE<br>FREQ | 4              | Integer measure of<br>the frequency in<br>[Hz] | RO         | UNSIGNED<br>16 |
| <b>40301</b>        | 300                        | INT<br>MEASURE<br>FREQ | 5              | Integer measure of<br>the frequency in<br>[Hz] | RO         | UNSIGNED<br>16 |
| <b>40302</b>        | 301                        | INT<br>MEASURE<br>FREQ | 6              | Integer measure of<br>the frequency in<br>[Hz] | RO         | UNSIGNED<br>16 |
| <b>40303</b>        | 302                        | INT<br>MEASURE<br>FREQ | 7              | Integer measure of<br>the frequency in<br>[Hz] | RO         | UNSIGNED<br>16 |
| <b>40304</b>        | 303                        | INT<br>MEASURE<br>FREQ | 8              | Integer measure of<br>the frequency in<br>[Hz] | RO         | UNSIGNED<br>16 |
| <b>40305</b>        | 304                        | INT<br>MEASURE<br>FREQ | 9              | Integer measure of<br>the frequency in<br>[Hz] | RO         | UNSIGNED<br>16 |
| <b>40306</b>        | 305                        | INT<br>MEASURE<br>FREQ | 10             | Integer measure of<br>the frequency in<br>[Hz] | RO         | UNSIGNED<br>16 |
| <b>40307</b>        | 306                        | INT<br>MEASURE<br>FREQ | 11             | Integer measure of<br>the frequency in<br>[Hz] | RO         | UNSIGNED<br>16 |
| <b>40308</b>        | 307                        | INT<br>MEASURE<br>FREQ | 12             | Integer measure of<br>the frequency in<br>[Hz] | RO         | UNSIGNED<br>16 |

|              |     |                        |    |                                                |    |                |
|--------------|-----|------------------------|----|------------------------------------------------|----|----------------|
| <b>40309</b> | 308 | INT<br>MEASURE<br>FREQ | 13 | Integer measure of<br>the frequency in<br>[Hz] | RO | UNSIGNED<br>16 |
| <b>40310</b> | 309 | INT<br>MEASURE<br>FREQ | 14 | Integer measure of<br>the frequency in<br>[Hz] | RO | UNSIGNED<br>16 |
| <b>40311</b> | 310 | INT<br>MEASURE<br>FREQ | 15 | Integer measure of<br>the frequency in<br>[Hz] | RO | UNSIGNED<br>16 |
| <b>40312</b> | 311 | INT<br>MEASURE<br>FREQ | 16 | Integer measure of<br>the frequency in<br>[Hz] | RO | UNSIGNED<br>16 |

| <b>ADDRESS (4x)</b> | <b>OFFSET ADDRESS (4x)</b> | <b>REGISTER</b> | <b>CHANNEL</b> | <b>DESCRIPTION</b>                                 | <b>W/R</b> | <b>TYPE</b> |
|---------------------|----------------------------|-----------------|----------------|----------------------------------------------------|------------|-------------|
| <b>40401</b>        | 400                        | FLOAT TLOW      | 1              | Floating point<br>measure of Tlow<br>in [ms] (LSW) | RO         | FLOAT 32    |
| <b>40402</b>        | 401                        |                 |                | Floating point<br>measure of Tlow<br>in [ms] (MSW) | RO         |             |
| <b>40403</b>        | 402                        | FLOAT TLOW      | 2              | Floating point<br>measure of Tlow<br>in [ms] (LSW) | RO         | FLOAT 32    |
| <b>40404</b>        | 403                        |                 |                | Floating point<br>measure of Tlow<br>in [ms] (MSW) | RO         |             |
| <b>40405</b>        | 404                        | FLOAT TLOW      | 3              | Floating point<br>measure of Tlow<br>in [ms] (LSW) | RO         | FLOAT 32    |
| <b>40406</b>        | 405                        |                 |                | Floating point<br>measure of Tlow<br>in [ms] (MSW) | RO         |             |
| <b>40407</b>        | 406                        | FLOAT TLOW      | 4              | Floating point<br>measure of Tlow<br>in [ms] (LSW) | RO         | FLOAT 32    |
| <b>40408</b>        | 407                        |                 |                | Floating point<br>measure of Tlow<br>in [ms] (MSW) | RO         |             |
| <b>40409</b>        | 408                        | FLOAT TLOW      | 5              | Floating point<br>measure of Tlow<br>in [ms] (LSW) | RO         | FLOAT 32    |
| <b>40410</b>        | 409                        |                 |                | Floating point<br>measure of Tlow<br>in [ms] (MSW) | RO         |             |
| <b>40411</b>        | 410                        | FLOAT TLOW      | 6              | Floating point<br>measure of Tlow<br>in [ms] (LSW) | RO         | FLOAT 32    |

|              |     |            |    |                                              |    |          |
|--------------|-----|------------|----|----------------------------------------------|----|----------|
| <b>40412</b> | 411 |            |    | Floating point measure of Tlow in [ms] (MSW) | RO |          |
| <b>40413</b> | 412 | FLOAT TLOW | 7  | Floating point measure of Tlow in [ms] (LSW) | RO | FLOAT 32 |
| <b>40414</b> | 413 |            |    | Floating point measure of Tlow in [ms] (MSW) | RO |          |
| <b>40415</b> | 414 | FLOAT TLOW | 8  | Floating point measure of Tlow in [ms] (LSW) | RO | FLOAT 32 |
| <b>40416</b> | 415 |            |    | Floating point measure of Tlow in [ms] (MSW) | RO |          |
| <b>40417</b> | 416 | FLOAT TLOW | 9  | Floating point measure of Tlow in [ms] (LSW) | RO | FLOAT 32 |
| <b>40418</b> | 417 |            |    | Floating point measure of Tlow in [ms] (MSW) | RO |          |
| <b>40419</b> | 418 | FLOAT TLOW | 10 | Floating point measure of Tlow in [ms] (LSW) | RO | FLOAT 32 |
| <b>40420</b> | 419 |            |    | Floating point measure of Tlow in [ms] (MSW) | RO |          |
| <b>40421</b> | 420 | FLOAT TLOW | 11 | Floating point measure of Tlow in [ms] (LSW) | RO | FLOAT 32 |
| <b>40422</b> | 421 |            |    | Floating point measure of Tlow in [ms] (MSW) | RO |          |
| <b>40423</b> | 422 | FLOAT TLOW | 12 | Floating point measure of Tlow in [ms] (LSW) | RO | FLOAT 32 |
| <b>40424</b> | 423 |            |    | Floating point measure of Tlow in [ms] (MSW) | RO |          |
| <b>40425</b> | 424 | FLOAT TLOW | 13 | Floating point measure of Tlow in [ms] (LSW) | RO | FLOAT 32 |
| <b>40426</b> | 425 |            |    | Floating point measure of Tlow in [ms] (MSW) | RO |          |
| <b>40427</b> | 426 | FLOAT TLOW | 14 | Floating point measure of Tlow in [ms] (LSW) | RO | FLOAT 32 |



|              |     |            |    |                                              |    |          |
|--------------|-----|------------|----|----------------------------------------------|----|----------|
| <b>40428</b> | 427 |            |    | Floating point measure of Flow in [ms] (MSW) | RO |          |
| <b>40429</b> | 428 | FLOAT TLOW | 15 | Floating point measure of Flow in [ms] (LSW) | RO | FLOAT 32 |
| <b>40430</b> | 429 |            |    | Floating point measure of Flow in [ms] (MSW) | RO |          |
| <b>40431</b> | 430 | FLOAT TLOW | 16 | Floating point measure of Flow in [ms] (LSW) | RO | FLOAT 32 |
| <b>40432</b> | 431 |            |    | Floating point measure of Flow in [ms] (MSW) | RO |          |

| <b>ADDRESS (4x)</b> | <b>OFFSET ADDRESS (4x)</b> | <b>REGISTER</b> | <b>CHANNEL</b> | <b>DESCRIPTION</b>                            | <b>W/R</b> | <b>TYPE</b> |
|---------------------|----------------------------|-----------------|----------------|-----------------------------------------------|------------|-------------|
| <b>40465</b>        | 464                        | FLOAT THIGH     | 1              | Floating point measure of Thigh in [ms] (LSW) | RO         | FLOAT 32    |
| <b>40466</b>        | 465                        |                 |                | Floating point measure of Thigh in [ms] (MSW) | RO         |             |
| <b>40467</b>        | 466                        | FLOAT THIGH     | 2              | Floating point measure of Thigh in [ms] (LSW) | RO         | FLOAT 32    |
| <b>40468</b>        | 467                        |                 |                | Floating point measure of Thigh in [ms] (MSW) | RO         |             |
| <b>40469</b>        | 468                        | FLOAT THIGH     | 3              | Floating point measure of Thigh in [ms] (LSW) | RO         | FLOAT 32    |
| <b>40470</b>        | 469                        |                 |                | Floating point measure of Thigh in [ms] (MSW) | RO         |             |
| <b>40471</b>        | 470                        | FLOAT THIGH     | 4              | Floating point measure of Thigh in [ms] (LSW) | RO         | FLOAT 32    |
| <b>40472</b>        | 471                        |                 |                | Floating point measure of Thigh in [ms] (MSW) | RO         |             |
| <b>40473</b>        | 472                        | FLOAT THIGH     | 5              | Floating point measure of Thigh in [ms] (LSW) | RO         | FLOAT 32    |
| <b>40474</b>        | 473                        |                 |                | Floating point measure of Thigh in [ms] (MSW) | RO         |             |

|              |     |             |    |                                               |    |          |
|--------------|-----|-------------|----|-----------------------------------------------|----|----------|
| <b>40475</b> | 474 | FLOAT THIGH | 6  | Floating point measure of Thigh in [ms] (LSW) | RO | FLOAT 32 |
| <b>40476</b> | 475 |             |    | Floating point measure of Thigh in [ms] (MSW) | RO |          |
| <b>40477</b> | 476 | FLOAT THIGH | 7  | Floating point measure of Thigh in [ms] (LSW) | RO | FLOAT 32 |
| <b>40478</b> | 477 |             |    | Floating point measure of Thigh in [ms] (MSW) | RO |          |
| <b>40479</b> | 478 | FLOAT THIGH | 8  | Floating point measure of Thigh in [ms] (LSW) | RO | FLOAT 32 |
| <b>40480</b> | 479 |             |    | Floating point measure of Thigh in [ms] (MSW) | RO |          |
| <b>40481</b> | 480 | FLOAT THIGH | 9  | Floating point measure of Thigh in [ms] (LSW) | RO | FLOAT 32 |
| <b>40482</b> | 481 |             |    | Floating point measure of Thigh in [ms] (MSW) | RO |          |
| <b>40483</b> | 482 | FLOAT THIGH | 10 | Floating point measure of Thigh in [ms] (LSW) | RO | FLOAT 32 |
| <b>40484</b> | 483 |             |    | Floating point measure of Thigh in [ms] (MSW) | RO |          |
| <b>40485</b> | 484 | FLOAT THIGH | 11 | Floating point measure of Thigh in [ms] (LSW) | RO | FLOAT 32 |
| <b>40486</b> | 485 |             |    | Floating point measure of Thigh in [ms] (MSW) | RO |          |
| <b>40487</b> | 486 | FLOAT THIGH | 12 | Floating point measure of Thigh in [ms] (LSW) | RO | FLOAT 32 |
| <b>40488</b> | 487 |             |    | Floating point measure of Thigh in [ms] (MSW) | RO |          |
| <b>40489</b> | 488 | FLOAT THIGH | 13 | Floating point measure of Thigh in [ms] (LSW) | RO | FLOAT 32 |
| <b>40490</b> | 489 |             |    | Floating point measure of Thigh in [ms] (MSW) | RO |          |

|              |     |             |    |                                               |    |          |
|--------------|-----|-------------|----|-----------------------------------------------|----|----------|
| <b>40491</b> | 490 | FLOAT THIGH | 14 | Floating point measure of Thigh in [ms] (LSW) | RO | FLOAT 32 |
| <b>40492</b> | 491 |             |    | Floating point measure of Thigh in [ms] (MSW) | RO |          |
| <b>40493</b> | 492 | FLOAT THIGH | 15 | Floating point measure of Thigh in [ms] (LSW) | RO | FLOAT 32 |
| <b>40494</b> | 493 |             |    | Floating point measure of Thigh in [ms] (MSW) | RO |          |
| <b>40495</b> | 494 | FLOAT THIGH | 16 | Floating point measure of Thigh in [ms] (LSW) | RO | FLOAT 32 |
| <b>40496</b> | 495 |             |    | Floating point measure of Thigh in [ms] (MSW) | RO |          |

| <b>ADDRESS (4x)</b> | <b>OFFSET ADDRESS (4x)</b> | <b>REGISTER</b> | <b>CHANNEL</b> | <b>DESCRIPTION</b>                                 | <b>W/R</b> | <b>TYPE</b> |
|---------------------|----------------------------|-----------------|----------------|----------------------------------------------------|------------|-------------|
| <b>40529</b>        | 528                        | FLOAT PERIOD    | 1              | Floating point measure of the Period in [ms] (LSW) | RO         | FLOAT 32    |
| <b>40530</b>        | 529                        |                 |                | Floating point measure of the Period in [ms] (MSW) | RO         |             |
| <b>40531</b>        | 530                        | FLOAT PERIOD    | 2              | Floating point measure of the Period in [ms] (LSW) | RO         | FLOAT 32    |
| <b>40532</b>        | 531                        |                 |                | Floating point measure of the Period in [ms] (MSW) | RO         |             |
| <b>40533</b>        | 532                        | FLOAT PERIOD    | 3              | Floating point measure of the Period in [ms] (LSW) | RO         | FLOAT 32    |
| <b>40534</b>        | 533                        |                 |                | Floating point measure of the Period in [ms] (MSW) | RO         |             |
| <b>40535</b>        | 534                        | FLOAT PERIOD    | 4              | Floating point measure of the                      | RO         | FLOAT 32    |

|              |     |              |    |                                                             |    |          |
|--------------|-----|--------------|----|-------------------------------------------------------------|----|----------|
|              |     |              |    | Period in [ms]<br>(LSW)                                     |    |          |
| <b>40536</b> | 535 |              |    | Floating point<br>measure of the<br>Period in [ms]<br>(MSW) | RO |          |
| <b>40537</b> | 536 | FLOAT PERIOD | 5  | Floating point<br>measure of the<br>Period in [ms]<br>(LSW) | RO | FLOAT 32 |
| <b>40538</b> | 537 |              |    | Floating point<br>measure of the<br>Period in [ms]<br>(MSW) | RO |          |
| <b>40539</b> | 538 | FLOAT PERIOD | 6  | Floating point<br>measure of the<br>Period in [ms]<br>(LSW) | RO | FLOAT 32 |
| <b>40540</b> | 539 |              |    | Floating point<br>measure of the<br>Period in [ms]<br>(MSW) | RO |          |
| <b>40541</b> | 540 | FLOAT PERIOD | 7  | Floating point<br>measure of the<br>Period in [ms]<br>(LSW) | RO | FLOAT 32 |
| <b>40542</b> | 541 |              |    | Floating point<br>measure of the<br>Period in [ms]<br>(MSW) | RO |          |
| <b>40543</b> | 542 | FLOAT PERIOD | 8  | Floating point<br>measure of the<br>Period in [ms]<br>(LSW) | RO | FLOAT 32 |
| <b>40544</b> | 543 |              |    | Floating point<br>measure of the<br>Period in [ms]<br>(MSW) | RO |          |
| <b>40545</b> | 544 | FLOAT PERIOD | 9  | Floating point<br>measure of the<br>Period in [ms]<br>(LSW) | RO | FLOAT 32 |
| <b>40546</b> | 545 |              |    | Floating point<br>measure of the<br>Period in [ms]<br>(MSW) | RO |          |
| <b>40547</b> | 546 | FLOAT PERIOD | 10 | Floating point<br>measure of the                            | RO | FLOAT 32 |

|              |     |              |    |                                                             |    |          |
|--------------|-----|--------------|----|-------------------------------------------------------------|----|----------|
|              |     |              |    | Period in [ms]<br>(LSW)                                     |    |          |
| <b>40548</b> | 547 |              |    | Floating point<br>measure of the<br>Period in [ms]<br>(MSW) | RO |          |
| <b>40549</b> | 548 | FLOAT PERIOD | 11 | Floating point<br>measure of the<br>Period in [ms]<br>(LSW) | RO | FLOAT 32 |
| <b>40550</b> | 549 |              |    | Floating point<br>measure of the<br>Period in [ms]<br>(MSW) | RO |          |
| <b>40551</b> | 550 | FLOAT PERIOD | 12 | Floating point<br>measure of the<br>Period in [ms]<br>(LSW) | RO | FLOAT 32 |
| <b>40552</b> | 551 |              |    | Floating point<br>measure of the<br>Period in [ms]<br>(MSW) | RO |          |
| <b>40553</b> | 552 | FLOAT PERIOD | 13 | Floating point<br>measure of the<br>Period in [ms]<br>(LSW) | RO | FLOAT 32 |
| <b>40554</b> | 553 |              |    | Floating point<br>measure of the<br>Period in [ms]<br>(MSW) | RO |          |
| <b>40555</b> | 554 | FLOAT PERIOD | 14 | Floating point<br>measure of the<br>Period in [ms]<br>(LSW) | RO | FLOAT 32 |
| <b>40556</b> | 555 |              |    | Floating point<br>measure of the<br>Period in [ms]<br>(MSW) | RO |          |
| <b>40557</b> | 556 | FLOAT PERIOD | 15 | Floating point<br>measure of the<br>Period in [ms]<br>(LSW) | RO | FLOAT 32 |
| <b>40558</b> | 557 |              |    | Floating point<br>measure of the<br>Period in [ms]<br>(MSW) | RO |          |
| <b>40559</b> | 558 | FLOAT PERIOD | 16 | Floating point<br>measure of the                            | RO | FLOAT 32 |

|              |     |  |  |                                                             |    |  |
|--------------|-----|--|--|-------------------------------------------------------------|----|--|
|              |     |  |  | Period in [ms]<br>(LSW)                                     |    |  |
| <b>40560</b> | 559 |  |  | Floating point<br>measure of the<br>Period in [ms]<br>(MSW) | RO |  |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET ADDRESS<br/>(4x)</b> | <b>REGISTER</b>    | <b>CHANNE<br/>L</b> | <b>DESSCRIPTIO<br/>N</b>                                             | <b>W/<br/>R</b> | <b>TYPE</b> |
|-------------------------|--------------------------------|--------------------|---------------------|----------------------------------------------------------------------|-----------------|-------------|
| <b>40593</b>            | 592                            | FLOAT<br>FREQUENCY | 1                   | Floating<br>point<br>measure of<br>the<br>Frequency in<br>[Hz] (LSW) | RO              | FLOAT<br>32 |
| <b>40594</b>            | 593                            |                    |                     | Floating<br>point<br>measure of<br>the<br>Frequency in<br>[Hz] (MSW) | RO              |             |
| <b>40595</b>            | 594                            | FLOAT<br>FREQUENCY | 2                   | Floating<br>point<br>measure of<br>the<br>Frequency in<br>[Hz] (LSW) | RO              | FLOAT<br>32 |
| <b>40596</b>            | 595                            |                    |                     | Floating<br>point<br>measure of<br>the<br>Frequency in<br>[Hz] (MSW) | RO              |             |
| <b>40597</b>            | 596                            | FLOAT<br>FREQUENCY | 3                   | Floating<br>point<br>measure of<br>the<br>Frequency in<br>[Hz] (LSW) | RO              | FLOAT<br>32 |
| <b>40598</b>            | 597                            |                    |                     | Floating<br>point<br>measure of<br>the<br>Frequency in<br>[Hz] (MSW) | RO              |             |
| <b>40599</b>            | 598                            | FLOAT<br>FREQUENCY | 4                   | Floating<br>point                                                    | RO              | FLOAT<br>32 |

|              |     |                 |   |                                                       |    |          |
|--------------|-----|-----------------|---|-------------------------------------------------------|----|----------|
|              |     |                 |   | measure of the Frequency in [Hz] (LSW)                |    |          |
| <b>40600</b> | 599 |                 |   | Floating point measure of the Frequency in [Hz] (MSW) | RO |          |
| <b>40601</b> | 600 | FLOAT FREQUENCY | 5 | Floating point measure of the Frequency in [Hz] (LSW) | RO | FLOAT 32 |
| <b>40602</b> | 601 |                 |   | Floating point measure of the Frequency in [Hz] (MSW) | RO |          |
| <b>40603</b> | 602 | FLOAT FREQUENCY | 6 | Floating point measure of the Frequency in [Hz] (LSW) | RO | FLOAT 32 |
| <b>40604</b> | 603 |                 |   | Floating point measure of the Frequency in [Hz] (MSW) | RO |          |
| <b>40605</b> | 604 | FLOAT FREQUENCY | 7 | Floating point measure of the Frequency in [Hz] (LSW) | RO | FLOAT 32 |
| <b>40606</b> | 605 |                 |   | Floating point measure of the Frequency in [Hz] (MSW) | RO |          |
| <b>40607</b> | 606 | FLOAT FREQUENCY | 8 | Floating point measure of                             | RO | FLOAT 32 |

|              |     |                    |    |                                                                      |    |             |
|--------------|-----|--------------------|----|----------------------------------------------------------------------|----|-------------|
|              |     |                    |    | the<br>Frequency in<br>[Hz] (LSW)                                    |    |             |
| <b>40608</b> | 607 |                    |    | Floating<br>point<br>measure of<br>the<br>Frequency in<br>[Hz] (MSW) | RO |             |
| <b>40609</b> | 608 | FLOAT<br>FREQUENCY | 9  | Floating<br>point<br>measure of<br>the<br>Frequency in<br>[Hz] (LSW) | RO | FLOAT<br>32 |
| <b>40610</b> | 609 |                    |    | Floating<br>point<br>measure of<br>the<br>Frequency in<br>[Hz] (MSW) | RO |             |
| <b>40611</b> | 610 | FLOAT<br>FREQUENCY | 10 | Floating<br>point<br>measure of<br>the<br>Frequency in<br>[Hz] (LSW) | RO | FLOAT<br>32 |
| <b>40612</b> | 611 |                    |    | Floating<br>point<br>measure of<br>the<br>Frequency in<br>[Hz] (MSW) | RO |             |
| <b>40613</b> | 612 | FLOAT<br>FREQUENCY | 11 | Floating<br>point<br>measure of<br>the<br>Frequency in<br>[Hz] (LSW) | RO | FLOAT<br>32 |
| <b>40614</b> | 613 |                    |    | Floating<br>point<br>measure of<br>the<br>Frequency in<br>[Hz] (MSW) | RO |             |
| <b>40615</b> | 614 | FLOAT<br>FREQUENCY | 12 | Floating<br>point<br>measure of<br>the                               | RO | FLOAT<br>32 |



|              |     |                 |    |                                                       |    |          |
|--------------|-----|-----------------|----|-------------------------------------------------------|----|----------|
|              |     |                 |    | Frequency in [Hz] (LSW)                               |    |          |
| <b>40616</b> | 615 |                 |    | Floating point measure of the Frequency in [Hz] (MSW) | RO |          |
| <b>40617</b> | 616 | FLOAT FREQUENCY | 13 | Floating point measure of the Frequency in [Hz] (LSW) | RO | FLOAT 32 |
| <b>40618</b> | 617 |                 |    | Floating point measure of the Frequency in [Hz] (MSW) | RO |          |
| <b>40619</b> | 618 | FLOAT FREQUENCY | 14 | Floating point measure of the Frequency in [Hz] (LSW) | RO | FLOAT 32 |
| <b>40620</b> | 619 |                 |    | Floating point measure of the Frequency in [Hz] (MSW) | RO |          |
| <b>40621</b> | 620 | FLOAT FREQUENCY | 15 | Floating point measure of the Frequency in [Hz] (LSW) | RO | FLOAT 32 |
| <b>40622</b> | 621 |                 |    | Floating point measure of the Frequency in [Hz] (MSW) | RO |          |
| <b>40623</b> | 622 | FLOAT FREQUENCY | 16 | Floating point measure of the                         | RO | FLOAT 32 |

|              |     |  |  |                                                       |    |  |
|--------------|-----|--|--|-------------------------------------------------------|----|--|
|              |     |  |  | Frequency in [Hz] (LSW)                               |    |  |
| <b>40624</b> | 623 |  |  | Floating point measure of the Frequency in [Hz] (MSW) | RO |  |

## 22.2. R-16DI-8DO: CONSECUTIVE REGISTERS MODBUS 4x COPY (WITH INTEGER MEASURE REGISTERS)

| ADDRESS<br>(4x) | OFFSET ADDRESS<br>(4x) | REGISTER                     | CHANNEL  | DESCRIPTION                                                                                                                                                                                                               | W/<br>R | TYPE           |
|-----------------|------------------------|------------------------------|----------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------|----------------|
| <b>48001</b>    | 8000                   | DIGITAL<br>INPUT<br>[16...1] | [1...16] | DIGITAL<br>INPUTS [16...<br>1] THE LEAST<br>SIGNIFICANT<br>BIT IS<br>RELATIVE TO<br>I01<br>EXAMPLE:<br>5 decimal =<br>0000 0000<br>0000 0101<br>binary => I01<br>= High, I02 =<br>LOW, I03 =<br>HIGH, I04...<br>I16 = LOW | RO      | UNSIGNED<br>16 |
| <b>48002</b>    | 8001                   | DIGITAL<br>OUT [8...1]       | [8...1]  | DIGITAL<br>OUTPUTS [8...<br>1] THE LEAST<br>SIGNIFICANT<br>BIT IS<br>RELATIVE TO<br>D01<br>EXAMPLE:<br>5 decimal =<br>0000 0000<br>0000 0101<br>binary =><br>D01=High,<br>D02=LOW,<br>D03=HIGH,<br>D04...D08=LO<br>W      | RW      | UNSIGNED<br>16 |
| <b>48003</b>    | 8002                   | COUNTER                      | 1        | LSW                                                                                                                                                                                                                       | RW      | UNSIGNED<br>32 |
| <b>48004</b>    | 8003                   |                              |          | MSW                                                                                                                                                                                                                       | RW      |                |
| <b>48005</b>    | 8004                   | COUNTER                      | 2        | LSW                                                                                                                                                                                                                       | RW      | UNSIGNED<br>32 |
| <b>48006</b>    | 8005                   |                              |          | MSW                                                                                                                                                                                                                       | RW      |                |
| <b>48007</b>    | 8006                   | COUNTER                      | 3        | LSW                                                                                                                                                                                                                       | RW      | UNSIGNED<br>32 |
| <b>48008</b>    | 8007                   |                              |          | MSW                                                                                                                                                                                                                       | RW      |                |
| <b>48009</b>    | 8008                   | COUNTER                      | 4        | LSW                                                                                                                                                                                                                       | RW      | UNSIGNED<br>32 |
| <b>48010</b>    | 8009                   |                              |          | MSW                                                                                                                                                                                                                       | RW      |                |
| <b>48011</b>    | 8010                   | COUNTER                      | 5        | LSW                                                                                                                                                                                                                       | RW      |                |

|              |      |                        |    |                                         |    |                |
|--------------|------|------------------------|----|-----------------------------------------|----|----------------|
| <b>48012</b> | 8011 |                        |    | MSW                                     | RW | UNSIGNED<br>32 |
| <b>48013</b> | 8012 | COUNTER                | 6  | LSW                                     | RW | UNSIGNED<br>32 |
| <b>48014</b> | 8013 |                        |    | MSW                                     | RW |                |
| <b>48015</b> | 8014 | COUNTER                | 7  | LSW                                     | RW | UNSIGNED<br>32 |
| <b>48016</b> | 8015 |                        |    | MSW                                     | RW |                |
| <b>48017</b> | 8016 | COUNTER                | 8  | LSW                                     | RW | UNSIGNED<br>32 |
| <b>48018</b> | 8017 |                        |    | MSW                                     | RW |                |
| <b>48019</b> | 8018 | COUNTER                | 9  | LSW                                     | RW | UNSIGNED<br>32 |
| <b>48020</b> | 8019 |                        |    | MSW                                     | RW |                |
| <b>48021</b> | 8020 | COUNTER                | 10 | LSW                                     | RW | UNSIGNED<br>32 |
| <b>48022</b> | 8021 |                        |    | MSW                                     | RW |                |
| <b>48023</b> | 8022 | COUNTER                | 11 | LSW                                     | RW | UNSIGNED<br>32 |
| <b>48024</b> | 8023 |                        |    | MSW                                     | RW |                |
| <b>48025</b> | 8024 | COUNTER                | 12 | LSW                                     | RW | UNSIGNED<br>32 |
| <b>48026</b> | 8025 |                        |    | MSW                                     | RW |                |
| <b>48027</b> | 8026 | COUNTER                | 13 | LSW                                     | RW | UNSIGNED<br>32 |
| <b>48028</b> | 8027 |                        |    | MSW                                     | RW |                |
| <b>48029</b> | 8028 | COUNTER                | 14 | LSW                                     | RW | UNSIGNED<br>32 |
| <b>48030</b> | 8029 |                        |    | MSW                                     | RW |                |
| <b>48031</b> | 8030 | COUNTER                | 15 | LSW                                     | RW | UNSIGNED<br>32 |
| <b>48032</b> | 8031 |                        |    | MSW                                     | RW |                |
| <b>48033</b> | 8032 | COUNTER                | 16 | LSW                                     | RW | UNSIGNED<br>32 |
| <b>48034</b> | 8033 |                        |    | MSW                                     | RW |                |
| <b>48035</b> | 8034 | INT<br>MEASURE<br>TLOW | 1  | Tlow Integer<br>measure<br>[x 50us] LSW | RO | UNSIGNED<br>32 |
| <b>48036</b> | 8035 |                        |    | Tlow Integer<br>measure<br>[x 50us] MSW | RO |                |
| <b>48037</b> | 8036 | INT<br>MEASURE<br>TLOW | 2  | Tlow Integer<br>measure<br>[x 50us] LSW | RO | UNSIGNED<br>32 |
| <b>48038</b> | 8037 |                        |    | Tlow Integer<br>measure [ms]<br>MSW     | RO |                |
| <b>48039</b> | 8038 | INT<br>MEASURE<br>TLOW | 3  | Tlow Integer<br>measure<br>[x 50us] LSW | RO | UNSIGNED<br>32 |
| <b>48040</b> | 8039 |                        |    | Tlow Integer<br>measure<br>[x 50us] MSW | RO |                |
| <b>48041</b> | 8040 | INT<br>MEASURE<br>TLOW | 4  | Tlow Integer<br>measure [ms]<br>LSW     | RO | UNSIGNED<br>32 |

|              |      |                        |    |                                      |    |                |
|--------------|------|------------------------|----|--------------------------------------|----|----------------|
| <b>48042</b> | 8041 |                        |    | Tlow Integer measure<br>[x 50us] MSW | RO |                |
| <b>48043</b> | 8042 | INT<br>MEASURE<br>TLOW | 5  | Tlow Integer measure<br>[x 50us] LSW | RO | UNSIGNED<br>32 |
| <b>48044</b> | 8043 |                        |    | Tlow Integer measure<br>[x 50us] MSW | RO |                |
| <b>48045</b> | 8044 | INT<br>MEASURE<br>TLOW | 6  | Tlow Integer measure<br>[x 50us] LSW | RO | UNSIGNED<br>32 |
| <b>48046</b> | 8045 |                        |    | Tlow Integer measure [ms]<br>MSW     | RO |                |
| <b>48047</b> | 8046 | INT<br>MEASURE<br>TLOW | 7  | Tlow Integer measure<br>[x 50us] LSW | RO | UNSIGNED<br>32 |
| <b>48048</b> | 8047 |                        |    | Tlow Integer measure<br>[x 50us] MSW | RO |                |
| <b>48049</b> | 8048 | INT<br>MEASURE<br>TLOW | 8  | Tlow Integer measure<br>[x 50us] LSW | RO | UNSIGNED<br>32 |
| <b>48050</b> | 8049 |                        |    | Tlow Integer measure<br>[x 50us] MSW | RO |                |
| <b>48051</b> | 8050 | INT<br>MEASURE<br>TLOW | 9  | Tlow Integer measure<br>[x 50us] LSW | RO | UNSIGNED<br>32 |
| <b>48052</b> | 8051 |                        |    | Tlow Integer measure<br>[x 50us] MSW | RO |                |
| <b>48053</b> | 8052 | INT<br>MEASURE<br>TLOW | 10 | Tlow Integer measure<br>[x 50us] LSW | RO | UNSIGNED<br>32 |
| <b>48054</b> | 8053 |                        |    | Tlow Integer measure<br>[x 50us] MSW | RO |                |
| <b>48055</b> | 8054 | INT<br>MEASURE<br>TLOW | 11 | Tlow Integer measure<br>[x 50us] LSW | RO | UNSIGNED<br>32 |
| <b>48056</b> | 8055 |                        |    | Tlow Integer measure<br>[x 50us] MSW | RO |                |
| <b>48057</b> | 8056 | INT<br>MEASURE<br>TLOW | 12 | Tlow Integer measure [ms]<br>LSW     | RO | UNSIGNED<br>32 |

|              |      |                         |    |                                       |    |                |
|--------------|------|-------------------------|----|---------------------------------------|----|----------------|
| <b>48058</b> | 8057 |                         |    | Tlow Integer measure<br>[x 50us] MSW  | RO |                |
| <b>48059</b> | 8058 | INT<br>MEASURE<br>TLOW  | 13 | Tlow Integer measure<br>[x 50us] LSW  | RO | UNSIGNED<br>32 |
| <b>48060</b> | 8059 |                         |    | Tlow Integer measure<br>[x 50us] MSW  | RO |                |
| <b>48061</b> | 8060 | INT<br>MEASURE<br>TLOW  | 14 | Tlow Integer measure<br>[x 50us] LSW  | RO | UNSIGNED<br>32 |
| <b>48062</b> | 8061 |                         |    | Tlow Integer measure [ms]<br>MSW      | RO |                |
| <b>48063</b> | 8062 | INT<br>MEASURE<br>TLOW  | 15 | Tlow Integer measure<br>[x 50us] LSW  | RO | UNSIGNED<br>32 |
| <b>48064</b> | 8063 |                         |    | Tlow Integer measure<br>[x 50us] MSW  | RO |                |
| <b>48065</b> | 8064 | INT<br>MEASURE<br>TLOW  | 16 | Tlow Integer measure<br>[x 50us] LSW  | RO | UNSIGNED<br>32 |
| <b>48066</b> | 8065 |                         |    | Tlow Integer measure<br>[x 50us] MSW  | RO |                |
| <b>48067</b> | 8066 | INT<br>MEASURE<br>THIGH | 1  | Thigh Integer measure<br>[x 50us] LSW | RO | UNSIGNED<br>32 |
| <b>48068</b> | 8067 |                         |    | Thigh Integer measure [ms]<br>MSW     | RO |                |
| <b>48069</b> | 8068 | INT<br>MEASURE<br>THIGH | 2  | Thigh Integer measure<br>[x 50us] LSW | RO | UNSIGNED<br>32 |
| <b>48070</b> | 8069 |                         |    | Thigh Integer measure<br>[x 50us] MSW | RO |                |
| <b>48071</b> | 8070 | INT<br>MEASURE<br>THIGH | 3  | Thigh Integer measure<br>[x 50us] LSW | RO | UNSIGNED<br>32 |
| <b>48072</b> | 8071 |                         |    | Thigh Integer measure<br>[x 50us] MSW | RO |                |
| <b>48073</b> | 8072 | INT<br>MEASURE<br>THIGH | 4  | Thigh Integer measure<br>[x 50us] LSW | RO | UNSIGNED<br>32 |

|              |      |                   |    |                                    |    |             |
|--------------|------|-------------------|----|------------------------------------|----|-------------|
| <b>48074</b> | 8073 |                   |    | Thigh Integer measure [x 50us] MSW | RO |             |
| <b>48075</b> | 8074 | INT MEASURE THIGH | 5  | Thigh Integer measure [x 50us] LSW | RO | UNSIGNED 32 |
| <b>48076</b> | 8075 |                   |    | Thigh Integer measure [x 50us] MSW | RO |             |
| <b>48077</b> | 8076 | INT MEASURE THIGH | 6  | Thigh Integer measure [x 50us] LSW | RO | UNSIGNED 32 |
| <b>48078</b> | 8077 |                   |    | Thigh Integer measure [ms] MSW     | RO |             |
| <b>48079</b> | 8078 | INT MEASURE THIGH | 7  | Thigh Integer measure [x 50us] LSW | RO | UNSIGNED 32 |
| <b>48080</b> | 8079 |                   |    | Thigh Integer measure [x 50us] MSW | RO |             |
| <b>48081</b> | 8080 | INT MEASURE THIGH | 8  | Thigh Integer measure [x 50us] LSW | RO | UNSIGNED 32 |
| <b>48082</b> | 8081 |                   |    | Thigh Integer measure [x 50us] MSW | RO |             |
| <b>48083</b> | 8082 | INT MEASURE THIGH | 9  | Thigh Integer measure [x 50us] LSW | RO | UNSIGNED 32 |
| <b>48084</b> | 8083 |                   |    | Thigh Integer measure [x 50us] MSW | RO |             |
| <b>48085</b> | 8084 | INT MEASURE THIGH | 10 | Thigh Integer measure [x 50us] LSW | RO | UNSIGNED 32 |
| <b>48086</b> | 8085 |                   |    | Thigh Integer measure [x 50us] MSW | RO |             |
| <b>48087</b> | 8086 | INT MEASURE THIGH | 11 | Thigh Integer measure [x 50us] LSW | RO | UNSIGNED 32 |
| <b>48088</b> | 8087 |                   |    | Thigh Integer measure [x 50us] MSW | RO |             |
| <b>48089</b> | 8088 | INT MEASURE THIGH | 12 | Thigh Integer measure [x 50us] LSW | RO | UNSIGNED 32 |

|              |      |                    |    |                                     |    |             |
|--------------|------|--------------------|----|-------------------------------------|----|-------------|
| <b>48090</b> | 8089 |                    |    | Thigh Integer measure [x 50us] MSW  | RO |             |
| <b>48091</b> | 8090 | INT MEASURE THIGH  | 13 | Thigh Integer measure [ms] LSW      | RO | UNSIGNED 32 |
| <b>48092</b> | 8091 |                    |    | Thigh Integer measure [x 50us] MSW  | RO |             |
| <b>48093</b> | 8092 | INT MEASURE THIGH  | 14 | Thigh Integer measure [x 50us] LSW  | RO | UNSIGNED 32 |
| <b>48094</b> | 8093 |                    |    | Thigh Integer measure [ms] MSW      | RO |             |
| <b>48095</b> | 8094 | INT MEASURE THIGH  | 15 | Thigh Integer measure [x 50us] LSW  | RO | UNSIGNED 32 |
| <b>48096</b> | 8095 |                    |    | Thigh Integer measure [x 50us] MSW  | RO |             |
| <b>48097</b> | 8096 | INT MEASURE THIGH  | 16 | Thigh Integer measure [x 50us] LSW  | RO | UNSIGNED 32 |
| <b>48098</b> | 8097 |                    |    | Thigh Integer measure [x 50us] MSW  | RO |             |
| <b>48099</b> | 8098 | INT MEASURE PERIOD | 1  | Period Integer measure [x 50us] LSW | RO | UNSIGNED 32 |
| <b>48100</b> | 8099 |                    |    | Period Integer measure [x 50us] MSW | RO |             |
| <b>48101</b> | 8100 | INT MEASURE PERIOD | 2  | Period Integer measure [x 50us] LSW | RO | UNSIGNED 32 |
| <b>48102</b> | 8101 |                    |    | Period Integer measure [x 50us] MSW | RO |             |
| <b>48103</b> | 8102 | INT MEASURE PERIOD | 3  | Period Integer measure [x 50us] LSW | RO | UNSIGNED 32 |



|              |      |                          |   |                                              |    |                |
|--------------|------|--------------------------|---|----------------------------------------------|----|----------------|
| <b>48104</b> | 8103 |                          |   | Period<br>Integer<br>measure<br>[x 50us] MSW | RO |                |
| <b>48105</b> | 8104 | INT<br>MEASURE<br>PERIOD | 4 | Period<br>Integer<br>measure<br>[x 50us] LSW | RO | UNSIGNED<br>32 |
| <b>48106</b> | 8105 |                          |   | Period<br>Integer<br>measure<br>[x 50us] MSW | RO |                |
| <b>48107</b> | 8106 | INT<br>MEASURE<br>PERIOD | 5 | Period<br>Integer<br>measure<br>[x 50us] LSW | RO | UNSIGNED<br>32 |
| <b>48108</b> | 8107 |                          |   | Period<br>Integer<br>measure<br>[x 50us] MSW | RO |                |
| <b>48109</b> | 8108 | INT<br>MEASURE<br>PERIOD | 6 | Period<br>Integer<br>measure<br>[x 50us] LSW | RO | UNSIGNED<br>32 |
| <b>48110</b> | 8109 |                          |   | Period<br>Integer<br>measure<br>[x 50us] MSW | RO |                |
| <b>48111</b> | 8110 | INT<br>MEASURE<br>PERIOD | 7 | Period<br>Integer<br>measure<br>[x 50us] LSW | RO | UNSIGNED<br>32 |
| <b>48112</b> | 8111 |                          |   | Period<br>Integer<br>measure<br>[x 50us] MSW | RO |                |
| <b>48113</b> | 8112 | INT<br>MEASURE<br>PERIOD | 8 | Period<br>Integer<br>measure<br>[x 50us] LSW | RO | UNSIGNED<br>32 |
| <b>48114</b> | 8113 |                          |   | Period<br>Integer<br>measure<br>[x 50us] MSW | RO |                |
| <b>48115</b> | 8114 | INT<br>MEASURE<br>PERIOD | 9 | Period<br>Integer<br>measure<br>[x 50us] LSW | RO | UNSIGNED<br>32 |

|              |      |                    |    |                                        |    |             |
|--------------|------|--------------------|----|----------------------------------------|----|-------------|
| <b>48116</b> | 8115 |                    |    | Period Integer measure<br>[x 50us] MSW | RO |             |
| <b>48117</b> | 8116 | INT MEASURE PERIOD | 10 | Period Integer measure<br>[x 50us] LSW | RO | UNSIGNED 32 |
| <b>48118</b> | 8117 |                    |    | Period Integer measure<br>[x 50us] MSW | RO |             |
| <b>48119</b> | 8118 | INT MEASURE PERIOD | 11 | Period Integer measure<br>[x 50us] LSW | RO | UNSIGNED 32 |
| <b>48120</b> | 8119 |                    |    | Period Integer measure<br>[x 50us] MSW | RO |             |
| <b>48121</b> | 8120 | INT MEASURE PERIOD | 12 | Period Integer measure<br>[x 50us] LSW | RO | UNSIGNED 32 |
| <b>48122</b> | 8121 |                    |    | Period Integer measure<br>[x 50us] MSW | RO |             |
| <b>48123</b> | 8122 | INT MEASURE PERIOD | 13 | Period Integer measure<br>[x 50us] LSW | RO | UNSIGNED 32 |
| <b>48124</b> | 8123 |                    |    | Period Integer measure<br>[x 50us] MSW | RO |             |
| <b>48125</b> | 8124 | INT MEASURE PERIOD | 14 | Period Integer measure<br>[x 50us] LSW | RO | UNSIGNED 32 |
| <b>48126</b> | 8125 |                    |    | Period Integer measure<br>[x 50us] MSW | RO |             |
| <b>48127</b> | 8126 | INT MEASURE PERIOD | 15 | Period Integer measure<br>[x 50us] LSW | RO | UNSIGNED 32 |

|              |      |                          |    |                                              |    |                |
|--------------|------|--------------------------|----|----------------------------------------------|----|----------------|
| <b>48128</b> | 8127 |                          |    | Period<br>Integer<br>measure<br>[x 50us] MSW | RO |                |
| <b>48129</b> | 8128 | INT<br>MEASURE<br>PERIOD | 16 | Period<br>Integer<br>measure<br>[x 50us] LSW | RO | UNSIGNED<br>32 |
| <b>48130</b> | 8129 |                          |    | Period<br>Integer<br>measure<br>[x 50us] MSW | RO |                |
| <b>48131</b> | 8130 | INT<br>MEASURE<br>FREQ   | 1  | Frequency<br>Integer<br>Measure [Hz]         | RO | UNSIGNED<br>16 |
| <b>48132</b> | 8131 | INT<br>MEASURE<br>FREQ   | 2  | Frequency<br>Integer<br>Measure [Hz]         | RO | UNSIGNED<br>16 |
| <b>48133</b> | 8132 | INT<br>MEASURE<br>FREQ   | 3  | Frequency<br>Integer<br>Measure [Hz]         | RO | UNSIGNED<br>16 |
| <b>48134</b> | 8133 | INT<br>MEASURE<br>FREQ   | 4  | Frequency<br>Integer<br>Measure [Hz]         | RO | UNSIGNED<br>16 |
| <b>48135</b> | 8134 | INT<br>MEASURE<br>FREQ   | 5  | Frequency<br>Integer<br>Measure [Hz]         | RO | UNSIGNED<br>16 |
| <b>48136</b> | 8135 | INT<br>MEASURE<br>FREQ   | 6  | Frequency<br>Integer<br>Measure [Hz]         | RO | UNSIGNED<br>16 |
| <b>48137</b> | 8136 | INT<br>MEASURE<br>FREQ   | 7  | Frequency<br>Integer<br>Measure [Hz]         | RO | UNSIGNED<br>16 |
| <b>48138</b> | 8137 | INT<br>MEASURE<br>FREQ   | 8  | Frequency<br>Integer<br>Measure [Hz]         | RO | UNSIGNED<br>16 |
| <b>48139</b> | 8138 | INT<br>MEASURE<br>FREQ   | 9  | Frequency<br>Integer<br>Measure [Hz]         | RO | UNSIGNED<br>16 |
| <b>48140</b> | 8139 | INT<br>MEASURE<br>FREQ   | 10 | Frequency<br>Integer<br>Measure [Hz]         | RO | UNSIGNED<br>16 |

|              |      |                        |    |                                      |    |                |
|--------------|------|------------------------|----|--------------------------------------|----|----------------|
| <b>48141</b> | 8140 | INT<br>MEASURE<br>FREQ | 11 | Frequency<br>Integer<br>Measure [Hz] | RO | UNSIGNED<br>16 |
| <b>48142</b> | 8141 | INT<br>MEASURE<br>FREQ | 12 | Frequency<br>Integer<br>Measure [Hz] | RO | UNSIGNED<br>16 |
| <b>48143</b> | 8142 | INT<br>MEASURE<br>FREQ | 13 | Frequency<br>Integer<br>Measure [Hz] | RO | UNSIGNED<br>16 |
| <b>48144</b> | 8143 | INT<br>MEASURE<br>FREQ | 14 | Frequency<br>Integer<br>Measure [Hz] | RO | UNSIGNED<br>16 |
| <b>48145</b> | 8144 | INT<br>MEASURE<br>FREQ | 15 | Frequency<br>Integer<br>Measure [Hz] | RO | UNSIGNED<br>16 |
| <b>48146</b> | 8145 | INT<br>MEASURE<br>FREQ | 16 | Frequency<br>Integer<br>Measure [Hz] | RO | UNSIGNED<br>16 |

**22.3. R-16DI-8DO: TABLE OF MODBUS REGISTERS 0x COIL STATUS (FUNCTION CODE 1)**

| <b>ADDRESS (0x)</b> | <b>OFFSET ADDRESS (0x)</b> | <b>REGISTER</b> | <b>CHANNEL</b> | <b>DESCRIPTION</b> | <b>W/R</b> | <b>TYPE</b> |
|---------------------|----------------------------|-----------------|----------------|--------------------|------------|-------------|
| <b>1</b>            | <b>0</b>                   | DIGITAL INPUT   | <b>1</b>       | DIGITAL INPUT      | RO         | BIT         |
| <b>2</b>            | <b>1</b>                   | DIGITAL INPUT   | <b>2</b>       | DIGITAL INPUT      | RO         | BIT         |
| <b>3</b>            | <b>2</b>                   | DIGITAL INPUT   | <b>3</b>       | DIGITAL INPUT      | RO         | BIT         |
| <b>4</b>            | <b>3</b>                   | DIGITAL INPUT   | <b>4</b>       | DIGITAL INPUT      | RO         | BIT         |
| <b>5</b>            | <b>4</b>                   | DIGITAL INPUT   | <b>5</b>       | DIGITAL INPUT      | RO         | BIT         |
| <b>6</b>            | <b>5</b>                   | DIGITAL INPUT   | <b>6</b>       | DIGITAL INPUT      | RO         | BIT         |
| <b>7</b>            | <b>6</b>                   | DIGITAL INPUT   | <b>7</b>       | DIGITAL INPUT      | RO         | BIT         |
| <b>8</b>            | <b>7</b>                   | DIGITAL INPUT   | <b>8</b>       | DIGITAL INPUT      | RO         | BIT         |
| <b>9</b>            | <b>8</b>                   | DIGITAL INPUT   | <b>9</b>       | DIGITAL INPUT      | RO         | BIT         |
| <b>10</b>           | <b>9</b>                   | DIGITAL INPUT   | <b>10</b>      | DIGITAL INPUT      | RO         | BIT         |
| <b>11</b>           | <b>10</b>                  | DIGITAL INPUT   | <b>11</b>      | DIGITAL INPUT      | RO         | BIT         |
| <b>12</b>           | <b>11</b>                  | DIGITAL INPUT   | <b>12</b>      | DIGITAL INPUT      | RO         | BIT         |
| <b>13</b>           | <b>12</b>                  | DIGITAL INPUT   | <b>13</b>      | DIGITAL INPUT      | RO         | BIT         |
| <b>14</b>           | <b>13</b>                  | DIGITAL INPUT   | <b>14</b>      | DIGITAL INPUT      | RO         | BIT         |
| <b>15</b>           | <b>14</b>                  | DIGITAL INPUT   | <b>15</b>      | DIGITAL INPUT      | RO         | BIT         |
| <b>16</b>           | <b>15</b>                  | DIGITAL INPUT   | <b>16</b>      | DIGITAL INPUT      | RO         | BIT         |

| <b>ADDRESS (0x)</b> | <b>OFFSET ADDRESS (0x)</b> | <b>REGISTER</b> | <b>CHANNEL</b> | <b>DESCRIPTION</b> | <b>W/R</b> | <b>TYPE</b> |
|---------------------|----------------------------|-----------------|----------------|--------------------|------------|-------------|
| <b>33</b>           | 32                         | DIGITAL OUT     | 1              | DIGITAL OUTPUT     | RW         | BIT         |
| <b>34</b>           | 33                         | DIGITAL OUT     | 2              | DIGITAL OUTPUT     | RW         | BIT         |
| <b>35</b>           | 34                         | DIGITAL OUT     | 3              | DIGITAL OUTPUT     | RW         | BIT         |
| <b>36</b>           | 35                         | DIGITAL OUT     | 4              | DIGITAL OUTPUT     | RW         | BIT         |
| <b>37</b>           | 36                         | DIGITAL OUT     | 5              | DIGITAL OUTPUT     | RW         | BIT         |
| <b>38</b>           | 37                         | DIGITAL OUT     | 6              | DIGITAL OUTPUT     | RW         | BIT         |
| <b>39</b>           | 38                         | DIGITAL OUT     | 7              | DIGITAL OUTPUT     | RW         | BIT         |
| <b>40</b>           | 39                         | DIGITAL OUT     | 8              | DIGITAL OUTPUT     | RW         | BIT         |

| <b>ADDRESS (0x)</b> | <b>OFFSET ADDRESS (0x)</b> | <b>REGISTER</b> | <b>CHANNEL</b> | <b>DESCRIPTION</b> | <b>W/R</b> | <b>TYPE</b> |
|---------------------|----------------------------|-----------------|----------------|--------------------|------------|-------------|
| <b>101</b>          | 100                        | COUNTER RESET   | 1              | COUNTER RESET      | RW         | BIT         |
| <b>102</b>          | 101                        | COUNTER RESET   | 2              | COUNTER RESET      | RW         | BIT         |
| <b>103</b>          | 102                        | COUNTER RESET   | 3              | COUNTER RESET      | RW         | BIT         |
| <b>104</b>          | 103                        | COUNTER RESET   | 4              | COUNTER RESET      | RW         | BIT         |
| <b>105</b>          | 104                        | COUNTER RESET   | 5              | COUNTER RESET      | RW         | BIT         |
| <b>106</b>          | 105                        | COUNTER RESET   | 6              | COUNTER RESET      | RW         | BIT         |
| <b>107</b>          | 106                        | COUNTER RESET   | 7              | COUNTER RESET      | RW         | BIT         |
| <b>108</b>          | 107                        | COUNTER RESET   | 8              | COUNTER RESET      | RW         | BIT         |
| <b>109</b>          | 108                        | COUNTER RESET   | 9              | COUNTER RESET      | RW         | BIT         |
| <b>110</b>          | 109                        | COUNTER RESET   | 10             | COUNTER RESET      | RW         | BIT         |
| <b>111</b>          | 110                        | COUNTER RESET   | 11             | COUNTER RESET      | RW         | BIT         |
| <b>112</b>          | 111                        | COUNTER RESET   | 12             | COUNTER RESET      | RW         | BIT         |
| <b>113</b>          | 112                        | COUNTER RESET   | 13             | COUNTER RESET      | RW         | BIT         |
| <b>114</b>          | 113                        | COUNTER RESET   | 14             | COUNTER RESET      | RW         | BIT         |
| <b>115</b>          | 114                        | COUNTER RESET   | 15             | COUNTER RESET      | RW         | BIT         |
| <b>116</b>          | 115                        | COUNTER RESET   | 16             | COUNTER RESET      | RW         | BIT         |

**22.4. R-16DI-8DO: TABLE OF REGISTERS 1x INPUT STATUS (FUNCTION CODE 2)**

| <b>ADDRESS (1x)</b> | <b>OFFSET ADDRESS (1x)</b> | <b>REGISTER</b> | <b>CHANNEL</b> | <b>DESCRIPTION</b> | <b>W/R</b> | <b>TYPE</b> |
|---------------------|----------------------------|-----------------|----------------|--------------------|------------|-------------|
| <b>10001</b>        | 0                          | DIGITAL INPUT   | 1              | DIGITAL INPUT      | RO         | BIT         |
| <b>10002</b>        | 1                          | DIGITAL INPUT   | 2              | DIGITAL INPUT      | RO         | BIT         |
| <b>10003</b>        | 2                          | DIGITAL INPUT   | 3              | DIGITAL INPUT      | RO         | BIT         |
| <b>10004</b>        | 3                          | DIGITAL INPUT   | 4              | DIGITAL INPUT      | RO         | BIT         |
| <b>10005</b>        | 4                          | DIGITAL INPUT   | 5              | DIGITAL INPUT      | RO         | BIT         |
| <b>10006</b>        | 5                          | DIGITAL INPUT   | 6              | DIGITAL INPUT      | RO         | BIT         |
| <b>10007</b>        | 6                          | DIGITAL INPUT   | 7              | DIGITAL INPUT      | RO         | BIT         |
| <b>10008</b>        | 7                          | DIGITAL INPUT   | 8              | DIGITAL INPUT      | RO         | BIT         |
| <b>10009</b>        | 8                          | DIGITAL INPUT   | 9              | DIGITAL INPUT      | RO         | BIT         |
| <b>10010</b>        | 9                          | DIGITAL INPUT   | 10             | DIGITAL INPUT      | RO         | BIT         |
| <b>10011</b>        | 10                         | DIGITAL INPUT   | 11             | DIGITAL INPUT      | RO         | BIT         |
| <b>10012</b>        | 11                         | DIGITAL INPUT   | 12             | DIGITAL INPUT      | RO         | BIT         |
| <b>10013</b>        | 12                         | DIGITAL INPUT   | 13             | DIGITAL INPUT      | RO         | BIT         |
| <b>10014</b>        | 13                         | DIGITAL INPUT   | 14             | DIGITAL INPUT      | RO         | BIT         |
| <b>10015</b>        | 14                         | DIGITAL INPUT   | 15             | DIGITAL INPUT      | RO         | BIT         |
| <b>10016</b>        | 15                         | DIGITAL INPUT   | 16             | DIGITAL INPUT      | RO         | BIT         |

## 23. MODBUS REGISTER TABLE FOR THE R-8AI-8DIDO DEVICE

### 23.1. R-8AI-8DIDO: MODBUS 4X HOLDING REGISTERS TABLE (FUNCTION CODE 3)

| <b>ADDRESS<br/>(4x)</b>        | <b>OFFSET<br/>ADDRESS<br/>(4x)</b> | <b>REGISTER</b>                       | <b>CHANNEL</b> | <b>DESCRIPTION</b>                                              | <b>W/R</b> | <b>TYPE</b>        |
|--------------------------------|------------------------------------|---------------------------------------|----------------|-----------------------------------------------------------------|------------|--------------------|
| <b>40001</b>                   | 0                                  | MACHINE-ID                            | -              | Device ID                                                       | RO         | UNSIGNED<br>16 BIT |
| <b>40002</b>                   | 1                                  | FIRMWARE<br>REVISION<br>(Maior/Minor) | -              | FW revision                                                     | RO         | UNSIGNED<br>16 BIT |
| <b>40003</b>                   | 2                                  | FIRMWARE<br>REVISION<br>(Fix/Build)   | -              | FW revision                                                     | RO         | UNSIGNED<br>16 BIT |
| <b>40004</b>                   | 3                                  | FW CODE                               | -              | FW code                                                         | RO         | UNSIGNED<br>16 BIT |
| <b>40005</b>                   | 4                                  | RESERVED                              | -              | -                                                               | RO         | UNSIGNED<br>16 BIT |
| <b>40006</b>                   | 5                                  | RESERVED                              | -              | -                                                               | RO         | UNSIGNED<br>16 BIT |
| <b>40007</b>                   | 6                                  | BOARD-ID                              | -              | HW revision                                                     | RO         | UNSIGNED<br>16 BIT |
| <b>40008</b>                   | 7                                  | BOOT REVISION<br>(Maior/Minor)        | -              | FW Bootloader revision                                          | RO         | UNSIGNED<br>16 BIT |
| <b>40009</b>                   | 8                                  | BOOT REVISION<br>(Fix/Build)          | -              | FW Bootloader revision                                          | RO         | UNSIGNED<br>16 BIT |
| <b>40010</b>                   | 9                                  | RESERVED                              | -              | -                                                               | RO         | UNSIGNED<br>16 BIT |
| <b>40011</b>                   | 10                                 | RESERVED                              | -              | -                                                               | RO         | UNSIGNED<br>16 BIT |
| <b>40012</b>                   | 11                                 | RESERVED                              | -              | -                                                               | RO         | UNSIGNED<br>16 BIT |
| <b>40013</b>                   | 12                                 | COMMAND                               | -              | Supported command list:<br>49568 decimal to perform<br>a Reboot | RW         | UNSIGNED<br>16 BIT |
| <b>40014<br/>...<br/>40058</b> | 13...57                            | RESERVED                              | -              | -                                                               | RW         | UNSIGNED<br>16 BIT |



| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>ADDRESS<br/>(4x)</b> | <b>REGISTER</b>             | <b>CHANNEL</b> | <b>DESCRIPTION</b>                                                                                                                                                                                                                                                | <b>W/R</b> | <b>TYPE</b>                 |
|-------------------------|------------------------------------|-----------------------------|----------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|------------|-----------------------------|
| <b>40059</b>            | 58                                 | DIGITAL OUT                 | [8...1]        | Digital output values<br>0 = Not active output<br>1 = Active output<br>THE LEAST<br>SIGNIFICANT BIT<br>RELATES TO IO1<br>EXAMPLE:<br>5 decimal = 0000<br>0000 0000 0101<br>binary =><br>OUT1= Active<br>OUT2= Not Active<br>OUT3= Active<br>OUT4..OUT8=Not Active | RW         | UNSIGNED<br>16 BIT          |
| <b>40060</b>            | 59                                 | DIGITAL IN                  | [8...1]        | Digital input values<br>0 = Low<br>1 = High<br>THE LEAST<br>SIGNIFICANT BIT<br>RELATES TO IO1<br>EXAMPLE:<br>5 decimal = 0000<br>0000 0000 0101<br>binary =><br>IN1 = High<br>IN2 = Low<br>IN3 = High<br>OUT4..OUT8 = Low                                         | RO         | UNSIGNED<br>16 BIT          |
| <b>40117</b>            | 116                                | CHANNEL<br>INTEGER<br>VALUE | 1              | Channel<br>measurement (unit of<br>measurement depending<br>on the type of<br>measurement or<br>configuration) [V] or [mV]<br>or [mA] or [°C] or [Ohm]<br>or Scaled                                                                                               | RO         | SIGNED<br>INTEGER<br>16 BIT |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>ADDRESS<br/>(4x)</b> | <b>REGISTER</b>             | <b>CHANNEL</b> | <b>DESCRIPTION</b>                                                                                                                                                  | <b>W/R</b> | <b>TYPE</b>                 |
|-------------------------|------------------------------------|-----------------------------|----------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------|------------|-----------------------------|
| <b>40118</b>            | 117                                | CHANNEL<br>INTEGER<br>VALUE | 2              | Channel<br>measurement (unit of<br>measurement depending<br>on the type of<br>measurement or<br>configuration) [V] or [mV]<br>or [mA] or [°C] or [Ohm]<br>or Scaled | RO         | SIGNED<br>INTEGER<br>16 BIT |
| <b>40119</b>            | 118                                | CHANNEL<br>INTEGER<br>VALUE | 3              | Channel<br>measurement (unit of<br>measurement depending<br>on the type of<br>measurement or<br>configuration) [V] or [mV]<br>or [mA] or [°C] or [Ohm]<br>or Scaled | RO         | SIGNED<br>INTEGER<br>16 BIT |
| <b>40120</b>            | 119                                | CHANNEL<br>INTEGER<br>VALUE | 4              | Channel<br>measurement (unit of<br>measurement depending<br>on the type of<br>measurement or<br>configuration) [V] or [mV]<br>or [mA] or [°C] or [Ohm]<br>or Scaled | RO         | SIGNED<br>INTEGER<br>16 BIT |
| <b>40121</b>            | 120                                | CHANNEL<br>INTEGER<br>VALUE | 5              | Channel<br>measurement (unit of<br>measurement depending<br>on the type of<br>measurement or<br>configuration) [V] or [mV]<br>or [mA] or [°C] or [Ohm]<br>or Scaled | RO         | SIGNED<br>INTEGER<br>16 BIT |
| <b>40122</b>            | 121                                | CHANNEL<br>INTEGER<br>VALUE | 6              | Channel<br>measurement (unit of<br>measurement depending<br>on the type of<br>measurement or<br>configuration) [V] or [mV]<br>or [mA] or [°C] or [Ohm]<br>or Scaled | RO         | SIGNED<br>INTEGER<br>16 BIT |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>ADDRESS<br/>(4x)</b> | <b>REGISTER</b>             | <b>CHANNEL</b> | <b>DESCRIPTION</b>                                                                                                                                                  | <b>W/R</b> | <b>TYPE</b>                 |
|-------------------------|------------------------------------|-----------------------------|----------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------|------------|-----------------------------|
| <b>40123</b>            | 122                                | CHANNEL<br>INTEGER<br>VALUE | 7              | Channel<br>measurement (unit of<br>measurement depending<br>on the type of<br>measurement or<br>configuration) [V] or [mV]<br>or [mA] or [°C] or [Ohm]<br>or Scaled | RO         | SIGNED<br>INTEGER<br>16 BIT |
| <b>40124</b>            | 123                                | CHANNEL<br>INTEGER<br>VALUE | 8              | Channel<br>measurement (unit of<br>measurement depending<br>on the type of<br>measurement or<br>configuration) [V] or [mV]<br>or [mA] or [°C] or [Ohm]<br>or Scaled | RO         | SIGNED<br>INTEGER<br>16 BIT |
| <b>40317</b>            | 316                                | CHANNEL VALUE               | 1              | Channel<br>measurement (unit of<br>measurement depending<br>on the type of<br>measurement or<br>configuration) [V] or [mV]<br>or [mA] or [°C] or [Ohm]<br>or Scaled | RO         | FLOAT 32                    |
| <b>40318</b>            | 317                                |                             |                |                                                                                                                                                                     | RO         |                             |
| <b>40319</b>            | 318                                | CHANNEL VALUE               | 2              | Channel<br>measurement (unit of<br>measurement depending<br>on the type of<br>measurement or<br>configuration) [V] or [mV]<br>or [mA] or [°C] or [Ohm]<br>or Scaled | RO         | FLOAT 32                    |
| <b>40320</b>            | 319                                |                             |                |                                                                                                                                                                     | RO         |                             |
| <b>40321</b>            | 320                                | CHANNEL VALUE               | 3              | Channel<br>measurement (unit of<br>measurement depending<br>on the type of<br>measurement or<br>configuration) [V] or [mV]<br>or [mA] or [°C] or [Ohm]<br>or Scaled | RO         | FLOAT 32                    |
| <b>40322</b>            | 321                                |                             |                |                                                                                                                                                                     | RO         |                             |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>ADDRESS<br/>(4x)</b> | <b>REGISTER</b> | <b>CHANNEL</b> | <b>DESCRIPTION</b>                                                                                                                                                  | <b>W/R</b> | <b>TYPE</b> |
|-------------------------|------------------------------------|-----------------|----------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------|------------|-------------|
| <b>40323</b>            | 322                                | CHANNEL VALUE   | 4              | Channel<br>measurement (unit of<br>measurement depending<br>on the type of<br>measurement or<br>configuration) [V] or [mV]<br>or [mA] or [°C] or [Ohm]<br>or Scaled | RO         | FLOAT 32    |
| <b>40324</b>            | 323                                |                 |                |                                                                                                                                                                     | RO         |             |
| <b>40325</b>            | 324                                | CHANNEL VALUE   | 5              | Channel<br>measurement (unit of<br>measurement depending<br>on the type of<br>measurement or<br>configuration) [V] or [mV]<br>or [mA] or [°C] or [Ohm]<br>or Scaled | RO         | FLOAT 32    |
| <b>40326</b>            | 325                                |                 |                |                                                                                                                                                                     | RO         |             |
| <b>40327</b>            | 326                                | CHANNEL VALUE   | 6              | Channel<br>measurement (unit of<br>measurement depending<br>on the type of<br>measurement or<br>configuration) [V] or [mV]<br>or [mA] or [°C] or [Ohm]<br>or Scaled | RO         | FLOAT 32    |
| <b>40328</b>            | 327                                |                 |                |                                                                                                                                                                     | RO         |             |
| <b>40329</b>            | 328                                | CHANNEL VALUE   | 7              | Channel<br>measurement (unit of<br>measurement depending<br>on the type of<br>measurement or<br>configuration) [V] or [mV]<br>or [mA] or [°C] or [Ohm]<br>or Scaled | RO         | FLOAT 32    |
| <b>40330</b>            | 329                                |                 |                |                                                                                                                                                                     | RO         |             |
| <b>40331</b>            | 330                                | CHANNEL VALUE   | 8              | Channel<br>measurement (unit of<br>measurement depending<br>on the type of<br>measurement or<br>configuration) [V] or [mV]<br>or [mA] or [°C] or [Ohm]<br>or Scaled | RO         | FLOAT 32    |
| <b>40332</b>            | 331                                |                 |                |                                                                                                                                                                     | RO         |             |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>ADDRESS<br/>(4x)</b> | <b>REGISTER</b>      | <b>CHANNEL</b> | <b>DESCRIPTION</b>                                                                                                                                                                                                                                                                                                                                                                                                                       | <b>W/R</b> | <b>TYPE</b>        |
|-------------------------|------------------------------------|----------------------|----------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|------------|--------------------|
| <b>40381</b>            | 380                                | ERRORS               | -              | Measurement errors<br>Bit = 0 OK<br>Bit = 1 FAIL<br>Bit[15]=CJ Err IN7&IN8<br>Bit[14]=CJ Err IN5&IN6<br>Bit[13]=CJ Err IN3&IN4<br>Bit[12]=CJ Err IN2&IN1<br>Bit[0]=GENERIC FAULT                                                                                                                                                                                                                                                         | RO         | UNSIGNED<br>16     |
| <b>40382</b>            | 381                                | ERRORS2              | -              | Measurement errors 2<br><br>Bit = 0 OK<br>Bit = 1 FAIL<br><br>Bit[15]=OVERFLOW IN8<br>Bit[14]=OVERFLOW IN7<br>Bit[13]=OVERFLOW IN6<br>Bit[12]=OVERFLOW IN5<br>Bit[11]=OVERFLOW IN4<br>Bit[10]=OVERFLOW IN3<br>Bit[9]=OVERFLOW IN2<br>Bit[8]=OVERFLOW IN1<br>Bit[7]=BURNOUT IN8<br>Bit[6]=BURNOUT IN7<br>Bit[5]=BURNOUT IN6<br>Bit[4]=BURNOUT IN5<br>Bit[3]=BURNOUT IN4<br>Bit[2]=BURNOUT IN3<br>Bit[1]=BURNOUT IN2<br>Bit[0]=BURNOUT IN1 | RO         | UNSIGNED<br>16 BIT |
| <b>40383</b>            | 382                                | DIP SWITCH<br>ERRORS | -              | Configuration Dip switch<br>Errors<br><br>Bit = 0 OK<br>Bit = 1 FAIL<br><br>Bit[7]=DIP SWITCH ERR IN8<br>Bit[6]= DIP SWITCH ERR IN7<br>Bit[5]= DIP SWITCH ERR IN6<br>Bit[4]= DIP SWITCH ERR IN5<br>Bit[3]= DIP SWITCH ERR IN4<br>Bit[2]= DIP SWITCH ERR IN3<br>Bit[1]= DIP SWITCH ERR IN2<br>Bit[0]= DIP SWITCH ERR IN1                                                                                                                  | RO         | UNSIGNED<br>16 BIT |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>ADDRESS<br/>(4x)</b> | <b>REGISTER</b>            | <b>CHANNEL</b> | <b>DESCRIPTION</b>                                                                                                                                       | <b>W/R</b> | <b>TYPE</b> |
|-------------------------|------------------------------------|----------------------------|----------------|----------------------------------------------------------------------------------------------------------------------------------------------------------|------------|-------------|
| <b>40416</b>            | 415                                | SECONDARY<br>CHANNEL VALUE | 1              | Secondary Channel<br>measurement (unit of<br>measurement depending<br>on the type of<br>measurement or<br>configuration) [V] or [mV]<br>or [mA] or [Ohm] | RO         | FLOAT 32    |
| <b>40417</b>            | 416                                |                            |                |                                                                                                                                                          | RO         |             |
| <b>40418</b>            | 417                                | SECONDARY<br>CHANNEL VALUE | 2              | Secondary Channel<br>measurement (unit of<br>measurement depending<br>on the type of<br>measurement or<br>configuration) [V] or [mV]<br>or [mA] or [Ohm] | RO         | FLOAT 32    |
| <b>40419</b>            | 418                                |                            |                |                                                                                                                                                          | RO         |             |
| <b>40420</b>            | 419                                | SECONDARY<br>CHANNEL VALUE | 3              | Secondary Channel<br>measurement (unit of<br>measurement depending<br>on the type of<br>measurement or<br>configuration) [V] or [mV]<br>or [mA] or [Ohm] | RO         | FLOAT 32    |
| <b>40421</b>            | 420                                |                            |                |                                                                                                                                                          | RO         |             |
| <b>40422</b>            | 421                                | SECONDARY<br>CHANNEL VALUE | 4              | Secondary Channel<br>measurement (unit of<br>measurement depending<br>on the type of<br>measurement or<br>configuration) [V] or [mV]<br>or [mA] or [Ohm] | RO         | FLOAT 32    |
| <b>40423</b>            | 422                                |                            |                |                                                                                                                                                          | RO         |             |
| <b>40424</b>            | 423                                | SECONDARY<br>CHANNEL VALUE | 5              | Secondary Channel<br>measurement (unit of<br>measurement depending<br>on the type of<br>measurement or<br>configuration) [V] or [mV]<br>or [mA] or [Ohm] | RO         | FLOAT 32    |
| <b>40425</b>            | 424                                |                            |                |                                                                                                                                                          | RO         |             |
| <b>40426</b>            | 425                                | SECONDARY<br>CHANNEL VALUE | 6              | Secondary Channel<br>measurement (unit of                                                                                                                | RO         | FLOAT 32    |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>ADDRESS<br/>(4x)</b> | <b>REGISTER</b>         | <b>CHANNEL</b> | <b>DESCRIPTION</b>                                                                                                                     | <b>W/R</b> | <b>TYPE</b> |
|-------------------------|------------------------------------|-------------------------|----------------|----------------------------------------------------------------------------------------------------------------------------------------|------------|-------------|
| <b>40427</b>            | 426                                |                         |                | measurement depending on the type of measurement or configuration) [V] or [mV] or [mA] or [Ohm]                                        | RO         |             |
| <b>40428</b>            | 427                                | SECONDARY CHANNEL VALUE | 7              | Secondary Channel measurement (unit of measurement depending on the type of measurement or configuration) [V] or [mV] or [mA] or [Ohm] | RO         | FLOAT 32    |
| <b>40429</b>            | 428                                |                         |                |                                                                                                                                        | RO         |             |
| <b>40430</b>            | 429                                | SECONDARY CHANNEL VALUE | 8              | Secondary Channel measurement (unit of measurement depending on the type of measurement or configuration) [V] or [mV] or [mA] or [Ohm] | RO         | FLOAT 32    |
| <b>40431</b>            | 430                                |                         |                |                                                                                                                                        | RO         |             |

**23.2. R-8AI-8DIDO: TABLE OF MODBUS REGISTERS 0x COIL STATUS (FUNCTION CODE 1)**

| <b>ADDRESS (0x)</b> | <b>OFFSET ADDRESS (0x)</b> | <b>REGISTER</b>             | <b>CHANNE<br/>L</b> | <b>DESCRIPTION</b>      | <b>W/<br/>R</b> | <b>TYP<br/>E</b> |
|---------------------|----------------------------|-----------------------------|---------------------|-------------------------|-----------------|------------------|
| <b>1</b>            | <b>0</b>                   | DIGITAL<br>INPUT/OUTPU<br>T | <b>1</b>            | DIGITAL<br>INPUT/OUTPUT | RW              | BIT              |
| <b>2</b>            | <b>1</b>                   | DIGITAL<br>INPUT/OUTPU<br>T | <b>2</b>            | DIGITAL<br>INPUT/OUTPUT | RW              | BIT              |
| <b>3</b>            | <b>2</b>                   | DIGITAL<br>INPUT/OUTPU<br>T | <b>3</b>            | DIGITAL<br>INPUT/OUTPUT | RW              | BIT              |
| <b>4</b>            | <b>3</b>                   | DIGITAL<br>INPUT/OUTPU<br>T | <b>4</b>            | DIGITAL<br>INPUT/OUTPUT | RW              | BIT              |
| <b>5</b>            | <b>4</b>                   | DIGITAL<br>INPUT/OUTPU<br>T | <b>5</b>            | DIGITAL<br>INPUT/OUTPUT | RW              | BIT              |
| <b>6</b>            | <b>5</b>                   | DIGITAL<br>INPUT/OUTPU<br>T | <b>6</b>            | DIGITAL<br>INPUT/OUTPUT | RW              | BIT              |
| <b>7</b>            | <b>6</b>                   | DIGITAL<br>INPUT/OUTPU<br>T | <b>7</b>            | DIGITAL<br>INPUT/OUTPUT | RW              | BIT              |
| <b>8</b>            | <b>7</b>                   | DIGITAL<br>INPUT/OUTPU<br>T | <b>8</b>            | DIGITAL<br>INPUT/OUTPUT | RW              | BIT              |



**23.3. R-8AI-8DIDO: TABLE OF MODBUS REGISTERS 1x INPUT STATUS (FUNCTION CODE 2)**

| <b>ADDRESS (1x)</b> | <b>OFFSET ADDRESS (1x)</b> | <b>REGISTER</b>  | <b>CHANNEL</b> | <b>DESCRIPTION</b>      | <b>W/R</b> | <b>TYPE</b> |
|---------------------|----------------------------|------------------|----------------|-------------------------|------------|-------------|
| <b>10001</b>        | <b>0</b>                   | DIGITAL<br>INPUT | <b>1</b>       | DIGITAL<br>INPUT/OUTPUT | RO         | BIT         |
| <b>10002</b>        | <b>1</b>                   | DIGITAL<br>INPUT | <b>2</b>       | DIGITAL<br>INPUT/OUTPUT | RO         | BIT         |
| <b>10003</b>        | <b>2</b>                   | DIGITAL<br>INPUT | <b>3</b>       | DIGITAL<br>INPUT/OUTPUT | RO         | BIT         |
| <b>10004</b>        | <b>3</b>                   | DIGITAL<br>INPUT | <b>4</b>       | DIGITAL<br>INPUT/OUTPUT | RO         | BIT         |
| <b>10005</b>        | <b>4</b>                   | DIGITAL<br>INPUT | <b>5</b>       | DIGITAL<br>INPUT/OUTPUT | RO         | BIT         |
| <b>10006</b>        | <b>5</b>                   | DIGITAL<br>INPUT | <b>6</b>       | DIGITAL<br>INPUT/OUTPUT | RO         | BIT         |
| <b>10007</b>        | <b>6</b>                   | DIGITAL<br>INPUT | <b>7</b>       | DIGITAL<br>INPUT/OUTPUT | RO         | BIT         |
| <b>10008</b>        | <b>7</b>                   | DIGITAL<br>INPUT | <b>8</b>       | DIGITAL<br>INPUT/OUTPUT | RO         | BIT         |

## 24. MODBUS REGISTER TABLE FOR THE R-SG3 DEVICE

### 24.1. R-SG3: MODBUS 4X HOLDING REGISTERS TABLE (FUNCTION CODE 3)

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>ADDRESS<br/>(4x)</b> | <b>REGISTER</b>                                  | <b>CHANNEL</b> | <b>DESCRIPTION</b>                                                                                                 | <b>W/<br/>R</b> | <b>TYPE</b>    |
|-------------------------|------------------------------------|--------------------------------------------------|----------------|--------------------------------------------------------------------------------------------------------------------|-----------------|----------------|
| <b>40001</b>            | 0                                  | MACHINE-ID                                       | -              | DEVICE IDENTIFICATION                                                                                              | RO              | UNSIGNED<br>16 |
| <b>40002</b>            | 1                                  | FIRMWARE REVISION                                | -              | FIRMWARE REVISION                                                                                                  | RO              | UNSIGNED<br>16 |
| <b>40003</b>            | 2                                  | MEASURE UNIT                                     | -              | Select the unit of measurement between:<br>0 = Kg<br>1=g<br>2=t<br>3=lb<br>4=l<br>5=N<br>6=bar<br>7=atm<br>8=other | RW              | UNSIGNED<br>16 |
| <b>40004</b>            | 3                                  | UNIPOLAR                                         | -              | 0 = compression and traction (Bipolar)<br>1 = Balance (Unipolar)                                                   | RW              | UNSIGNED<br>16 |
| <b>40005</b>            | 4                                  | ANALOG OUTPUT TYPE<br>(Only Model ZE-SG3, Z-SG3) | -              | 0 = Voltage, 1 = Current                                                                                           | RW              | UNSIGNED<br>16 |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>ADDRESS<br/>(4x)</b> | <b>REGISTER</b>                            | <b>CHANNEL</b> | <b>DESCRIPTION</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       | <b>W/<br/>R</b> | <b>TYPE</b>            |
|-------------------------|------------------------------------|--------------------------------------------|----------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----------------|------------------------|
| <b>40006</b>            | <b>5</b>                           | <b>DIGITAL IN TYPE/<br/>DIGITAL IN-OUT</b> | <b>-</b>       | <p>(MSB) DIGITAL IN TYPE:<br/>Select the input digital 1/2 type<br/>BIT[8]<br/>0 = digital input 1 acquires the tare<br/>1 = Status of the digital input 1 is shown on Modbus<br/>BIT[9]<br/>0 = digital input 2 acquires the tare<br/>1 = Status of the digital input 2 is shown on Modbus<br/>BIT[15..10]<br/>NOT USED</p> <p>(LSB) DIGITAL IN-OUT<br/>BIT[0]<br/>0 = DIDO1 configured as Input<br/>1 = DIDO1 configured as output<br/>BIT[1]<br/>0 = DIDO2 configured as Input<br/>1 = DIDO2 configured as output</p> | <b>RW</b>       | <b>UNSIGNED<br/>16</b> |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>ADDRESS<br/>(4x)</b> | <b>REGISTER</b>                | <b>CHANNEL</b> | <b>DESCRIPTION</b>                                                                                | <b>W/<br/>R</b> | <b>TYPE</b>           |
|-------------------------|------------------------------------|--------------------------------|----------------|---------------------------------------------------------------------------------------------------|-----------------|-----------------------|
| <b>40007</b>            | 6                                  | CALIBRATION MODE               | -              | Set the calibration type mode:<br>0 = Factory calibration<br>1 = Calibration with standard weight | RW              | UNSIGNED 16           |
| <b>40008</b>            | 7                                  | RESERVED                       | -              | RESERVED MSW                                                                                      | RW              | UNSIGNED 32           |
| <b>40009</b>            | 8                                  |                                |                | RESERVER LSW                                                                                      | RW              |                       |
| <b>40010</b>            | 9                                  | RESERVED                       | -              | RESERVED MSW                                                                                      | RW              | UNSIGNED 32           |
| <b>40011</b>            | 10                                 |                                |                | RESERVER LSW                                                                                      | RW              |                       |
| <b>40012</b>            | 11                                 | RESERVED                       | -              | RESERVED MSW                                                                                      | RW              | UNSIGNED 32           |
| <b>40013</b>            | 12                                 |                                |                | RESERVER LSW                                                                                      | RW              |                       |
| <b>40014</b>            | 13                                 | CELL SENSE RATIO<br>[mV/V]     | -              | Load cell sensitivity value in [mV/V] MSW                                                         | RW              | FLOATING POINT 32 BIT |
| <b>40015</b>            | 14                                 |                                |                | Load cell sensitivity value in [mV/V] LSW                                                         | RW              |                       |
| <b>40016</b>            | 15                                 | CELL FULL SCALE<br>[kg/g/t...] | -              | Full scale value of the load cell in technical units [kg/g/t...] MSW                              | RW              | FLOATING POINT 32 BIT |
| <b>40017</b>            | 16                                 |                                |                | Full scale value of the load cell                                                                 | RW              |                       |

| ADDRESS<br>(4x) | OFFSET<br>ADDRESS<br>(4x) | REGISTER                                                                         | CHANNEL | DESCRIPTION                                                                                                                    | W/<br>R | TYPE                        |
|-----------------|---------------------------|----------------------------------------------------------------------------------|---------|--------------------------------------------------------------------------------------------------------------------------------|---------|-----------------------------|
|                 |                           |                                                                                  |         | in technical<br>units [kg/g/t...]<br>LSW                                                                                       |         |                             |
| 40018           | 17                        | STANDARD WEIGHT CELL<br>FLOAT<br>[kg/g/t...]                                     | -       | Standard<br>weight value<br>in technical<br>units to use in<br>calibration<br>mode with<br>sample weight<br>[kg/g/t...]<br>MSW | RW      | FLOATING<br>POINT 32<br>BIT |
| 40019           | 18                        |                                                                                  |         | Standard<br>weight value<br>in technical<br>units to use in<br>calibration<br>mode with<br>sample weight<br>[kg/g/t...]<br>LSW | RW      |                             |
| 40020           | 19                        | THRESHOLD DO1<br>[kg/g/t...]                                                     | -       | Alarm<br>threshold<br>value in<br>technical units<br>for digital<br>output 1<br>[kg/g/t...]<br>MSW                             | RW      | FLOATING<br>POINT 32<br>BIT |
| 40021           | 20                        |                                                                                  |         | Alarm<br>threshold<br>value in<br>technical units<br>for digital<br>output 1<br>[kg/g/t...]<br>LSW                             | RW      |                             |
| 40022           | 21                        | OUTPUT WEIGHT START<br>SCALE<br>[kg/g/t...]<br>(Only for model ZE-SG3,<br>Z-SG3) | -       | Initial scale<br>value of the<br>net weight for<br>the analog<br>output<br>[kg/g/t...]<br>MSW                                  | RW      | FLOATING<br>POINT 32<br>BIT |
| 40023           | 22                        |                                                                                  |         | Initial scale<br>value of the<br>net weight for<br>the analog                                                                  | RW      |                             |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>ADDRESS<br/>(4x)</b> | <b>REGISTER</b>                                                                 | <b>CHANNEL</b> | <b>DESCRIPTION</b>                                                                                    | <b>W/<br/>R</b> | <b>TYPE</b>                 |
|-------------------------|------------------------------------|---------------------------------------------------------------------------------|----------------|-------------------------------------------------------------------------------------------------------|-----------------|-----------------------------|
|                         |                                    |                                                                                 |                | output<br>[kg/g/t...] LSW                                                                             |                 |                             |
| <b>40024</b>            | 23                                 | OUTPUT WEIGHT STOP<br>SCALE<br>[kg/g/t...]<br>(Only for model ZE-SG3,<br>Z-SG3) | -              | Full scale value<br>of the net<br>weight for the<br>analog output<br>[kg/g/t...] MSW                  | RW              | FLOATING<br>POINT 32<br>BIT |
| <b>40025</b>            | 24                                 |                                                                                 |                | Full scale value<br>of the net<br>weight for the<br>analog output<br>[kg/g/t...] LSW                  | RW              |                             |
| <b>40026</b>            | 25                                 | OUTPUT STOP SCALE<br>[V/mA]<br>(Only for model ZE-SG3,<br>Z-SG3)                | -              | Full scale value<br>of the analog<br>output in<br>[V/mA] MSW                                          | RW              | FLOATING<br>POINT 32<br>BIT |
| <b>40027</b>            | 26                                 |                                                                                 |                | Full scale value<br>of the analog<br>output in<br>[V/mA] LSW                                          | RW              |                             |
| <b>40028</b>            | 27                                 | OUTPUT START SCALE<br>[V/mA]<br>(Only for model ZE-SG3,<br>Z-SG3)               | -              | Initial scale<br>value of the<br>analog output<br>in [V/mA] MSW                                       | RW              | FLOATING<br>POINT 32<br>BIT |
| <b>40029</b>            | 28                                 |                                                                                 |                | Initial scale<br>value of the<br>analog output<br>in [V/mA] LSW                                       | RW              |                             |
| <b>40030</b>            | 29                                 | DELTA WEIGHT<br>[kg/g/t...]                                                     | -              | Delta weight<br>value in<br>technical units<br>for stable<br>weighing<br>condition<br>[kg/g/t...] MSW | RW              | FLOATING<br>POINT 32<br>BIT |
| <b>40031</b>            | 30                                 |                                                                                 |                | Delta weight<br>value in<br>technical units<br>for stable<br>weighing                                 | RW              |                             |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>ADDRESS<br/>(4x)</b> | <b>REGISTER</b>         | <b>CHANNEL</b> | <b>DESCRIPTION</b>                                                                                                                                                                                                                                                                                                                                                                                      | <b>W/<br/>R</b> | <b>TYPE</b>        |
|-------------------------|------------------------------------|-------------------------|----------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----------------|--------------------|
|                         |                                    |                         |                | condition<br>[kg/g/t...] LSW                                                                                                                                                                                                                                                                                                                                                                            |                 |                    |
| <b>40032</b>            | 31                                 | DELTA TIME<br>[*100 ms] | -              | Value of the<br>delta time in<br>100ms<br>quantities for<br>the stable<br>weighing<br>condition                                                                                                                                                                                                                                                                                                         | RW              | UNSIGNED<br>16 BIT |
| <b>40033</b>            | 32                                 | DOUT MODE               | 1-2            | BIT[0]<br>0 = DOUT1<br>NORMALLY<br>OPEN<br>1 = DOUT1<br>NORMALLY<br>CLOSE<br><br>BIT[1]<br>0 = DOUT2<br>NORMALLY<br>OPEN<br>1 = DOUT2<br>NORMALLY<br>CLOSE<br>BIT[2..7] NOT<br>USED<br><br>BIT[8..11]<br>0 = DOUT1<br>MODE FULL<br>SCALE<br>1 = DOUT1<br>MODE<br>OVERTHRESH<br>OLD<br>2 = DOUT1<br>STABLE<br>WEIGHT<br>3 = DOUT1<br>FROM<br>MODBUS<br>4 = DOUT1<br>OVERTHRESH<br>OLD WITH<br>HYSTERESIS | RW              | UNSIGNED<br>16 BIT |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>ADDRESS<br/>(4x)</b> | <b>REGISTER</b>      | <b>CHANNEL</b> | <b>DESCRIPTION</b>                                                                                                                                                                                                             | <b>W/<br/>R</b> | <b>TYPE</b>        |
|-------------------------|------------------------------------|----------------------|----------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----------------|--------------------|
|                         |                                    |                      |                | BIT[12..15]<br>0 = DOUT2<br>MODE FULL<br>SCALE<br>1 = DOUT2<br>MODE<br>OVERTHRESH<br>OLD<br>2 = DOUT2<br>STABLE<br>WEIGHT<br>3 = DOUT2<br>FROM<br>MODBUS<br>4 = DOUT2<br>OVERTHRESH<br>OLD WITH<br>HYSTERESIS                  |                 |                    |
| <b>40034</b>            | 33                                 | ADVANCED ADC SPEED   | -              | Configure the<br>sampling rate<br>of the ADC.<br>It is active only<br>if you set the<br>filtering at<br>level 7<br>(advanced).<br>0 = 960 Hz<br>1 = 300 Hz<br>2 = 150 Hz<br>3 = 100 Hz<br>4 = 60 Hz<br>5 = 12 Hz<br>6 = 4.7 Hz | RW              | UNSIGNED<br>16 BIT |
| <b>40035</b>            | 34                                 | AUTOMATIC TARE RESET | -              | 0 = Disable the<br>tare tracker<br>If >= 1 it is the<br>value of ADC<br>points within<br>which the tare<br>automatically<br>will be reset .<br>If after 5<br>seconds of<br>stable<br>weighing                                  | RW              | UNSIGNED<br>32     |



| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>ADDRESS<br/>(4x)</b> | <b>REGISTER</b>                          | <b>CHANNEL</b> | <b>DESCRIPTION</b>                                                                                                                               | <b>W/<br/>R</b> | <b>TYPE</b>           |
|-------------------------|------------------------------------|------------------------------------------|----------------|--------------------------------------------------------------------------------------------------------------------------------------------------|-----------------|-----------------------|
|                         |                                    |                                          |                | condition the ADC value of the net weight deviates by less than this value then a new tare is acquired<br>ADC POINTS MSW                         |                 |                       |
| <b>40036</b>            | 35                                 |                                          | -              | ADC POINTS LSW                                                                                                                                   | RW              |                       |
| <b>40037</b>            | 36                                 | THRESHOLD HYSTERESIS DO 1<br>[kg/g/t...] | -              | Hysteresis value for digital output 1 threshold MSW                                                                                              | RW              | FLOATING POINT 32 BIT |
| <b>40038</b>            | 37                                 |                                          | -              | Hysteresis value for digital output 1 threshold LSW                                                                                              | RW              |                       |
| <b>40039</b>            | 38                                 | ADVANCED DENOISE FILTER VARIATION        | -              | It represents the change in ADC points due to noise alone.<br>It is active only if you set the filtering at level 7 (advanced).<br>ADC VALUE MSW | RW              | FLOATING POINT 32 BIT |
| <b>40040</b>            | 39                                 |                                          | -              | ADC VALUE LSW                                                                                                                                    | RW              |                       |
| <b>40041</b>            | 40                                 | ADVANCED DENOISE FILTER RESPONSE         | -              | Represents a parameter related to the response speed of the filter, it can vary from 0.001                                                       | RW              | FLOATING POINT 32 BIT |

| ADDRESS<br>(4x) | OFFSET<br>ADDRESS<br>(4x) | REGISTER             | CHANNEL | DESCRIPTION                                                                                                                                                                                                                                                                                                                                                                | W/<br>R | TYPE            |
|-----------------|---------------------------|----------------------|---------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------|-----------------|
|                 |                           |                      |         | (Slowest Response) to 1 (Fast Response). It is active only if you set the filtering at level 7 (advanced). RESPONSE SPEED MSW                                                                                                                                                                                                                                              |         |                 |
| <b>40042</b>    | 41                        |                      | -       | RESPONSE SPEED LSW                                                                                                                                                                                                                                                                                                                                                         | RW      |                 |
| <b>40043</b>    | 42                        | DENOISE FILTER VALUE | -       | <p>It is possible to configure a pre-established filter level. The higher the filter level is, plus the weight measurement will be stable but slow. A value other than 7 will override the advanced parameters. In the "Advanced" mode it will be possible to act on the individual parameters of the ADVANCED NOISE FILTER VARIATION, ADVANCED NOISE FILTER RESPONSE,</p> | RW      | UNSIGNED 16 BIT |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>ADDRESS<br/>(4x)</b> | <b>REGISTER</b>    | <b>CHANNEL</b> | <b>DESCRIPTION</b>                                                                                                                                                                                                                                                                                                                     | <b>W/<br/>R</b> | <b>TYPE</b>        |
|-------------------------|------------------------------------|--------------------|----------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----------------|--------------------|
|                         |                                    |                    |                | ADVANCED<br>ADC SPEED<br><br>0 = FILTER<br>RESPONSE<br>TIME 2 ms<br>1 = FILTER<br>RESPONSE<br>TIME 6,7 ms<br>2 = FILTER<br>RESPONSE<br>TIME 13 ms<br>3 = FILTER<br>RESPONSE<br>TIME 30 ms<br>4 = FILTER<br>RESPONSE<br>TIME 50 ms<br>5 = FILTER<br>RESPONSE<br>TIME 250 ms<br>6 = FILTER<br>RESPONSE<br>TIME 850 ms<br>7 =<br>ADVANCED |                 |                    |
| <b>40044</b>            | 43                                 | RESOLUTION<br>MODE | -              | Select the type<br>of resolution<br>to be used in<br>the Weight<br>measurement:<br>0 =<br>Automatic<br>Resolution<br>(calculated on<br>the basis of<br>the full scale<br>to obtain<br>about 20000<br>points)<br>1 = Manual<br>resolution<br>(Taken from<br>MANUAL<br>RESOLUTION<br>register)                                           | RW              | UNSIGNED<br>16 BIT |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>ADDRESS<br/>(4x)</b> | <b>REGISTER</b>               | <b>CHANNEL</b> | <b>DESCRIPTION</b>                                                                                       | <b>W/<br/>R</b> | <b>TYPE</b>           |
|-------------------------|------------------------------------|-------------------------------|----------------|----------------------------------------------------------------------------------------------------------|-----------------|-----------------------|
|                         |                                    |                               |                | 2 = Max resolution (Full 24 bits resolution)                                                             |                 |                       |
| <b>40045</b>            | 44                                 | DENOISE FILTER ENABLE         | -              | 0 = Noise filter disabled (advanced mode enabled) 1 = Noise filter enabled                               | RW              | UNSIGNED 16 BIT       |
| <b>40046</b>            | 45                                 | MANUAL RESOLUTION [kg/g/t...] | -              | Sets the manual resolution with which the weight measure is displayed in technical units [kg/g/t...] MSW | RW              | FLOATING POINT 32 BIT |
| <b>40047</b>            | 46                                 |                               | -              | Sets the manual resolution with which the weight measure is displayed in technical units [kg/g/t...] LSW | RW              |                       |
| <b>40048</b>            | 47                                 | ONE PIECE WEIGHT [kg/g/t...]  | -              | Sets the weight of a single piece (used for pieces counter) MSW                                          | RW              | FLOATING POINT 32 BIT |
| <b>40049</b>            | 48                                 |                               | -              | Sets the weight of a single piece (used for pieces counter) LSW                                          | RW              |                       |
| <b>40050</b>            | 49                                 | THRESHOLD DO2 [kg/g/t...]     | -              | Alarm threshold value in                                                                                 | RW              | FLOATING POINT 32 BIT |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>ADDRESS<br/>(4x)</b> | <b>REGISTER</b>                             | <b>CHANNEL</b> | <b>DESCRIPTION</b>                                                                              | <b>W/<br/>R</b> | <b>TYPE</b>                 |
|-------------------------|------------------------------------|---------------------------------------------|----------------|-------------------------------------------------------------------------------------------------|-----------------|-----------------------------|
|                         |                                    |                                             |                | technical units<br>for digital<br>output 2<br>[kg/g/t...] MSW                                   |                 |                             |
| <b>40051</b>            | 50                                 |                                             | -              | Alarm<br>threshold<br>value in<br>technical units<br>for digital<br>output 2<br>[kg/g/t...] LSW | RW              |                             |
| <b>40052</b>            | 51                                 | THRESHOLD HYSTERESIS<br>DO 2<br>[kg/g/t...] | -              | Hysteresis<br>value for<br>digital output<br>1 threshold<br>MSW                                 | RW              | FLOATING<br>POINT 32<br>BIT |
| <b>40053</b>            | 52                                 |                                             | -              | Hysteresis<br>value for<br>digital output<br>1 threshold<br>LSW                                 | RW              |                             |
| <b>40061</b>            | 60                                 | RESERVED                                    | -              | Reserved                                                                                        | RO              | FLOATING<br>POINT 32<br>BIT |
| <b>40062</b>            | 61                                 |                                             |                | Reserved                                                                                        | RO              |                             |
| <b>40063</b>            | 62                                 | 16 BIT ADC FILTERED                         | -              | ADC<br>Converted in<br>16 Bit filtered<br>value                                                 | RO              | UNSIGNED<br>16 BIT          |
| <b>40064</b>            | 63                                 | NET WEIGHT VALUE<br>[Kg/g/t..]              | -              | Net weight in<br>technical units<br>MSW                                                         | RO              | FLOATING<br>POINT 32<br>BIT |
| <b>40065</b>            | 64                                 |                                             |                | Net weight in<br>technical units<br>LSW                                                         | RO              |                             |
| <b>40066</b>            | 65                                 | GROSS WEIGHT VALUE<br>[Kg/g/t..]            | -              | Gross weight<br>in technical<br>units MSW                                                       | RO              | FLOATING<br>POINT 32<br>BIT |
| <b>40067</b>            | 66                                 |                                             |                | Gross weight<br>in technical<br>units LSW                                                       | RO              |                             |
| <b>40068</b>            | 67                                 | TARE WEIGHT VALUE<br>[kg/g/t...]            | -              | Tare weight in<br>technical units<br>MSW                                                        | RO              | FLOATING<br>POINT 32<br>BIT |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>ADDRESS<br/>(4x)</b> | <b>REGISTER</b>                                       | <b>CHANNEL</b> | <b>DESCRIPTION</b>                                                     | <b>W/<br/>R</b> | <b>TYPE</b>                          |
|-------------------------|------------------------------------|-------------------------------------------------------|----------------|------------------------------------------------------------------------|-----------------|--------------------------------------|
| <b>40069</b>            | 68                                 |                                                       | -              | Tare weight in technical units LSW                                     | RO              |                                      |
| <b>40070</b>            | 69                                 | <b>INTEGER NET<br/>WEIGHT VALUE<br/>[kg/g/t...]</b>   | -              | Net weight in technical units MSW                                      | RO              | <b>SIGNED 32<br/>BIT</b>             |
| <b>40071</b>            | 70                                 |                                                       | -              | Net weight in technical units LSW                                      | RO              |                                      |
| <b>40072</b>            | 71                                 | <b>INTEGER GROSS<br/>WEIGHT VALUE<br/>[kg/g/t...]</b> | -              | Gross weight in technical units MSW                                    | RO              | <b>SIGNED 32<br/>BIT</b>             |
| <b>40073</b>            | 72                                 |                                                       | -              | Gross weight in technical units LSW                                    | RO              |                                      |
| <b>40074</b>            | 73                                 | <b>INTEGER TARE<br/>WEIGHT VALUE [kg/g/t...]</b>      | -              | Tare weight in technical units MSW                                     | RO              | <b>SIGNED 32<br/>BIT</b>             |
| <b>40075</b>            | 74                                 |                                                       | -              | Tare weight in technical units LSW                                     | RO              |                                      |
| <b>40076</b>            | 75                                 | <b>FACTORY<br/>MANUAL TARE<br/>[kg/g/t...]</b>        | -              | Value to use for the manual tare in the factory mode calibration (MSW) | RW              | <b>FLOATING<br/>POINT 32<br/>BIT</b> |
| <b>40077</b>            | 76                                 |                                                       | -              | Value to use for the manual tare in the factory mode calibration (LSW) | RW              |                                      |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>ADDRESS<br/>(4x)</b> | <b>REGISTER</b> | <b>CHANNEL</b> | <b>DESCRIPTION</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  | <b>W/<br/>R</b> | <b>TYPE</b>                |
|-------------------------|------------------------------------|-----------------|----------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----------------|----------------------------|
| <b>40078</b>            | <b>77</b>                          | <b>STATUS</b>   | <b>-</b>       | Status register:<br>BIT 0 LSBIT (RO)<br>Bit 0 = 1 THRESHOLD AND STABLE WEIGHT for DIDO 1<br>BIT 1 (RO)<br>Bit 1 = 1 FULL SCALE CELL<br>BIT 2 (RO)<br>Bit 2 = 1 NET WEIGHT < 0<br>BIT 3 (RO)<br>Bit 3 = 1 THRESHOLD AND STABLE WEIGHT for DIDO 2<br>BIT 4 (RO)<br>Bit 4 = 1 Stable weight<br>BIT 5-6 (R/W)<br>Only if the output mode commandable from modbus has been chosen:<br>Bit 5 = 1 digital output 2 on<br>Bit 5 = 0 digital output 2 off<br>Bit 6 = 1 digital output 1 on<br>Bit 6 = 0 digital output 1 off<br>BIT 7 (RO)<br>Bit 7 = 1 Threshold with hysteresis for DIDO 1<br>BIT 8 (RO)<br>Bit 8 = 1 tare | <b>RW</b>       | <b>UNSIGNED<br/>16 BIT</b> |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>ADDRESS<br/>(4x)</b> | <b>REGISTER</b>  | <b>CHANNEL</b> | <b>DESCRIPTION</b>                                                                                                                                                          | <b>W/<br/>R</b> | <b>TYPE</b>        |
|-------------------------|------------------------------------|------------------|----------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----------------|--------------------|
|                         |                                    |                  |                | tracker (if enabled)<br>BIT 9 (RO)<br>Bit 9 = 1<br>Threshold with hysteresis for DIDO 2<br>BIT 10..15 Not used                                                              |                 |                    |
| <b>40079</b>            | 78                                 | PASSWORD         | -              | Reserved                                                                                                                                                                    | RW              | UNSIGNED<br>16 BIT |
| <b>40080</b>            | 79                                 | COMMAND REGISTER | -              | Command register once the command has been executed, the register returns to the value 0<br><br>43948 (decimal)<br>Reboot the device<br><br>49594 (decimal)<br>Acquires the | RW              | UNSIGNED<br>16 BIT |



| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>ADDRESS<br/>(4x)</b> | <b>REGISTER</b> | <b>CHANNEL</b> | <b>DESCRIPTION</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     | <b>W/<br/>R</b> | <b>TYPE</b> |
|-------------------------|------------------------------------|-----------------|----------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----------------|-------------|
|                         |                                    |                 |                | <p>tare in RAM<br/>(at reboot is lost)</p> <p>49914<br/>(decimal)<br/>Acquires the tare in Flash for the calibration procedure in both operating modes (factory calibration and with sample weight)</p> <p>50700<br/>(decimal)<br/>Acquires the sample weight value in Flash for calibration with standard weight</p> <p>50773<br/>(decimal)<br/>Acquires the tare value from the register<br/>MANUAL TARE<br/>(only for the factory calibration mode)</p> <p>49151<br/>(decimal)<br/>Reset the maximum net weight</p> |                 |             |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>ADDRESS<br/>(4x)</b> | <b>REGISTER</b>               | <b>CHANNEL</b> | <b>DESCRIPTION</b>                                                                | <b>W/<br/>R</b> | <b>TYPE</b>                 |
|-------------------------|------------------------------------|-------------------------------|----------------|-----------------------------------------------------------------------------------|-----------------|-----------------------------|
|                         |                                    |                               |                | 45056<br>(decimal)<br>Reset the<br>register with<br>the minimum<br>net weight     |                 |                             |
| <b>40081</b>            | 80                                 | PIECES NR                     | -              | PIECES<br>COUNTER<br>VALUE                                                        | RO              | UNSIGNED<br>16 BIT          |
| <b>40082</b>            | 81                                 | MAX NET<br>WEIGHT [Kg/g/t...] | -              | Maximum<br>weight value<br>net in<br>technical units<br>from last<br>reboot [MSW] | RO              | FLOATING<br>POINT 32<br>BIT |
| <b>40083</b>            | 82                                 |                               | -              | Maximum<br>weight value<br>net in<br>technical units<br>from last<br>reboot [LSW] | RO              |                             |
| <b>40084</b>            | 83                                 | MIN NET<br>WEIGHT [Kg/g/t...] | -              | Minimum<br>weight value<br>net in<br>technical units<br>from last<br>reboot [MSW] | RO              | FLOATING<br>POINT 32<br>BIT |
| <b>40085</b>            | 84                                 |                               | -              | Minimum<br>weight value<br>net in<br>technical units<br>from last<br>reboot [LSW] | RO              |                             |
| <b>40086</b>            | 85                                 | RESERVED                      | -              | Reserved                                                                          | RO              | UNSIGNED<br>32 BIT          |
| <b>40087</b>            | 86                                 |                               | -              | Reserved                                                                          | RO              |                             |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>ADDRESS<br/>(4x)</b> | <b>REGISTER</b>            | <b>CHANNEL</b> | <b>DESCRIPTION</b>                  | <b>W/<br/>R</b> | <b>TYPE</b>        |
|-------------------------|------------------------------------|----------------------------|----------------|-------------------------------------|-----------------|--------------------|
| <b>40088</b>            | 87                                 | RESERVED                   | -              | Reserved                            | RO              | UNSIGNED<br>32 BIT |
| <b>40089</b>            | 88                                 |                            | -              | Reserved                            | RO              |                    |
| <b>40090</b>            | 89                                 | RESERVED                   | -              | Reserved                            | RO              | UNSIGNED<br>32 BIT |
| <b>40091</b>            | 90                                 |                            | -              | Reserved                            | RO              |                    |
| <b>40092</b>            | 91                                 | ADC RAW 24 BIT             | -              | ADC 24 bit<br>value not<br>filtered | RO              | UNSIGNED<br>32 BIT |
| <b>40093</b>            | 92                                 |                            | -              | ADC 24 bit<br>value not<br>filtered | RO              |                    |
| <b>40094</b>            | 93                                 | ADC RAW 24 BIT<br>FILTERED | -              | ADC 24 bit<br>value filtered        | RO              | UNSIGNED<br>32 BIT |
| <b>40095</b>            | 94                                 |                            | -              | ADC 24 bit<br>value filtered        | RO              |                    |

## 25. MODBUS REGISTER TABLE FOR THE R-4AO-8DIDO PRODUCT

### 25.1. R-4AO-8DIDO: MODBUS 4X HOLDING REGISTERS TABLE (FUNCTION CODE 3)

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>(4x)</b> | <b>REGISTER</b>                | <b>CHANNEL</b> | <b>DESCRIPTION</b>    | <b>W/R</b> | <b>TYPE</b>        |
|-------------------------|------------------------|--------------------------------|----------------|-----------------------|------------|--------------------|
| <b>40001</b>            | 0                      | MACHINE-ID                     | -              | Device identification | RO         | UNSIGNED<br>16 BIT |
| <b>40002</b>            | 1                      | FW REVISION<br>(Maior/Minor)   | -              | Fw Revision           | RO         | UNSIGNED<br>16 BIT |
| <b>40003</b>            | 2                      | FW REVISION<br>(Fix/Build)     | -              | Fw Revision           | RO         | UNSIGNED<br>16 BIT |
| <b>40004</b>            | 3                      | FW CODE                        | -              | Fw Code               | RO         | UNSIGNED<br>16 BIT |
| <b>40005</b>            | 4                      | BOARD-ID                       | -              | Hw Revision           | RO         | UNSIGNED<br>16 BIT |
| <b>40006</b>            | 5                      | MODBUS ID                      | -              | -                     | RO         | UNSIGNED<br>16 BIT |
| <b>40007</b>            | 6                      | BOOT REVISION<br>(Maior/Minor) | -              | Bootloader Revision   | RO         | UNSIGNED<br>16 BIT |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>(4x)</b> | <b>REGISTER</b>              | <b>CHANNEL</b> | <b>DESCRIPTION</b>                                                                                                                                                                                                                                                                                                                                             | <b>W/R</b> | <b>TYPE</b>        |
|-------------------------|------------------------|------------------------------|----------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|------------|--------------------|
| <b>40008</b>            | 7                      | BOOT REVISION<br>(Fix/Build) | -              | Bootloader Revision                                                                                                                                                                                                                                                                                                                                            | RO         | UNSIGNED<br>16 BIT |
| <b>40009</b>            | 8                      | BOOTLOADER<br>CODE           | -              | Bootloader Code                                                                                                                                                                                                                                                                                                                                                | RO         | UNSIGNED<br>16 BIT |
| <b>40010</b>            | 9                      | RESERVED                     | -              | -                                                                                                                                                                                                                                                                                                                                                              | RO         | UNSIGNED<br>16 BIT |
| <b>40011</b>            | 10                     | RESERVED                     | -              | -                                                                                                                                                                                                                                                                                                                                                              | RO         | UNSIGNED<br>16 BIT |
| <b>40012</b>            | 11                     | RESERVED                     | -              | -                                                                                                                                                                                                                                                                                                                                                              | RO         | UNSIGNED<br>16 BIT |
| <b>40013</b>            | 12                     | COMMAND_AUX<br>_3H           | -              | Aux Command Register                                                                                                                                                                                                                                                                                                                                           | RW         | UNSIGNED<br>16 BIT |
| <b>40014</b>            | 13                     | COMMAND_AUX<br>_3L           | -              | Aux Command Register                                                                                                                                                                                                                                                                                                                                           | RW         | UNSIGNED<br>16 BIT |
| <b>40015</b>            | 14                     | COMMAND_AUX<br>2             | -              | Aux Command Register                                                                                                                                                                                                                                                                                                                                           | RW         | UNSIGNED<br>16 BIT |
| <b>40016</b>            | 15                     | COMMAND_AUX<br>1             | -              | Aux Command Register                                                                                                                                                                                                                                                                                                                                           | RW         | UNSIGNED<br>16 BIT |
| <b>40017</b>            | 16                     | COMMAND                      | -              | Aux Command Register<br><br>0xC1A0 = reset device<br>0x0100 = set factory<br>default<br>0x0101 = clear al counters                                                                                                                                                                                                                                             | RW         | UNSIGNED<br>16 BIT |
| <b>40018</b>            | 17                     | STATUS                       | -              | Device Status                                                                                                                                                                                                                                                                                                                                                  | R          | UNSIGNED<br>16 BIT |
| <b>40019</b>            | 18                     | RESERVED                     | -              | -                                                                                                                                                                                                                                                                                                                                                              | RW         | UNSIGNED<br>16 BIT |
| <b>40020</b>            | 19                     | RESERVED                     | -              | -                                                                                                                                                                                                                                                                                                                                                              | RW         | UNSIGNED<br>16 BIT |
| <b>40021</b>            | 20                     | ANALOG STATUS<br>BIT         | -              | bit 0 : under-scale AO1<br>bit 1 : under-scale AO2<br>bit 2 : under-scale AO3<br>bit 3 : under-scale AO4<br>bit 4 : over-scale AO1<br>bit 5 : over-scale AO2<br>bit 6 : over-scale AO3<br>bit 7 : over-scale AO4<br>bit 8 : fail watchdog AO1<br>bit 9 : fail watchdog AO2<br>bit 10 : fail watchdog AO3<br>bit 11 : fail watchdog AO4<br>bit 12..15: not used | R          | UNSIGNED<br>16 BIT |
| <b>40022</b>            | 21                     | DIGITAL I/O                  | 8..1           | Digital IO Value [Channel<br>8...1]                                                                                                                                                                                                                                                                                                                            | RW         | UNSIGNED<br>16 BIT |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>(4x)</b> | <b>REGISTER</b> | <b>CHANNEL</b> | <b>DESCRIPTION</b> | <b>W/R</b> | <b>TYPE</b>                 |
|-------------------------|------------------------|-----------------|----------------|--------------------|------------|-----------------------------|
| <b>40023</b>            | 22                     | RESERVED        | -              | -                  | RW         | UNSIGNED<br>16 BIT          |
| <b>40024</b>            | 23                     | COUNTER         | 1              | MSW                | RW         | UNSIGNED<br>32 BIT          |
| <b>40025</b>            | 24                     |                 |                | LSW                | RW         |                             |
| <b>40026</b>            | 25                     | COUNTER         | 2              | MSW                | RW         | UNSIGNED<br>32 BIT          |
| <b>40027</b>            | 26                     |                 |                | LSW                | RW         |                             |
| <b>40028</b>            | 27                     | COUNTER         | 3              | MSW                | RW         | UNSIGNED<br>32 BIT          |
| <b>40029</b>            | 28                     |                 |                | LSW                | RW         |                             |
| <b>40030</b>            | 29                     | COUNTER         | 4              | MSW                | RW         | UNSIGNED<br>32 BIT          |
| <b>40031</b>            | 30                     |                 |                | LSW                | RW         |                             |
| <b>40032</b>            | 31                     | COUNTER         | 5              | MSW                | RW         | UNSIGNED<br>32 BIT          |
| <b>40033</b>            | 32                     |                 |                | LSW                | RW         |                             |
| <b>40034</b>            | 33                     | COUNTER         | 6              | MSW                | RW         | UNSIGNED<br>32 BIT          |
| <b>40035</b>            | 34                     |                 |                | LSW                | RW         |                             |
| <b>40036</b>            | 35                     | COUNTER         | 7              | MSW                | RW         | UNSIGNED<br>32 BIT          |
| <b>40037</b>            | 36                     |                 |                | LSW                | RW         |                             |
| <b>40038</b>            | 37                     | COUNTER         | 8              | MSW                | RW         | UNSIGNED<br>32 BIT          |
| <b>40039</b>            | 38                     |                 |                | LSW                | RW         |                             |
| <b>40040</b>            | 39                     | PERIOD [s]      | 1              | MSW                | R          | FLOATING<br>POINT<br>32 BIT |
| <b>40041</b>            | 40                     |                 |                | LSW                | R          |                             |
| <b>40042</b>            | 41                     | PERIOD [s]      | 2              | MSW                | R          | FLOATING<br>POINT<br>32 BIT |
| <b>40043</b>            | 42                     |                 |                | LSW                | R          |                             |
| <b>40044</b>            | 43                     | PERIOD [s]      | 3              | MSW                | R          | FLOATING<br>POINT<br>32 BIT |
| <b>40045</b>            | 44                     |                 |                | LSW                | R          |                             |
| <b>40046</b>            | 45                     | PERIOD [s]      | 4              | MSW                | R          | FLOATING<br>POINT<br>32 BIT |
| <b>40047</b>            | 46                     |                 |                | LSW                | R          |                             |
| <b>40048</b>            | 47                     | PERIOD [s]      | 5              | MSW                | R          | FLOATING<br>POINT<br>32 BIT |
| <b>40049</b>            | 48                     |                 |                | LSW                | R          |                             |
| <b>40050</b>            | 49                     | PERIOD [s]      | 6              | MSW                | R          | FLOATING<br>POINT<br>32 BIT |
| <b>40051</b>            | 50                     |                 |                | LSW                | R          |                             |
| <b>40052</b>            | 51                     | PERIOD [s]      | 7              | MSW                | R          | FLOATING<br>POINT<br>32 BIT |
| <b>40053</b>            | 52                     |                 |                | LSW                | R          |                             |
| <b>40054</b>            | 53                     | PERIOD [s]      | 8              | MSW                | R          | FLOATING<br>POINT<br>32 BIT |
| <b>40055</b>            | 54                     |                 |                | LSW                | R          |                             |
| <b>40056</b>            | 55                     | FREQUENCY [Hz]  | 1              | MSW                | R          | FLOATING<br>POINT<br>32 BIT |
| <b>40057</b>            | 56                     |                 |                | LSW                | R          |                             |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>(4x)</b> | <b>REGISTER</b>                                | <b>CHANNEL</b> | <b>DESCRIPTION</b>                                            | <b>W/R</b> | <b>TYPE</b>                 |
|-------------------------|------------------------|------------------------------------------------|----------------|---------------------------------------------------------------|------------|-----------------------------|
| <b>40058</b>            | 57                     | FREQUENCY [Hz]                                 | 2              | MSW                                                           | R          | FLOATING<br>POINT<br>32 BIT |
| <b>40059</b>            | 58                     |                                                |                | LSW                                                           | R          |                             |
| <b>40060</b>            | 59                     | FREQUENCY [Hz]                                 | 3              | MSW                                                           | R          | FLOATING<br>POINT<br>32 BIT |
| <b>40061</b>            | 60                     |                                                |                | LSW                                                           | R          |                             |
| <b>40062</b>            | 61                     | FREQUENCY [Hz]                                 | 4              | MSW                                                           | R          | FLOATING<br>POINT<br>32 BIT |
| <b>40063</b>            | 62                     |                                                |                | LSW                                                           | R          |                             |
| <b>40064</b>            | 63                     | FREQUENCY [Hz]                                 | 5              | MSW                                                           | R          | FLOATING<br>POINT<br>32 BIT |
| <b>40065</b>            | 64                     |                                                |                | LSW                                                           | R          |                             |
| <b>40066</b>            | 65                     | FREQUENCY [Hz]                                 | 6              | MSW                                                           | R          | FLOATING<br>POINT<br>32 BIT |
| <b>40067</b>            | 66                     |                                                |                | LSW                                                           | R          |                             |
| <b>40068</b>            | 67                     | FREQUENCY [Hz]                                 | 7              | MSW                                                           | R          | FLOATING<br>POINT<br>32 BIT |
| <b>40069</b>            | 68                     |                                                |                | LSW                                                           | R          |                             |
| <b>40070</b>            | 69                     | FREQUENCY [Hz]                                 | 8              | MSW                                                           | R          | FLOATING<br>POINT<br>32 BIT |
| <b>40071</b>            | 70                     |                                                |                | LSW                                                           | R          |                             |
| <b>40072</b>            | 71                     | ANALOG OUTPUT<br>ENG (SCALED)<br>VALUE FLOAT   | 1              | ENG. SCALED FLOAT<br>ANALOG OUTPUT VALUE<br>(default mV/uA)   | R/W        | FLOATING<br>POINT<br>32 BIT |
| <b>40073</b>            | 72                     |                                                |                |                                                               |            |                             |
| <b>40074</b>            | 73                     | ANALOG OUTPUT<br>ENG (SCALED)<br>VALUE FLOAT   | 2              | ENG. SCALED FLOAT<br>ANALOG OUTPUT VALUE<br>(default mV/uA)   | R/W        | FLOATING<br>POINT<br>32 BIT |
| <b>40075</b>            | 74                     |                                                |                |                                                               |            |                             |
| <b>40076</b>            | 75                     | ANALOG OUTPUT<br>ENG (SCALED)<br>VALUE FLOAT   | 3              | ENG. SCALED FLOAT<br>ANALOG OUTPUT VALUE<br>(default mV/uA)   | R/W        | FLOATING<br>POINT<br>32 BIT |
| <b>40077</b>            | 76                     |                                                |                |                                                               |            |                             |
| <b>40078</b>            | 77                     | ANALOG OUTPUT<br>ENG (SCALED)<br>VALUE FLOAT   | 4              | ENG. SCALED FLOAT<br>ANALOG OUTPUT VALUE<br>(default mV/uA)   | R/W        | FLOATING<br>POINT<br>32 BIT |
| <b>40079</b>            | 78                     |                                                |                |                                                               |            |                             |
| <b>40080</b>            | 79                     | ANALOG OUTPUT<br>ENG (SCALED)<br>VALUE INTEGER | 1              | ENG. SCALED INTEGER<br>ANALOG OUTPUT VALUE<br>(default mV/uA) | R/W        | SIGNED<br>INTEGER 16<br>BIT |
| <b>40081</b>            | 80                     | ANALOG OUTPUT<br>ENG (SCALED)<br>VALUE INTEGER | 2              | ENG. SCALED INTEGER<br>ANALOG OUTPUT VALUE<br>(default mV/uA) | R/W        | SIGNED<br>INTEGER 16<br>BIT |
| <b>40082</b>            | 81                     | ANALOG OUTPUT<br>ENG (SCALED)<br>VALUE INTEGER | 3              | ENG. SCALED INTEGER<br>ANALOG OUTPUT VALUE<br>(default mV/uA) | R/W        | SIGNED<br>INTEGER 16<br>BIT |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>(4x)</b> | <b>REGISTER</b>                                | <b>CHANNEL</b> | <b>DESCRIPTION</b>                                            | <b>W/R</b> | <b>TYPE</b>                 |
|-------------------------|------------------------|------------------------------------------------|----------------|---------------------------------------------------------------|------------|-----------------------------|
| <b>40083</b>            | <b>82</b>              | ANALOG OUTPUT<br>ENG (SCALED)<br>VALUE INTEGER | <b>4</b>       | ENG. SCALED INTEGER<br>ANALOG OUTPUT VALUE<br>(default mV/uA) | R/W        | SIGNED<br>INTEGER 16<br>BIT |

## 25.2. R-4AO-8DIDO: TABLE OF MODBUS REGISTERS 0x COIL STATUS (FUNCTION CODE 1)

| <b>ADDRESS (0x)</b> | <b>OFFSET ADDRESS<br/>(0x)</b> | <b>REGISTER</b>         | <b>CHANNE<br/>L</b> | <b>DESCRIPTION</b>      | <b>W/<br/>R</b> | <b>TYPE</b> |
|---------------------|--------------------------------|-------------------------|---------------------|-------------------------|-----------------|-------------|
| <b>1</b>            | <b>0</b>                       | DIGITAL<br>INPUT/OUTPUT | <b>1</b>            | DIGITAL<br>INPUT/OUTPUT | RW              | BIT         |
| <b>2</b>            | <b>1</b>                       | DIGITAL<br>INPUT/OUTPUT | <b>2</b>            | DIGITAL<br>INPUT/OUTPUT | RW              | BIT         |
| <b>3</b>            | <b>2</b>                       | DIGITAL<br>INPUT/OUTPUT | <b>3</b>            | DIGITAL<br>INPUT/OUTPUT | RW              | BIT         |
| <b>4</b>            | <b>3</b>                       | DIGITAL<br>INPUT/OUTPUT | <b>4</b>            | DIGITAL<br>INPUT/OUTPUT | RW              | BIT         |
| <b>5</b>            | <b>4</b>                       | DIGITAL<br>INPUT/OUTPUT | <b>5</b>            | DIGITAL<br>INPUT/OUTPUT | RW              | BIT         |
| <b>6</b>            | <b>5</b>                       | DIGITAL<br>INPUT/OUTPUT | <b>6</b>            | DIGITAL<br>INPUT/OUTPUT | RW              | BIT         |
| <b>7</b>            | <b>6</b>                       | DIGITAL<br>INPUT/OUTPUT | <b>7</b>            | DIGITAL<br>INPUT/OUTPUT | RW              | BIT         |
| <b>8</b>            | <b>7</b>                       | DIGITAL<br>INPUT/OUTPUT | <b>8</b>            | DIGITAL<br>INPUT/OUTPUT | RW              | BIT         |

## 25.3. R-4AO-8DIDO: TABLE OF MODBUS REGISTERS 1x INPUT STATUS (FUNCTION CODE 2)

| <b>ADDRESS (1x)</b> | <b>OFFSET ADDRESS (1x)</b> | <b>REGISTER</b>  | <b>CHANNEL</b> | <b>DESCRIPTION</b>      | <b>W/R</b> | <b>TYPE</b> |
|---------------------|----------------------------|------------------|----------------|-------------------------|------------|-------------|
| <b>10001</b>        | <b>0</b>                   | DIGITAL<br>INPUT | <b>1</b>       | DIGITAL<br>INPUT/OUTPUT | RO         | BIT         |
| <b>10002</b>        | <b>1</b>                   | DIGITAL<br>INPUT | <b>2</b>       | DIGITAL<br>INPUT/OUTPUT | RO         | BIT         |
| <b>10003</b>        | <b>2</b>                   | DIGITAL<br>INPUT | <b>3</b>       | DIGITAL<br>INPUT/OUTPUT | RO         | BIT         |
| <b>10004</b>        | <b>3</b>                   | DIGITAL<br>INPUT | <b>4</b>       | DIGITAL<br>INPUT/OUTPUT | RO         | BIT         |
| <b>10005</b>        | <b>4</b>                   | DIGITAL<br>INPUT | <b>5</b>       | DIGITAL<br>INPUT/OUTPUT | RO         | BIT         |
| <b>10006</b>        | <b>5</b>                   | DIGITAL<br>INPUT | <b>6</b>       | DIGITAL<br>INPUT/OUTPUT | RO         | BIT         |
| <b>10007</b>        | <b>6</b>                   | DIGITAL<br>INPUT | <b>7</b>       | DIGITAL<br>INPUT/OUTPUT | RO         | BIT         |

|       |   |                  |   |                         |    |     |
|-------|---|------------------|---|-------------------------|----|-----|
| 10008 | 7 | DIGITAL<br>INPUT | 8 | DIGITAL<br>INPUT/OUTPUT | RO | BIT |
|-------|---|------------------|---|-------------------------|----|-----|

## 26. MODBUS REGISTER TABLE FOR THE R-4RTD-8DIDO PRODUCT

### 26.1. R-4RTD-8DIDO: MODBUS 4X HOLDING REGISTERS TABLE (FUNCTION CODE 3)

| ADDRESS<br>(4x)  | OFFSET<br>(4x) | REGISTER                       | CHANNEL | DESCRIPTION                                                                                                                                                                              | W/R | TYPE               |
|------------------|----------------|--------------------------------|---------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----|--------------------|
| 40001            | 0              | MACHINE-ID                     | -       | Device identification                                                                                                                                                                    | RO  | UNSIGNED<br>16 BIT |
| 40002            | 1              | FW REVISION<br>(Maior/Minor)   | -       | Fw Revision                                                                                                                                                                              | RO  | UNSIGNED<br>16 BIT |
| 40003            | 2              | FW REVISION<br>(Fix/Build)     | -       | Fw Revision                                                                                                                                                                              | RO  | UNSIGNED<br>16 BIT |
| 40004            | 3              | FW CODE                        | -       | Fw Code                                                                                                                                                                                  | RO  | UNSIGNED<br>16 BIT |
| 40005            | 4              | BOARD-ID                       | -       | Hw Revision                                                                                                                                                                              | RO  | UNSIGNED<br>16 BIT |
| 40006            | 5              | MODBUS ID                      | -       | -                                                                                                                                                                                        | RO  | UNSIGNED<br>16 BIT |
| 40007            | 6              | BOOT REVISION<br>(Maior/Minor) | -       | Bootloader Revision                                                                                                                                                                      | RO  | UNSIGNED<br>16 BIT |
| 40008            | 7              | BOOT REVISION<br>(Fix/Build)   | -       | Bootloader Revision                                                                                                                                                                      | RO  | UNSIGNED<br>16 BIT |
| 40009            | 8              | BOOTLOADER<br>CODE             | -       | Bootloader Code                                                                                                                                                                          | RO  | UNSIGNED<br>16 BIT |
| 40010            | 9              | RESERVED                       | -       | -                                                                                                                                                                                        | RO  | UNSIGNED<br>16 BIT |
| 40011            | 10             | RESERVED                       | -       | -                                                                                                                                                                                        | RO  | UNSIGNED<br>16 BIT |
| 40012            | 11             | RESERVED                       | -       | -                                                                                                                                                                                        | RO  | UNSIGNED<br>16 BIT |
| 40013            | 12             | COMMAND                        | -       | Command Register<br><br>0xC1A0 = reset device<br>0x0100 = set factory<br>default                                                                                                         | RW  | UNSIGNED<br>16 BIT |
| 40014..<br>40029 | 13..39         | RESERVED                       | -       | -                                                                                                                                                                                        | RO  | UNSIGNED<br>16 BIT |
| 40030            | 29             | STATUS                         | -       | bit 0 : OVERRANGE RTD1<br>bit 1 : OVERRANGE RTD2<br>bit 2 : OVERRANGE RTD3<br>bit 3 : OVERRANGE RTD4<br>bit 4 : BURN RTD1<br>bit 5 : BURN RTD2<br>bit 6 : BURN RTD3<br>bit 7 : BURN RTD4 | RO  | UNSIGNED<br>16 BIT |



| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>(4x)</b> | <b>REGISTER</b>                 | <b>CHANNEL</b> | <b>DESCRIPTION</b>                                              | <b>W/R</b> | <b>TYPE</b>        |
|-------------------------|------------------------|---------------------------------|----------------|-----------------------------------------------------------------|------------|--------------------|
|                         |                        |                                 |                | bit 15 : FAIL                                                   |            |                    |
| <b>40031</b>            | 30                     | DIGITAL OUTPUT                  | 1..8           | bit 0 = OUTPUT 1<br>bit 1 = OUTPUT 2<br>....<br>bit 7= OUTPUT 8 | RW         | UNSIGNED<br>16 BIT |
| <b>40032</b>            | 31                     | DIGITAL INPUT                   | 1..8           | bit 0 = INPUT 1<br>bit 1 = INPUT 2<br>....<br>bit 7= INPUT 8    | RO         | UNSIGNED<br>16 BIT |
| <b>40033</b>            | 32                     | CHANNEL<br>TEMPERATURE<br>FLOAT | 1              | TEMPERATURE                                                     | MSW<br>RO  | FLOAT 32<br>BIT    |
| <b>40034</b>            | 33                     |                                 |                |                                                                 | LSW<br>RO  |                    |
| <b>40035</b>            | 34                     | CHANNEL<br>TEMPERATURE<br>FLOAT | 2              | TEMPERATURE                                                     | MSW<br>RO  | FLOAT 32<br>BIT    |
| <b>40036</b>            | 35                     |                                 |                |                                                                 | LSW<br>RO  |                    |
| <b>40037</b>            | 36                     | CHANNEL<br>TEMPERATURE<br>FLOAT | 3              | TEMPERATURE                                                     | MSW<br>RO  | FLOAT 32<br>BIT    |
| <b>40038</b>            | 37                     |                                 |                |                                                                 | LSW<br>RO  |                    |
| <b>40039</b>            | 38                     | CHANNEL<br>TEMPERATURE<br>FLOAT | 4              | TEMPERATURE                                                     | MSW<br>RO  | FLOAT 32<br>BIT    |
| <b>40040</b>            | 39                     |                                 |                |                                                                 | LSW<br>RO  |                    |
| <b>40041</b>            | 32                     | CHANNEL<br>RESITANCE<br>FLOAT   | 1              | RESISTANCE                                                      | MSW<br>RO  | FLOAT 32<br>BIT    |
| <b>40042</b>            | 33                     |                                 |                |                                                                 | LSW<br>RO  |                    |
| <b>40043</b>            | 34                     | CHANNEL<br>RESITANCE<br>FLOAT   | 2              | RESISTANCE                                                      | MSW<br>RO  | FLOAT 32<br>BIT    |
| <b>40044</b>            | 35                     |                                 |                |                                                                 | LSW<br>RO  |                    |
| <b>40045</b>            | 36                     | CHANNEL<br>RESITANCE<br>FLOAT   | 3              | RESISTANCE                                                      | MSW<br>RO  | FLOAT 32<br>BIT    |
| <b>40046</b>            | 37                     |                                 |                |                                                                 | LSW<br>RO  |                    |
| <b>40047</b>            | 38                     | CHANNEL<br>RESITANCE<br>FLOAT   | 4              | RESISTANCE                                                      | MSW<br>RO  | FLOAT 32<br>BIT    |
| <b>40048</b>            | 39                     |                                 |                |                                                                 | LSW<br>RO  |                    |
| <b>40049</b>            | 48                     | CHANNEL ENG.<br>VALUE FLOAT     | 1              | RESISTANCE                                                      | MSW<br>RO  | FLOAT 32<br>BIT    |
| <b>40050</b>            | 49                     |                                 |                |                                                                 | LSW<br>RO  |                    |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>(4x)</b> | <b>REGISTER</b>               | <b>CHANNEL</b> | <b>DESCRIPTION</b>           | <b>W/R</b> | <b>TYPE</b>        |
|-------------------------|------------------------|-------------------------------|----------------|------------------------------|------------|--------------------|
| <b>40051</b>            | 50                     | CHANNEL ENG.<br>VALUE FLOAT   | 2              | RESISTANCE                   | MSW<br>RO  | FLOAT 32<br>BIT    |
| <b>40052</b>            | 51                     |                               |                |                              | LSW<br>RO  |                    |
| <b>40053</b>            | 52                     | CHANNEL ENG.<br>VALUE FLOAT   | 3              | RESISTANCE                   | MSW<br>RO  | FLOAT 32<br>BIT    |
| <b>40054</b>            | 53                     |                               |                |                              | LSW<br>RO  |                    |
| <b>40055</b>            | 54                     | CHANNEL ENG.<br>VALUE FLOAT   | 4              | RESISTANCE                   | MSW<br>RO  | FLOAT 32<br>BIT    |
| <b>40056</b>            | 55                     |                               |                |                              | LSW<br>RO  |                    |
| <b>40057</b>            | 56                     | CHANNEL<br>TEMPERATURE<br>INT | 1              | TEMPERATURE                  | RO         | SIGNED 16<br>BIT   |
| <b>40058</b>            | 57                     | CHANNEL<br>TEMPERATURE<br>INT | 2              | TEMPERATURE                  | RO         | SIGNED 16<br>BIT   |
| <b>40059</b>            | 58                     | CHANNEL<br>TEMPERATURE<br>INT | 3              | TEMPERATURE                  | RO         | SIGNED 16<br>BIT   |
| <b>40060</b>            | 59                     | CHANNEL<br>TEMPERATURE<br>INT | 4              | TEMPERATURE                  | RO         | SIGNED 16<br>BIT   |
| <b>40061</b>            | 60                     | ADC RAW                       | 1              | ADC RAW VALUE                | RO         | UNSIGNED<br>16 BIT |
| <b>40062</b>            | 61                     | ADC RAW                       | 2              | ADC RAW VALUE                | RO         | UNSIGNED<br>16 BIT |
| <b>40063</b>            | 62                     | ADC RAW                       | 3              | ADC RAW VALUE                | RO         | UNSIGNED<br>16 BIT |
| <b>40064</b>            | 63                     | ADC RAW                       | 4              | ADC RAW VALUE                | RO         | UNSIGNED<br>16 BIT |
| <b>41847</b>            | 1846                   | COUNTER                       | 1              | DIGITAL INPUT COUNTER<br>MSW | RW         | UNSIGNED<br>32 BIT |
| <b>41848</b>            | 1847                   |                               |                | DIGITAL INPUT COUNTER<br>LSW | RW         |                    |
| <b>41849</b>            | 1848                   | COUNTER                       | 2              | DIGITAL INPUT COUNTER<br>MSW | RW         | UNSIGNED<br>32 BIT |
| <b>41850</b>            | 1849                   |                               |                | DIGITAL INPUT COUNTER<br>LSW | RW         |                    |
| <b>41851</b>            | 1850                   | COUNTER                       | 3              | DIGITAL INPUT COUNTER<br>MSW | RW         | UNSIGNED<br>32 BIT |
| <b>41852</b>            | 1851                   |                               |                | DIGITAL INPUT COUNTER<br>LSW | RW         |                    |
| <b>41853</b>            | 1852                   | COUNTER                       | 4              | DIGITAL INPUT COUNTER<br>MSW | RW         | UNSIGNED<br>32 BIT |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>(4x)</b> | <b>REGISTER</b> | <b>CHANNEL</b> | <b>DESCRIPTION</b>           | <b>W/R</b> | <b>TYPE</b>        |
|-------------------------|------------------------|-----------------|----------------|------------------------------|------------|--------------------|
| <b>41854</b>            | 1853                   |                 |                | DIGITAL INPUT COUNTER<br>LSW | RW         |                    |
| <b>41855</b>            | 1854                   | COUNTER         | 5              | DIGITAL INPUT COUNTER<br>MSW | RW         | UNSIGNED<br>32 BIT |
| <b>41856</b>            | 1855                   |                 |                | DIGITAL INPUT COUNTER<br>LSW | RW         |                    |
| <b>41857</b>            | 1856                   | COUNTER         | 6              | DIGITAL INPUT COUNTER<br>MSW | RW         | UNSIGNED<br>32 BIT |
| <b>41858</b>            | 1857                   |                 |                | DIGITAL INPUT COUNTER<br>LSW | RW         |                    |
| <b>41859</b>            | 1858                   | COUNTER         | 7              | DIGITAL INPUT COUNTER<br>MSW | RW         | UNSIGNED<br>32 BIT |
| <b>41860</b>            | 1859                   |                 |                | DIGITAL INPUT COUNTER<br>LSW | RW         |                    |
| <b>41861</b>            | 1860                   | COUNTER         | 8              | DIGITAL INPUT COUNTER<br>MSW | RW         | UNSIGNED<br>32 BIT |
| <b>41862</b>            | 1861                   |                 |                | DIGITAL INPUT COUNTER<br>LSW | RW         |                    |

## 26.2. R-4RTD-8DIDO: TABLE OF MODBUS REGISTERS 0x COIL STATUS (FUNCTION CODE 1)

| <b>ADDRESS (0x)</b> | <b>OFFSET ADDRESS (0x)</b> | <b>REGISTER</b>             | <b>CHANNE<br/>L</b> | <b>DESCRIPTION</b>      | <b>W/<br/>R</b> | <b>TYP<br/>E</b> |
|---------------------|----------------------------|-----------------------------|---------------------|-------------------------|-----------------|------------------|
| <b>1</b>            | 0                          | DIGITAL<br>INPUT/OUTPU<br>T | 1                   | DIGITAL<br>INPUT/OUTPUT | RW              | BIT              |
| <b>2</b>            | 1                          | DIGITAL<br>INPUT/OUTPU<br>T | 2                   | DIGITAL<br>INPUT/OUTPUT | RW              | BIT              |
| <b>3</b>            | 2                          | DIGITAL<br>INPUT/OUTPU<br>T | 3                   | DIGITAL<br>INPUT/OUTPUT | RW              | BIT              |
| <b>4</b>            | 3                          | DIGITAL<br>INPUT/OUTPU<br>T | 4                   | DIGITAL<br>INPUT/OUTPUT | RW              | BIT              |
| <b>5</b>            | 4                          | DIGITAL<br>INPUT/OUTPU<br>T | 5                   | DIGITAL<br>INPUT/OUTPUT | RW              | BIT              |
| <b>6</b>            | 5                          | DIGITAL<br>INPUT/OUTPU<br>T | 6                   | DIGITAL<br>INPUT/OUTPUT | RW              | BIT              |
| <b>7</b>            | 6                          | DIGITAL<br>INPUT/OUTPU<br>T | 7                   | DIGITAL<br>INPUT/OUTPUT | RW              | BIT              |

|   |   |                         |   |                         |    |     |
|---|---|-------------------------|---|-------------------------|----|-----|
| 8 | 7 | DIGITAL<br>INPUT/OUTPUT | 8 | DIGITAL<br>INPUT/OUTPUT | RW | BIT |
|---|---|-------------------------|---|-------------------------|----|-----|

### 26.3. R-4RTD-8DIDO: TABLE OF MODBUS REGISTERS 1x INPUT STATUS (FUNCTION CODE 2)

| ADDRESS (1x) | OFFSET ADDRESS (1x) | REGISTER         | CHANNEL | DESCRIPTION             | W/R | TYPE |
|--------------|---------------------|------------------|---------|-------------------------|-----|------|
| 10001        | 0                   | DIGITAL<br>INPUT | 1       | DIGITAL<br>INPUT/OUTPUT | RO  | BIT  |
| 10002        | 1                   | DIGITAL<br>INPUT | 2       | DIGITAL<br>INPUT/OUTPUT | RO  | BIT  |
| 10003        | 2                   | DIGITAL<br>INPUT | 3       | DIGITAL<br>INPUT/OUTPUT | RO  | BIT  |
| 10004        | 3                   | DIGITAL<br>INPUT | 4       | DIGITAL<br>INPUT/OUTPUT | RO  | BIT  |
| 10005        | 4                   | DIGITAL<br>INPUT | 5       | DIGITAL<br>INPUT/OUTPUT | RO  | BIT  |
| 10006        | 5                   | DIGITAL<br>INPUT | 6       | DIGITAL<br>INPUT/OUTPUT | RO  | BIT  |
| 10007        | 6                   | DIGITAL<br>INPUT | 7       | DIGITAL<br>INPUT/OUTPUT | RO  | BIT  |
| 10008        | 7                   | DIGITAL<br>INPUT | 8       | DIGITAL<br>INPUT/OUTPUT | RO  | BIT  |

## 27. R-SG3: LOAD CELL CALIBRATION THROUGH MODBUS REGISTERS

It is not always possible to use the Web server to calibrate the load cell, for example if a PLC or an HMI is to be used.

It is also possible to implement the calibration of a load cell in a simple way by sending Modbus RTU / TCP-IP commands according to the type of operation chosen.

### 27.1. CELL CALIBRATION PROCEDURE WITH FACTORY PARAMETERS AND WITH TARE ACQUIRED FROM THE FIELD

- 1) Place the tare on the load cell
- 2) Wait for the measurement to stabilize
- 3) Write the decimal value 49914 in the COMMAND REGISTER
- 4) The device saves the new tare value in flash and resets the COMMAND REGISTER value
- 5) The load cell is calibrated

## 27.2. CELL CALIBRATION PROCEDURE WITH FACTORY PARAMETERS AND MANUALLY ENTERED TARE

- 1) Write the tare value in technical units in the FACTORY MANUAL TARE registers
- 2) Write the decimal value 50773 in the COMMAND REGISTER
- 3) The device acquires the new flash tare value and resets the COMMAND REGISTER value
- 4) The load cell is calibrated

## 27.3. PROCEDURE FOR CALIBRATION OF THE CELL WITH A STANDARD WEIGHT

- 1) Place the tare on the load cell
- 2) Wait for the measurement to stabilize
- 3) Write the decimal value 49914 in the COMMAND REGISTER
- 4) The device saves the new tare value in flash and resets the COMMAND REGISTER value
- 5) Enter the weight value of the standard weight in technical units in the STANDARD WEIGHT VALUE registers
- 6) Place the standard weight on the load cell
- 7) Wait for the measurement to stabilize
- 8) Write the decimal value 50700 in the COMMAND REGISTER
- 9) The device saves the new standard weight value in flash and resets the COMMAND REGISTER value
- 10) The load cell is calibrated

## 28. MODBUS REGISTER TABLE FOR THE R-2AI-6DIDO PRODUCT

### 28.1. R-2AI-6DIDO: MODBUS 4X HOLDING REGISTERS TABLE (FUNCTION CODE 3)

| ADDRESS<br>(4x) | OFFSET<br>(4x) | REGISTER                     | CHANNEL | DESCRIPTION                                                                                               | W/R | TYPE               |
|-----------------|----------------|------------------------------|---------|-----------------------------------------------------------------------------------------------------------|-----|--------------------|
| 40001           | 0              | MACHINE-ID                   | -       | Device identification                                                                                     | RO  | UNSIGNED<br>16 BIT |
| 40002           | 1              | FW REVISION<br>(Maior/Minor) | -       | Fw Revision                                                                                               | RO  | UNSIGNED<br>16 BIT |
| 40003           | 2              | FW REVISION<br>(Fix/Build)   | -       | Fw Revision                                                                                               | RO  | UNSIGNED<br>16 BIT |
| 40004-<br>40018 | 3...17         | Reserved                     | -       |                                                                                                           |     | UNSIGNED<br>16 BIT |
| 40019           | 18             | COMMAND                      | -       | 256 decimal = set factory<br>default<br>49568 decimal = reboot<br>device                                  | RW  | UNSIGNED<br>16 BIT |
| 40020           | 19             | STATUS                       | -       | bit 0 : Digital I/O Fault<br>bit 1 : Digital VBUS present<br>bit 2 : Ain1 Overage<br>bit 3 : Ain2 Overage | RO  | UNSIGNED<br>16 BIT |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>(4x)</b> | <b>REGISTER</b>     | <b>CHANNEL</b> | <b>DESCRIPTION</b>               | <b>W/R</b> | <b>TYPE</b>                 |
|-------------------------|------------------------|---------------------|----------------|----------------------------------|------------|-----------------------------|
| <b>40021-40026</b>      | 20..25                 | Reserved            |                |                                  |            | UNSIGNED<br>16 BIT          |
| <b>40027</b>            | 26                     | Digital I/O state : |                | bit 0 = I/O 1,....bit 5= I/O 6   | RW         | UNSIGNED<br>16 BIT          |
| <b>40028</b>            | 27                     | Digital Input state |                | : bit 0 = I/O 1,....bit 5= I/O 6 | RO         | UNSIGNED<br>16 BIT          |
| <b>40029-40030</b>      | 28-29                  | COUNTER             | 1              | DIGITAL INPUT<br>COUNTER         | RW         | UNSIGNED<br>32 BIT          |
| <b>40031-40032</b>      | 30-31                  | COUNTER             | 2              | DIGITAL INPUT<br>COUNTER         | RW         | UNSIGNED<br>32 BIT          |
| <b>40033-40034</b>      | 32-33                  | COUNTER             | 3              | DIGITAL INPUT<br>COUNTER         | RW         | UNSIGNED<br>32 BIT          |
| <b>40035-40036</b>      | 34-35                  | COUNTER             | 4              | DIGITAL INPUT<br>COUNTER         | RW         | UNSIGNED<br>32 BIT          |
| <b>40037-40038</b>      | 36-37                  | COUNTER             | 5              | DIGITAL INPUT<br>COUNTER         | RW         | UNSIGNED<br>32 BIT          |
| <b>40039-40040</b>      | 38-39                  | COUNTER             | 6              | DIGITAL INPUT<br>COUNTER         | RW         | UNSIGNED<br>32 BIT          |
| <b>40041-40042</b>      | 40-41                  | PERIOD              | 1              | PERIOD [s] DIGITAL<br>INPUT      | R          | FLOATING<br>POINT 32<br>BIT |
| <b>40043-40044</b>      | 42-43                  | PERIOD              | 2              | PERIOD [s] DIGITAL<br>INPUT      | R          | FLOATING<br>POINT 32<br>BIT |
| <b>40045-40046</b>      | 44-45                  | PERIOD              | 3              | PERIOD [s] DIGITAL<br>INPUT      | R          | FLOATING<br>POINT 32<br>BIT |
| <b>40047-40048</b>      | 46-47                  | PERIOD              | 4              | PERIOD [s] DIGITAL<br>INPUT      | R          | FLOATING<br>POINT 32<br>BIT |
| <b>40049-40050</b>      | 48-49                  | PERIOD              | 5              | PERIOD [s] DIGITAL<br>INPUT      | R          | FLOATING<br>POINT 32<br>BIT |
| <b>40051-40052</b>      | 50-51                  | PERIOD              | 6              | PERIOD [s] DIGITAL<br>INPUT      | R          | FLOATING<br>POINT 32<br>BIT |
| <b>40053-40054</b>      | 52-53                  | FREQUENCY           | 1              | FREQUENCY [Hz]<br>DIGITAL INPUT  | R          | FLOATING<br>POINT 32<br>BIT |
| <b>40055-40056</b>      | 54-55                  | FREQUENCY           | 2              | FREQUENCY [Hz]<br>DIGITAL INPUT  | R          | FLOATING<br>POINT 32<br>BIT |

| <b>ADDRESS<br/>(4x)</b> | <b>OFFSET<br/>(4x)</b> | <b>REGISTER</b>                      | <b>CHANNEL</b> | <b>DESCRIPTION</b>                            | <b>W/R</b> | <b>TYPE</b>                 |
|-------------------------|------------------------|--------------------------------------|----------------|-----------------------------------------------|------------|-----------------------------|
| <b>40057-<br/>40058</b> | 56-57                  | FREQUENCY                            | 3              | FREQUENCY [Hz]<br>DIGITAL INPUT               | R          | FLOATING<br>POINT 32<br>BIT |
| <b>40059-<br/>40060</b> | 58-59                  | FREQUENCY                            | 4              | FREQUENCY [Hz]<br>DIGITAL INPUT               | R          | FLOATING<br>POINT 32<br>BIT |
| <b>40061-<br/>40062</b> | 60-61                  | FREQUENCY                            | 5              | FREQUENCY [Hz]<br>DIGITAL INPUT               | R          | FLOATING<br>POINT 32<br>BIT |
| <b>40063-<br/>40064</b> | 62-63                  | FREQUENCY                            | 6              | FREQUENCY [Hz]<br>DIGITAL INPUT               | R          | FLOATING<br>POINT 32<br>BIT |
| <b>40065</b>            | 64                     | ADC RAW                              | 1              | ADC 16 BIT RAW VALUE                          | R          | UNSIGNED<br>16 BIT          |
| <b>40066</b>            | 65                     | ADC RAW                              | 2              | ADC 16 BIT RAW VALUE                          | R          | UNSIGNED<br>16 BIT          |
| <b>40067-<br/>40068</b> | 66-67                  | Analog Input                         | 1              | ANALOG INPUT VALUE                            | R          | FLOATING<br>POINT 32<br>BIT |
| <b>40069-<br/>40070</b> | 68-69                  | Analog Input                         | 2              | ANALOG INPUT VALUE                            | R          | FLOATING<br>POINT 32<br>BIT |
| <b>40071-<br/>40072</b> | 70-71                  | Analog Input<br>Engineering<br>Value | 1              | ANALOG INPUT<br>ENGINEERING (SCALED)<br>VALUE | R          | FLOATING<br>POINT 32<br>BIT |
| <b>40073-<br/>40074</b> | 72-73                  | Analog Input<br>Engineering<br>Value | 2              | ANALOG INPUT<br>ENGINEERING (SCALED)<br>VALUE | R          | FLOATING<br>POINT 32<br>BIT |

## 28.2. R-2AI-6DIDO: TABLE OF MODBUS REGISTERS 0x COIL STATUS (FUNCTION CODE 1)

| <b>ADDRESS (0x)</b> | <b>OFFSET ADDRESS (0x)</b> | <b>REGISTER</b>             | <b>CHANNE<br/>L</b> | <b>DESCRIPTION</b>      | <b>W/<br/>R</b> | <b>TYP<br/>E</b> |
|---------------------|----------------------------|-----------------------------|---------------------|-------------------------|-----------------|------------------|
| <b>1</b>            | 0                          | DIGITAL<br>INPUT/OUTPU<br>T | 1                   | DIGITAL<br>INPUT/OUTPUT | RW              | BIT              |
| <b>2</b>            | 1                          | DIGITAL<br>INPUT/OUTPU<br>T | 2                   | DIGITAL<br>INPUT/OUTPUT | RW              | BIT              |
| <b>3</b>            | 2                          | DIGITAL<br>INPUT/OUTPU<br>T | 3                   | DIGITAL<br>INPUT/OUTPUT | RW              | BIT              |

|   |   |                         |   |                         |    |     |
|---|---|-------------------------|---|-------------------------|----|-----|
| 4 | 3 | DIGITAL<br>INPUT/OUTPUT | 4 | DIGITAL<br>INPUT/OUTPUT | RW | BIT |
| 5 | 4 | DIGITAL<br>INPUT/OUTPUT | 5 | DIGITAL<br>INPUT/OUTPUT | RW | BIT |
| 6 | 5 | DIGITAL<br>INPUT/OUTPUT | 6 | DIGITAL<br>INPUT/OUTPUT | RW | BIT |

### 28.3. R-2AI-6DIDO: TABLE OF MODBUS REGISTERS 1x INPUT STATUS (FUNCTION CODE 2)

| ADDRESS (1x) | OFFSET ADDRESS (1x) | REGISTER         | CHANNEL | DESCRIPTION             | W/R | TYPE |
|--------------|---------------------|------------------|---------|-------------------------|-----|------|
| 10001        | 0                   | DIGITAL<br>INPUT | 1       | DIGITAL<br>INPUT/OUTPUT | RO  | BIT  |
| 10002        | 1                   | DIGITAL<br>INPUT | 2       | DIGITAL<br>INPUT/OUTPUT | RO  | BIT  |
| 10003        | 2                   | DIGITAL<br>INPUT | 3       | DIGITAL<br>INPUT/OUTPUT | RO  | BIT  |
| 10004        | 3                   | DIGITAL<br>INPUT | 4       | DIGITAL<br>INPUT/OUTPUT | RO  | BIT  |
| 10005        | 4                   | DIGITAL<br>INPUT | 5       | DIGITAL<br>INPUT/OUTPUT | RO  | BIT  |
| 10006        | 5                   | DIGITAL<br>INPUT | 6       | DIGITAL<br>INPUT/OUTPUT | RO  | BIT  |

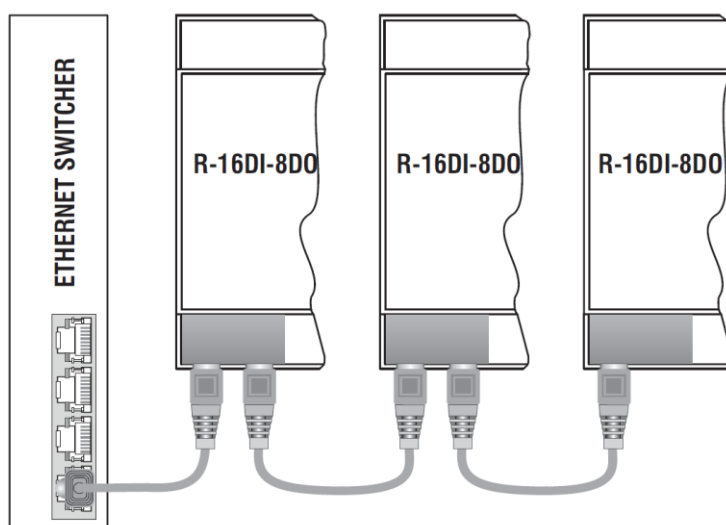
## 29. CABLE HARNESS FOR MODELS WITH DOUBLE ETHERNET PORT

Models with double Ethernet port can be connected in daisy chain and take advantage of the Lan Fault Bypass.

### 29.1. CHAIN ETHERNET CONNECTION (DAISY CHAIN)

Using the daisy chain connection it is not necessary to use switches to connect the devices.  
An example (in this case on R-16DI-8DO) of connection of 3 devices is as follows:

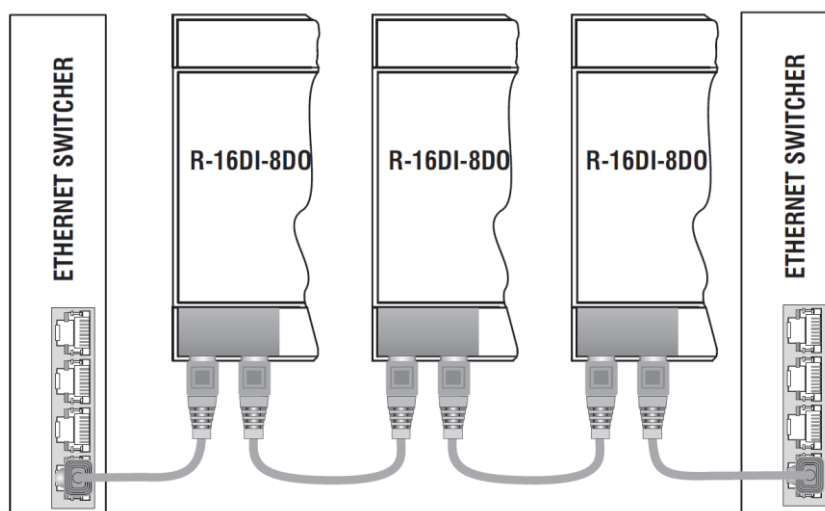




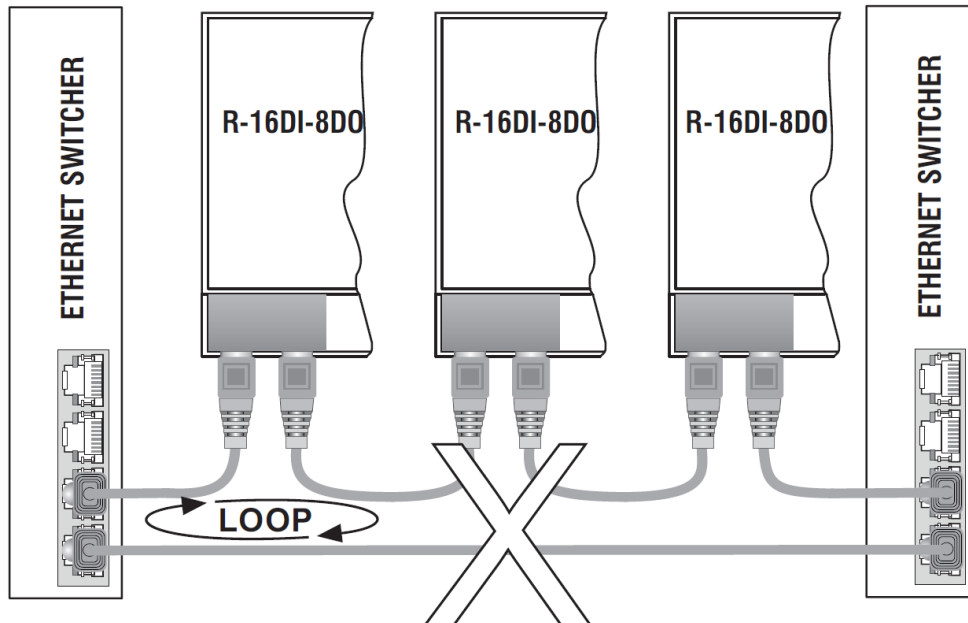
## **! ATTENTION!**

**IT IS NOT POSSIBLE CREATE LOOPS WITH ETHERNET CABLES**

If it is necessary to connect the devices to the switches, correct wiring is as follows:



In the Ethernet wiring there must be no loop, otherwise the communication will not work, some examples of incorrect wiring are the following:



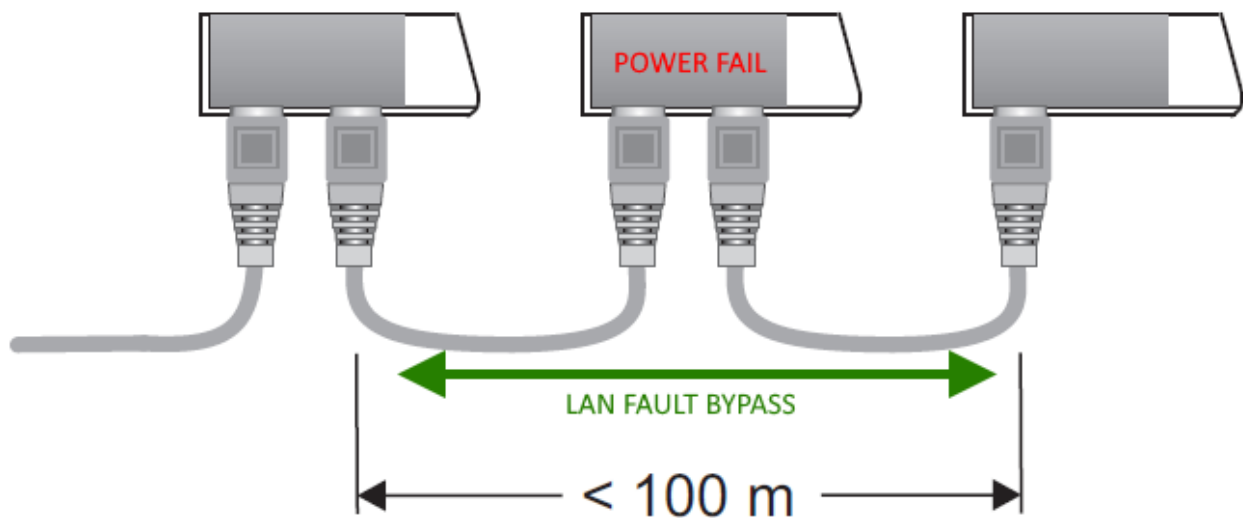
## 29.2. LAN FAULT-BYPASS FUNCTION

The LAN fault-bypass function allows you to keep the connection between the two Ethernet ports of the device ON, in the event of power failure problems.

If a device turns off, the chain is not interrupted and the devices downstream of the switched-off one will still be accessible.

This function has a limited duration: the connection remains active for a few days, typically 4.

The Lan fault-bypass function requires that the sum of the lengths of the two cables connected to the switched off module is less than 100m.



### 30. SEARCH AND MODIFICATION OF THE DEVICE IP WITH THE SENECA DISCOVERY DEVICE TOOL

The search and modification of an IP of the device can be performed via the Seneca Discovery Device software.

If Seneca devices that are not part of the R series are also used, it is more convenient to set the addresses with a single software.

When in the R series device the STS LED is on steady, it is possible to obtain the IP address which has been set using the "Seneca Discovery Device" tool too.

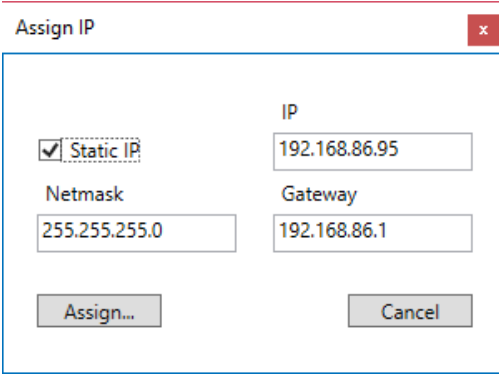
The software can be downloaded from:

<https://www.seneca.it/en/linee-di-prodotto/software/easy/sdd>

Pressing the "search" button starts the search for all Seneca devices present in the network even if with IP addresses not compatible with the current PC configuration:



It is now possible to change the address by pressing the "Assign" button:



The image shows a software dialog box titled "Assign IP" with a red close button in the top right corner. Inside the dialog, there are four input fields arranged in two columns. The left column contains a checked checkbox labeled "Static IP", a "Netmask" field with the value "255.255.255.0", and an "Assign..." button at the bottom. The right column contains an "IP" field with the value "192.168.86.95", a "Gateway" field with the value "192.168.86.1", and a "Cancel" button at the bottom.

| Field     | Value                               |
|-----------|-------------------------------------|
| Static IP | <input checked="" type="checkbox"/> |
| Netmask   | 255.255.255.0                       |
| IP        | 192.168.86.95                       |
| Gateway   | 192.168.86.1                        |

The software works on layer 2 level and it is therefore not necessary to have an Ethernet configuration compatible with the device you are looking for.